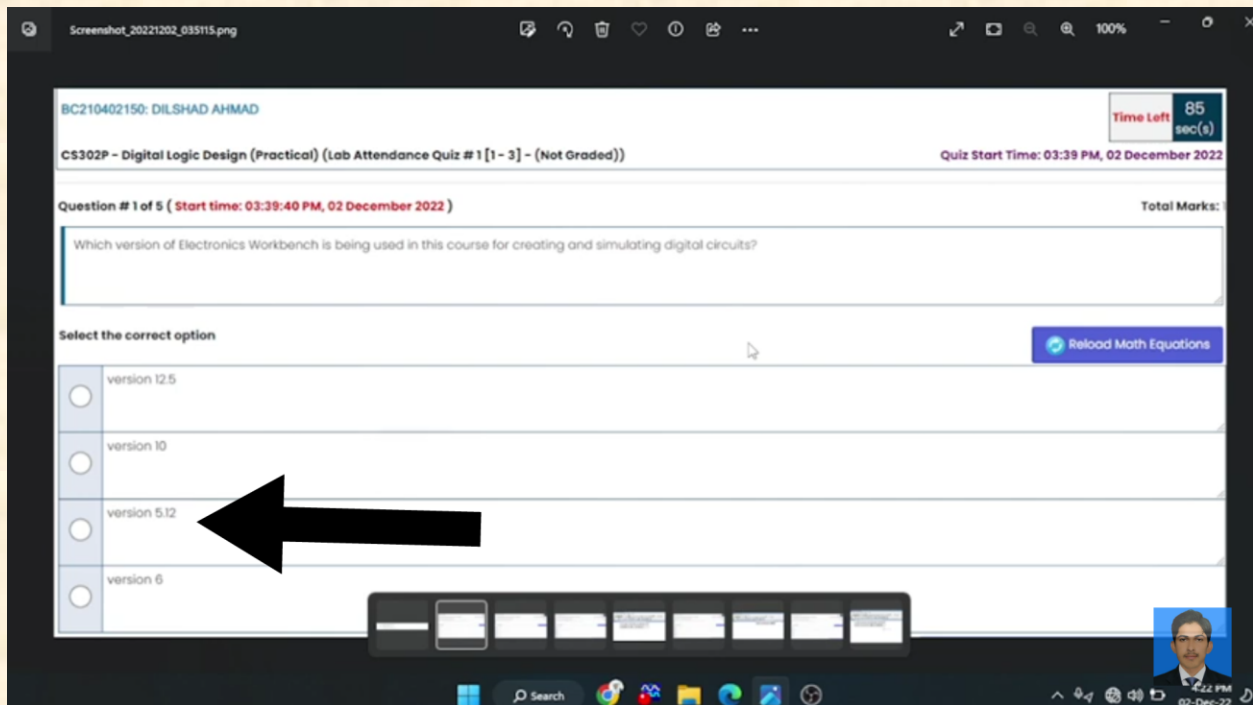


CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI



BC210402150: DILSHAD AHMAD Time Left 85 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1 - 3] - (Not Graded)) Quiz Start Time: 03:39 PM, 02 December 2022

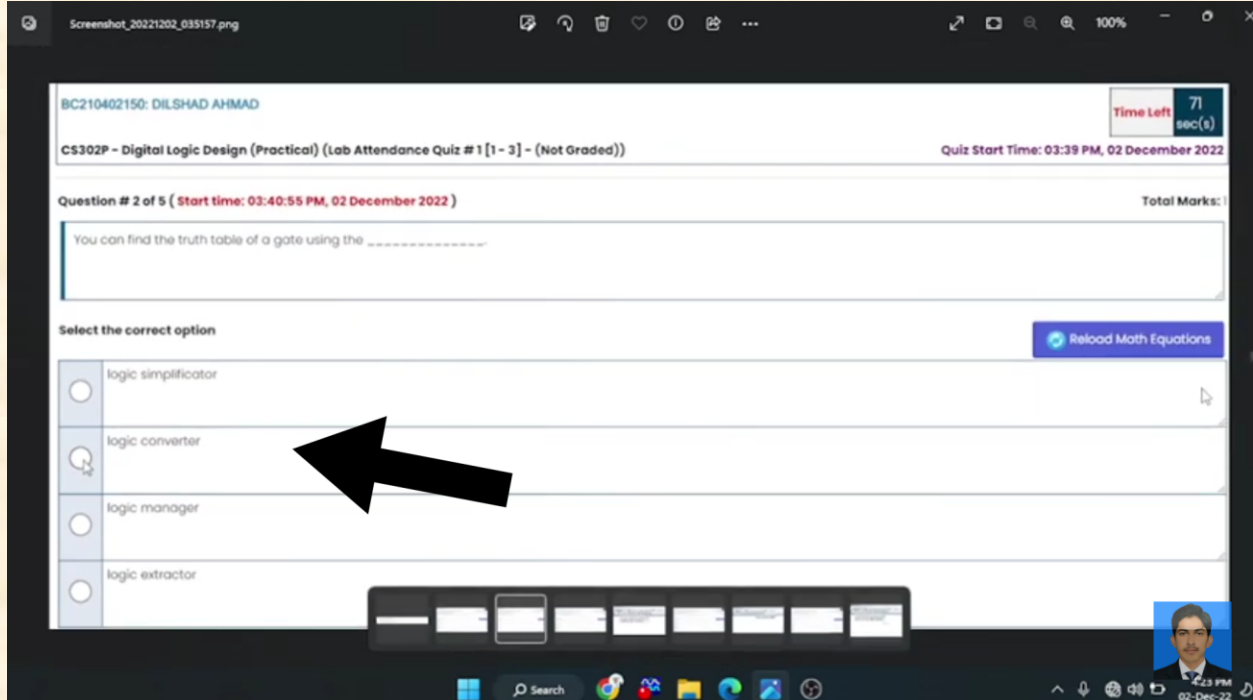
Question # 1 of 5 (Start time: 03:39:40 PM, 02 December 2022) Total Marks:

Which version of Electronics Workbench is being used in this course for creating and simulating digital circuits?

Select the correct option Reload Math Equations

- version 12.5
- version 10
- version 5.12
- version 6

The screenshot shows a quiz interface with a question about the version of Electronics Workbench used in the course. The correct answer, 'version 5.12', is indicated by a black arrow pointing to the selected radio button. The interface includes a user profile, course name, question number, and a 'Reload Math Equations' button.



BC210402150: DILSHAD AHMAD Time Left 71 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1 - 3] - (Not Graded)) Quiz Start Time: 03:39 PM, 02 December 2022

Question # 2 of 5 (Start time: 03:40:55 PM, 02 December 2022) Total Marks:

You can find the truth table of a gate using the _____

Select the correct option Reload Math Equations

- logic simplificator
- logic converter
- logic manager
- logic extractor

The screenshot shows a quiz interface with a question about finding truth tables for logic gates. The correct answer, 'logic converter', is indicated by a black arrow pointing to the selected radio button. The interface includes a user profile, course name, question number, and a 'Reload Math Equations' button.

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Screenshot_20221202_040154.png

BC210402150: DILSHAD AHMAD

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1 - 3] - (Not Graded))

Time Left 56 sec(s)

Quiz Start Time: 03:39 PM, 02 December 2022

Question # 3 of 5 (Start time: 03:42:41 PM, 02 December 2022)

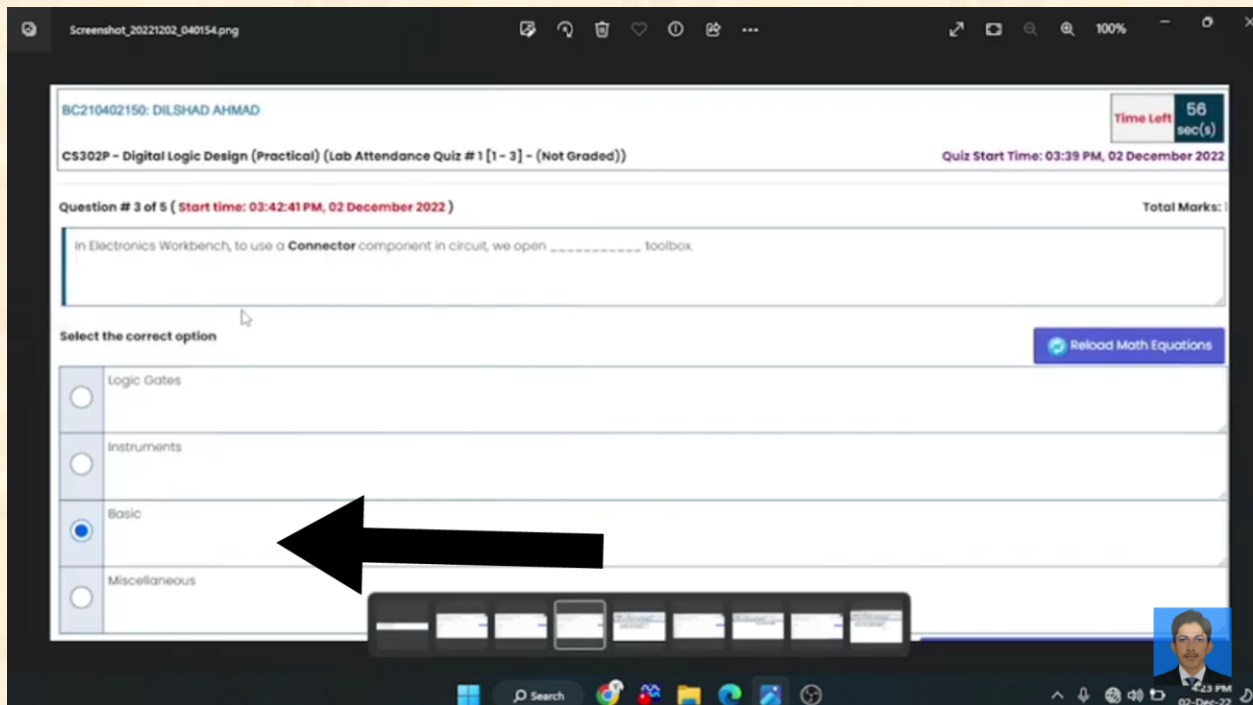
Total Marks: 1

In Electronics Workbench, to use a Connector component in circuit, we open _____ toolbox.

Select the correct option

- Logic Gates
- Instruments
- Basic
- Miscellaneous

Reload Math Equations



Screenshot_20221202_040638.png

BC210402150: DILSHAD AHMAD

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1 - 3] - (Not Graded))

Time Left 82 sec(s)

Quiz Start Time: 03:39 PM, 02 December 2022

Question # 4 of 5 (Start time: 03:46:33 PM, 02 December 2022)

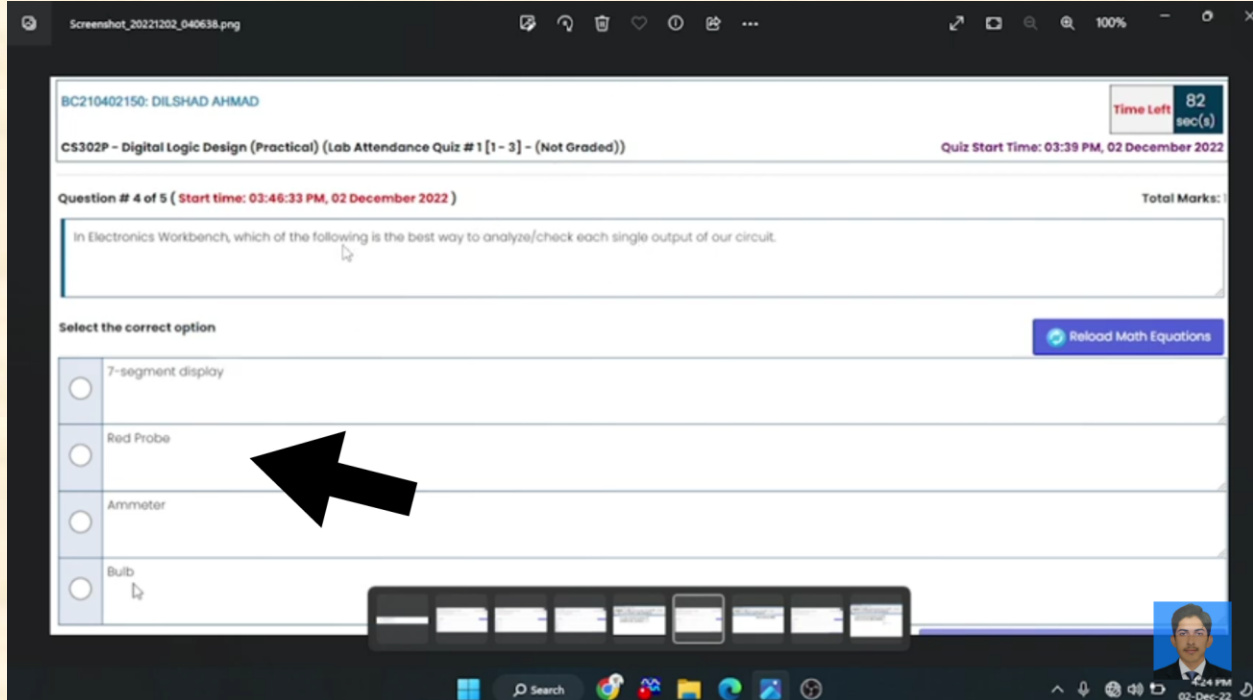
Total Marks: 1

In Electronics Workbench, which of the following is the best way to analyze/check each single output of our circuit.

Select the correct option

- 7-segment display
- Red Probe
- Ammeter
- Bulb

Reload Math Equations



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BC210402150: DILSHAD AHMAD

Time Left: 83 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 1 [1 - 3] - (Not Graded))

Quiz Start Time: 03:39 PM, 02 December 2022

Question # 5 of 5 (Start time: 03:47:02 PM, 02 December 2022) Total Marks: 1

In Electronics Workbench, the default value for the frequency of the clock source is _____

Select the correct option

- 5 Hz
- 2 Hz
- 50 Hz
- 2 KHz

Reload Math Equations

Google

_____ gate will give high output (1) if any input is high (1)

About 64,700,000 results (0.59 seconds)

In other words for a **logic OR gate**, any "HIGH" input will give a "HIGH" output. The logic or Boolean expression given for a digital logic OR gate is $A + B$ giving us the Boolean expression $A + B$.

https://www.electronics-tutorials.ws/logic/logic_3.html

Logic OR Gate Tutorial

People also ask

- Which gate will give high output 1 if any input is high?
- Which gate gives output 1 when all of its input is 1?
- Which gate will give high output if any output is high?

Question # 1 of 5 (Start time: 11:40:14 AM, 02 December 2022) Total Marks: 1

_____ gate will give high output (1) if any input is high (1).

Select the correct option

- NOR
- AND
- NOT
- OR

Reload Math Equations

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The image displays two screenshots of a quiz interface. The top screenshot shows a search for "For inverting an input, we use _____ gate" with results from Khan Academy and AllAboutCircuits.com. The quiz question asks for the correct option, with "NOT Gate" selected and indicated by a black arrow. The bottom screenshot shows a search for "In Electronics Workbench, the default value" with results from physics.udel.edu and wps.prenhall.com. The quiz question asks for the default value for the duty cycle of the clock source, with "50%" selected and indicated by a black arrow. Both screenshots include a Windows taskbar at the bottom and a watermark for "clideo.com" and "www.BANDICAM.com".

Top Screenshot:

Search: For inverting an input, we use _____ gate

Results:

- NOT gate. The simplest gate is the NOT gate, also known as an input and outputs the opposite value.
- Logic gates | AP CSP (article) - Khan Academy
- Multiple-input Gates | Logic Gates | Electronics Text

Quiz Question # 4 of 5 (Start time: 11:42:55 AM, 02 December 2022) Total Marks: 1

For inverting an input, we use _____ gate

Select the correct option

- OR Gate
- NOT Gate
- AND Gate
- XOR Gate

Bottom Screenshot:

Search: In Electronics Workbench, the default value

Results:

- Electronic WorkBench tutorial
- chert_floyd_principles_7 | Pulse Response of Reactiv
- EWB practical workbook - SlideShare

Quiz Question # 5 of 5 (Start time: 11:43:16 AM, 02 December 2022) Total Marks: 1

In Electronics Workbench, the default value for the duty cycle of the clock source is _____

Select the correct option

- 100%
- 5%
- 2%
- 50%

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Google - The ____ gate works opp... - www.BANDICAM.com

Google

The ____ gate works opposite to the OR g

About 53,600 results (0.46 seconds)

Including results for The ____ gate works opposite to the algebra representation is: $C=(A+B)$
Search only for The ____ gate works opposite to the OR gate. Its Boolean algebra representation is: $C=(A+B)$

https://www.electronics-tutorials.ws/logic/logic_5 :
Logic NAND Gate Tutorial
The logic or Boolean expression given for a logic NAND gate is that for L is the opposite to the AND gate, and which it performs on ...
Missing: ____ | Must include: ____

https://www.electronics-tutorials.ws/logic/logic_6 :
Logic NOR Gate Tutorial
The logic or Boolean expression given for a logic NOR gate is that for L which it performs on the complements of the inputs. Its ...

https://www.techtarget.com/whatis/definition/logic-g... :
logic gate (AND, OR, XOR, NOT, NAND, NOR and
Logic gates are based on **Boolean algebra**. At any given moment, every the two binary conditions, false or true. False represents 0, and ...

BC220206916: FAKIA BIBI

Time Left 81 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1-3] - (Not Graded))

Quiz Start Time: 11:40 AM, 02 December 2022

Question # 2 of 5 (Start time: 11:40:42 AM, 02 December 2022) Total Marks: 1

The ____ gate works opposite to the OR gate. Its Boolean algebra representation is: $C=(A+B)$

Select the correct option

Reload Math Equations

NAND

XOR

NOR

NOT

knowledge-hub-...mp4

clideo.co

REKHA KNOWLEDGE HUB

Question # 5 of 5 (Start time: 09:51:18 PM, 02 December 2022)

Which command we can use to "Clicking to Select" multiple items in simulation.

Select the correct option

CTRL+ click

ALT+ click

Shift+ click

Tab+ click

Type here to search

16°C Smoke

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Quiz start Time: 09:47 PM

Question # 4 of 5 (Start time: 09:50:22 PM, 02 December 2022)

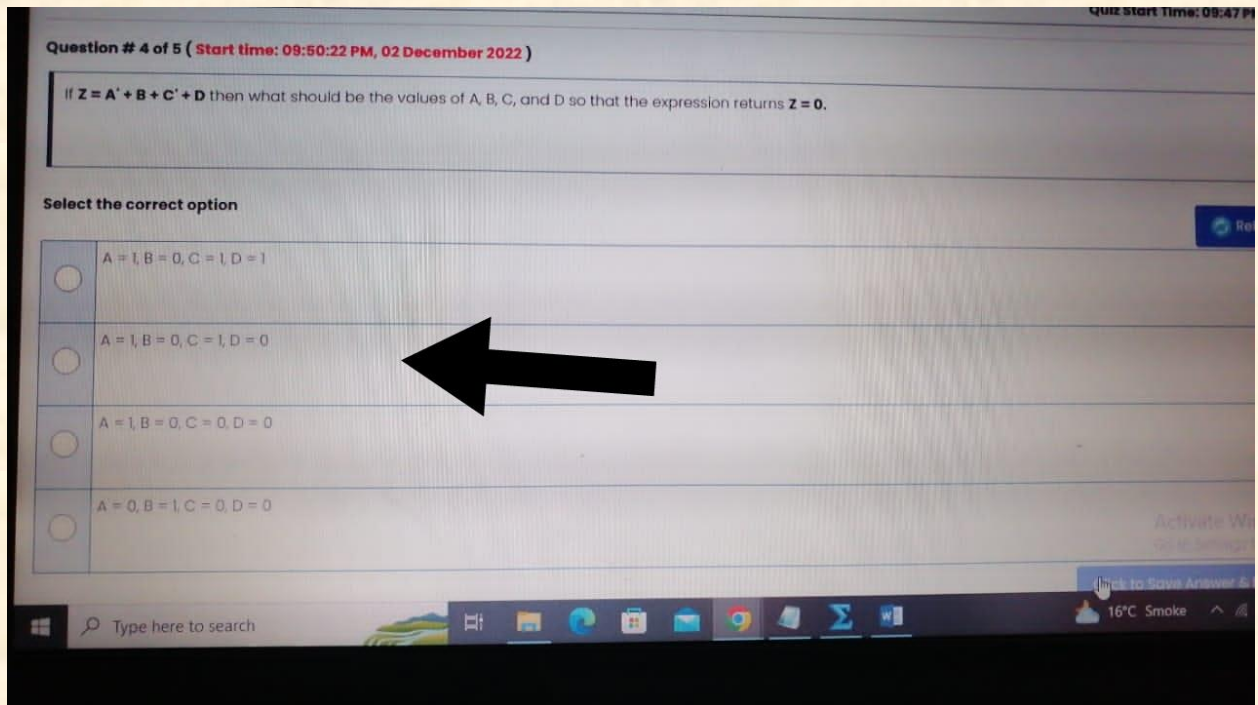
If $Z = A' + B + C' + D$ then what should be the values of A, B, C, and D so that the expression returns $Z = 0$.

Select the correct option

- A = 1, B = 0, C = 1, D = 1
- A = 1, B = 0, C = 1, D = 0
- A = 1, B = 0, C = 0, D = 0
- A = 0, B = 1, C = 0, D = 0

Click to Save Answer & Move to Next Question

16°C Smoke



Quiz start Time: 09:47 PM, 02 Decemb

Question # 2 of 5 (Start time: 09:48:37 PM, 02 December 2022)

The _____ gate works opposite to the AND gate. Its Boolean algebra representation is: $C = (A.B)$.

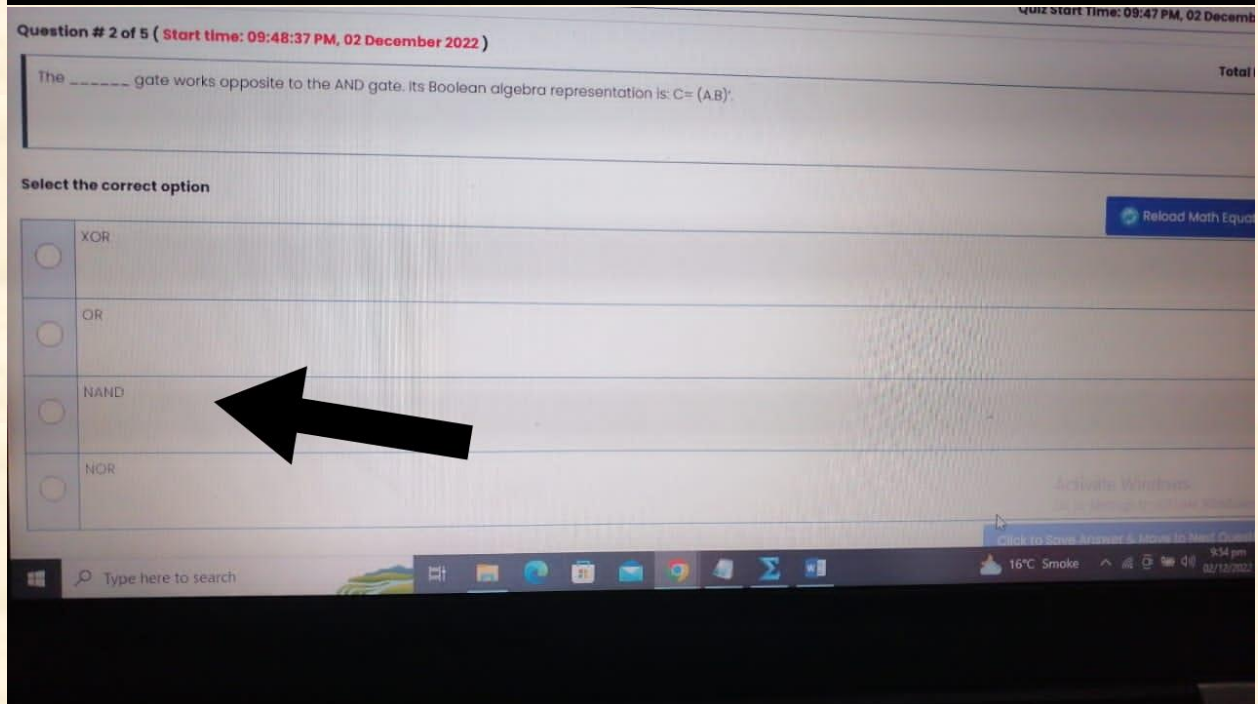
Select the correct option

- XOR
- OR
- NAND
- NOR

Reload Math Equat

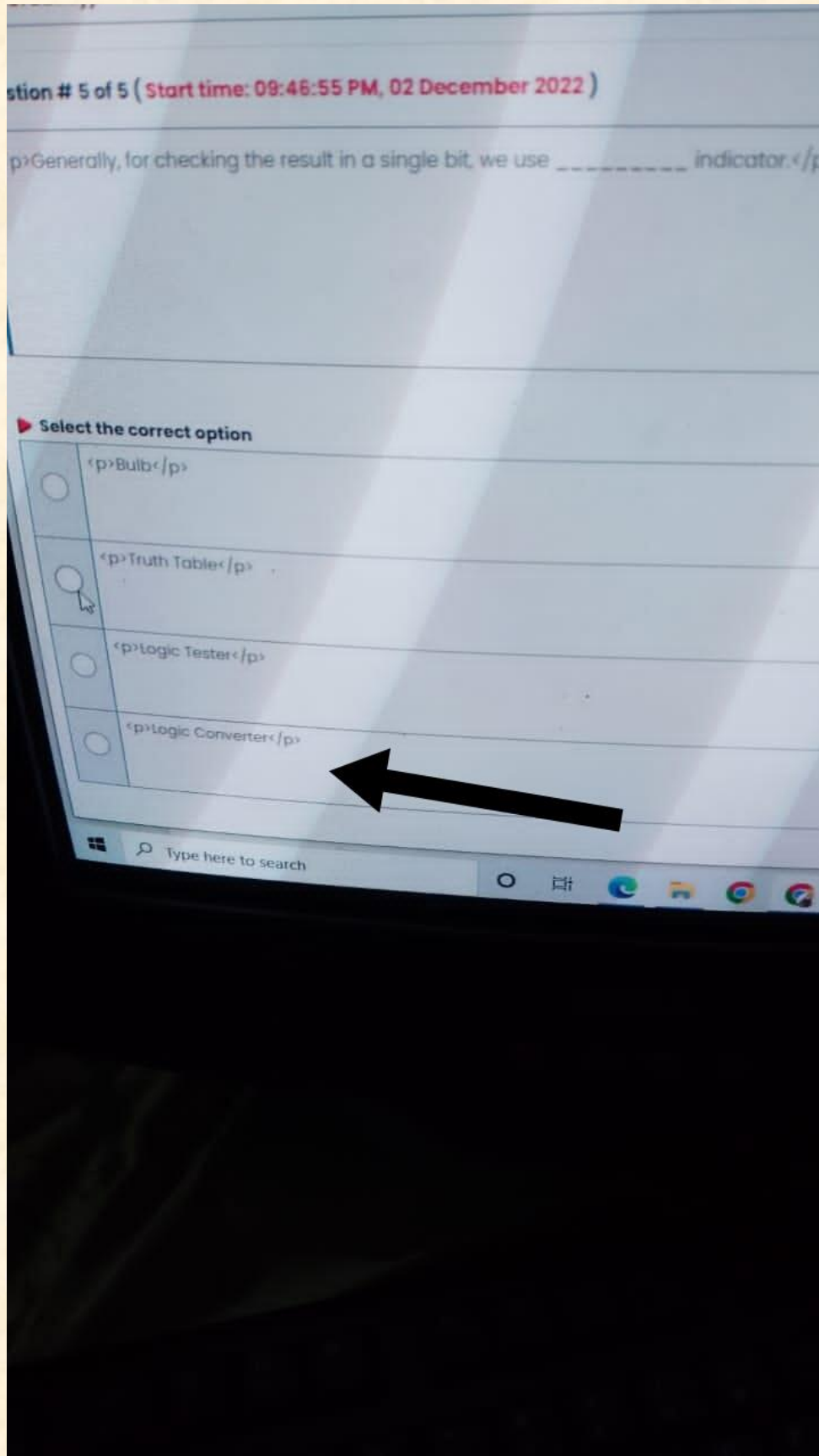
Click to Save Answer & Move to Next Question

16°C Smoke 9:34 pm 02/12/2022



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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Question # 2 of 5 (Start time: 08:40:29 PM, 02 December 20

A logic circuit having two inputs A and B can have -----

Select the correct option

- 2
- 4
- 1
- 3



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BC210402150: DILSHAD AHMAD Time Left 86 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 202

Question # 2 of 10 (Start time: 05:21:15 PM, 12 December 2022) Total Marks

We can simplify a logic circuit using _____ device in Electronic WorkBench.

Select the correct option Reload Math Equations

- Boolean Algebra
- Truth Table
- Circuit Minimizer
- Logic Converter

Click to Save Answer & Move to Next Question

A large purple arrow points to the "Logic Converter" option.

BC210402150: DILSHAD AHMAD Time Left 81 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 202

Question # 3 of 10 (Start time: 05:21:44 PM, 12 December 2022) Total Marks

_____ is used to generate a square wave/pulse.

Select the correct option Reload Math Equations

- Frequency
- Time Interval
- NAND Gate
- Clock

Click to Save Answer & Move to Next Question

A large purple arrow points to the "Clock" option.

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Screenshot_20221212_053458.png

BC210402150: DILSHAD AHMAD Time Left 86 sec(s)


CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 2022

Question # 4 of 10 (Start time: 05:23:57 PM, 12 December 2022) Total Marks

To draw a logic circuit for boolean expression $AB'+C$, how many gates are used?

Select the correct option Reload Math Equations

- 4
- 2
- 3
- 1



Screenshot_20221212_053516.png

BC210402150: DILSHAD AHMAD Time Left 03 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 2022


Question # 5 of 10 (Start time: 05:24:14 PM, 12 December 2022) Total Marks

De Morgan's Theory is used to convert _____ gates to _____ ones.

Select the correct option Reload Math Equations

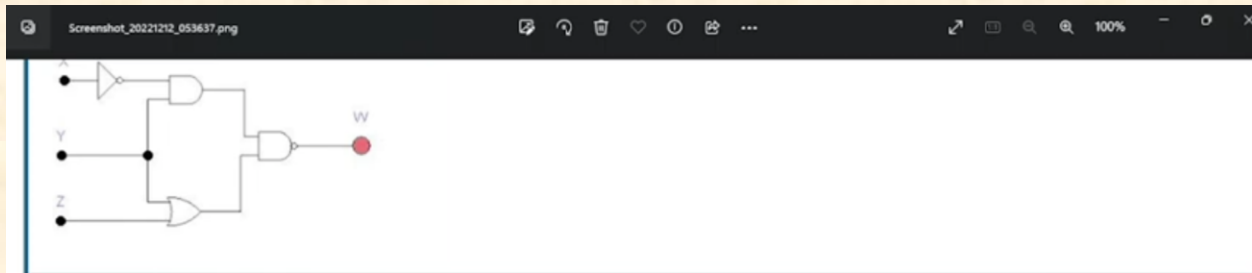
- AND/NAND, OR/NOR
- AND/XOR, XOR/AND
- AND/OR, NOR/NAND
- OR/NOT, NOT/OR

Click to Save Answer & Move to Next Question



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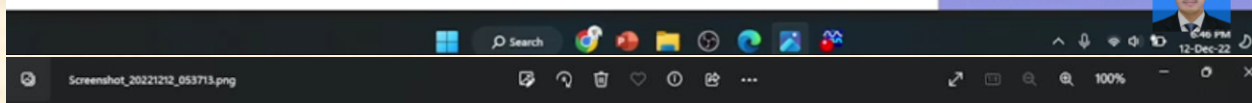


Select the correct option

Reload Math Equations

- $A + C'$
- $A + B' + C$
- $A + B'$
- $B + C$

Click to Save Answer & Move to Next Question



BC210402150: DILSHAD AHMAD

Time Left 86 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 05:18 PM, 12 December 2022

Question # 7 of 10 (Start time: 05:27:15 PM, 12 December 2022)

Total Marks

The _____ gate works opposite to the OR gate. Its Boolean algebra representation is: $C = (A+B)'$

Select the correct option

Reload Math Equations

- NOT
- NOR
- NAND
- XOR



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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Screenshot_20221212_053746.png

BC210402150: DILSHAD AHMAD

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Time Left 75 sec(s)

Quiz Start Time: 05:18 PM, 12 December 2022

Question # 8 of 10 (Start time: 05:28:39 PM, 12 December 2022)

Total Marks

If $A = 0$ & $B = 1$, What will be the output of $F = (A \cdot B) + A' \cdot (A+B)$.

Select the correct option

Reload Math Equations

0

1

Click to Save Answer & Move to Next Question

Screenshot_20221212_053810.png

BC210402150: DILSHAD AHMAD

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Time Left 80 sec(s)

Quiz Start Time: 05:18 PM, 12 December 2022

Question # 9 of 10 (Start time: 05:30:11 PM, 12 December 2022)

Total Marks

In Electronics Workbench, which of the following is the best way to analyze/check each single output of our circuit.

Select the correct option

Reload Math Equations

Bulb

Red Probe

7-segment display

Ammeter

Click to Save Answer & Move to Next Question

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Screenshot_20221212_053838.png

BC210402150: DILSHAD AHMAD Time Left 81 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 202

Question # 10 of 10 (Start time: 05:31:44 PM, 12 December 2022) Total Marks

For a Boolean expression: $C = A \cdot B$ gate will be used.

Select the correct option Reload Math Equations

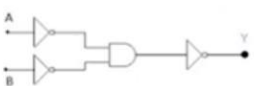
- XOR
- NOR
- OR
- AND

Click to Save Answer & Move to Next Question

Screenshot_20221212_053305.png

Question # 1 of 10 (Start time: 05:18:17 PM, 12 December 2022) Total Marks

What will be the Boolean Equation for (Y) for the given Gate Level Diagram.



$\overline{A + B} = A + B$

Select the correct option Reload Math Equations

- A'B'
- A' + B
- A + B
- AB

Click to Save Answer & Move to Next Question

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CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

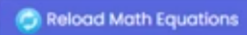
Quiz Start Time: 03:07 PM, 13 December 2022

Question # 10 of 10 (Start time: 03:16:42 PM, 13 December 2022)




Total Marks: 1

_____ is known as a universal gate.

Select the correct option

 Reload Math Equations

<input type="radio"/>	XNOR
<input checked="" type="radio"/>	AND
<input type="radio"/>	NAND
<input type="radio"/>	OR



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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

9:23 PM 4G 3.3 K/s



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Question # 1 of 10 (Start time: 09:24:42 PM, 13 December 2022)

What will be the Boolean Expression of the stated Gate Level Diagram.

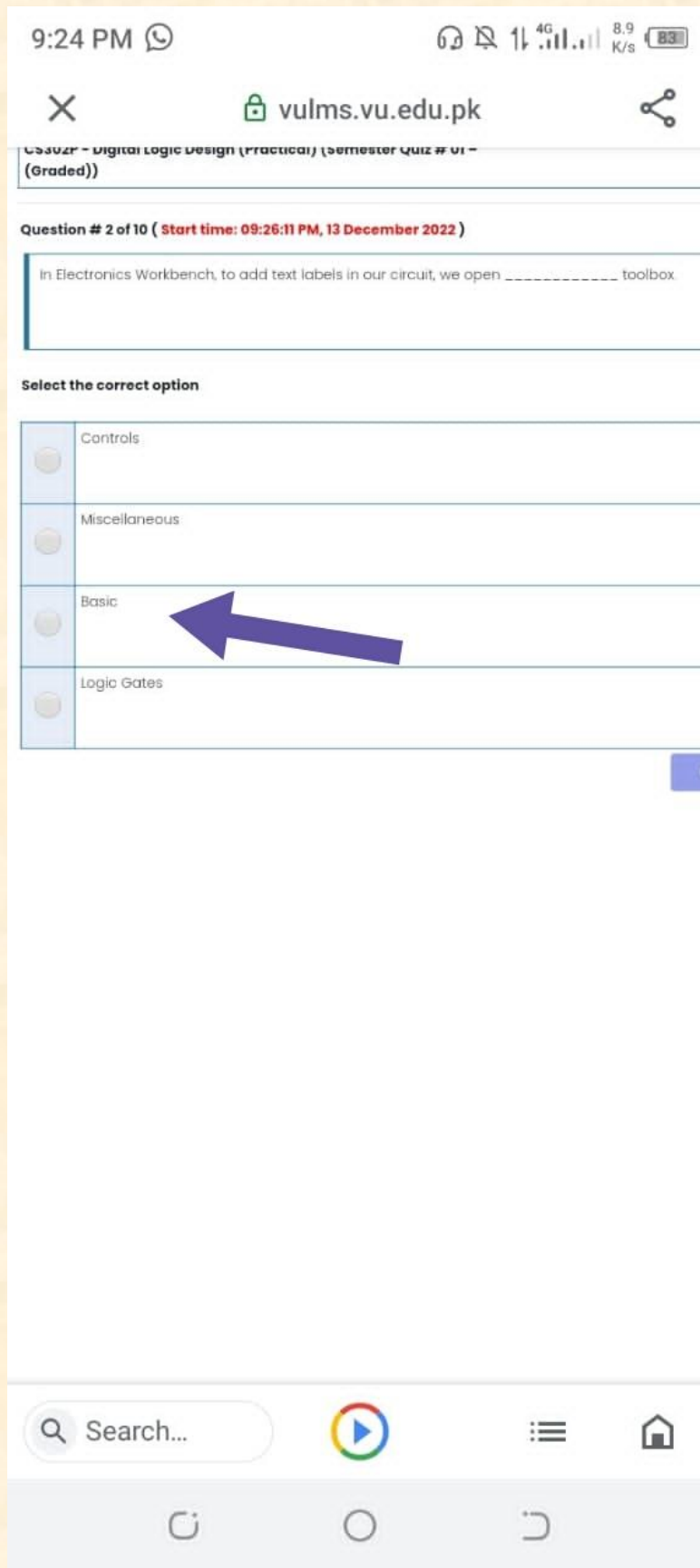
Select the correct option

<input type="radio"/>	$A+B+C'$
<input type="radio"/>	$A'+B'+C'$
<input checked="" type="radio"/>	$A'+B+C'$
<input type="radio"/>	$A+B+C$

Search...  

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9:26 PM

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Question # 3 of 10 (Start time: 09:27:34 PM, 13 December 2022)

According to the commutative law of Boolean algebra, which of the following expression is **TRUE**?

Select the correct option







- $AB = A'B$
- $A'B = AB$
- $AB = BA$
- $AB = A+B$




Not Sure

Search...   

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9:27 PM      8.1 K/s  83%


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Question # 4 of 10 (Start time: 09:28:55 PM, 13 December 2022)

To draw a logic circuit for boolean expression $AB'+C$, how many gates are used?


Select the correct option


1




3 




2

4

 cli

 Search...

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9:28 PM 4G 793 B/s 83%

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QUESTION # 3 OF 10 (START TIME: 09:22:42 PM, 13 DECEMBER 2022)




If $Z = A' + B + C' + D$ then what should be the values of A, B, C, and D so that the expression returns $Z = 0$.

Select the correct option

- A = 1, B = 0, C = 0, D = 0
- A = 0, B = 1, C = 0, D = 0
- A = 1, B = 0, C = 1, D = 1
- A = 1, B = 0, C = 1, D = 0

Click to See Answer

Search...   

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9:29 PM

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CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Question # 6 of 10 (Start time: 09:31:10 PM, 13 December 2022)

A commutative law of boolean algebra involves _____ boolean variable(s)

Select the correct option

- 2
- 4
- 1
- 3

Click

Search...

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9:30 PM 4G 28.6 K/s 82%

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CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Question # 7 of 10 (Start time: 09:32:30 PM, 13 December 2022)

What will be the Simplified Boolean Expression of the given Gate Level Diagram.

```
graph LR; X((X)) --> NOT1[NOT]; NOT1 --> AND1[AND]; Y((Y)) --> AND1; Y --> OR1[OR]; Z((Z)) --> OR1; AND1 --> AND2[AND]; OR1 --> AND2; AND2 --> W((W));
```

Select the correct option






<input type="radio"/>	$A + B' + C$
<input type="radio"/>	$A + B'$
<input type="radio"/>	$B + C$
<input type="radio"/>	$A + C'$



Not sure

Search...

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9:31 PM     


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Question # 8 of 10 (Start time: 09:33:13 PM, 13 December 2022)

Switch can be found in _____ toolbox.

Select the correct option

<input type="radio"/>	Input
<input type="radio"/>	Sources
<input type="radio"/>	Gates
<input type="radio"/>	Basic



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9:33 PM 4G 474 B/s 82%

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(Graaegj)

Question # 9 of 10 (Start time: 09:34:54 PM, 13 December 2022)

If $A = 0$ & $B = 1$, What will be the output of $F = (A \cdot B) + A'(A+B)$.

Select the correct option

<input type="radio"/>	0
<input checked="" type="radio"/>	1

🔍 Search... ▶ ☰ 🏠

⏪ ⏩ ⏹

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9:34 PM

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Signal Logic Design (Faculty) (Semester Quiz # 01 (Graded))

Question # 10 of 10 (Start time: 09:36:02 PM, 13 December 2022)

A K-Map of size 2x2 can be used to map a _____ input boolean expression.

Select the correct option

- 4
- 1
- 3
- 2

Search...

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10:11

10:11 64%

home vulms.vu.edu.pk 1

BC210411740: MISBAH SHAKOOR

Time Left 89 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 10:09 PM, 13 December 2022

Question # 2 of 10 (start time: 10:11:17 PM, 13 December 2022) Total Marks: 1

_____ gate with a bubble at the output is know as NAND gate.

Select the correct option

Reload Math Equations

OR

AND

NOT

NOR

Click to Save Answer & Move to Next Question

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

10:12

63%

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BC210411740: MISBAH SHAKOOR Time Left 89 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 10:09 PM, 13 December 2022

Question # 3 of 10 (Start time: 10:12:41 PM, 13 December 2022) Total Marks: 1

_____ is known as a universal gate.

Select the correct option Reload Math Equations

<input type="radio"/>	OR
<input checked="" type="radio"/>	NAND
<input type="radio"/>	AND
<input type="radio"/>	XNOR

Click to Save Answer & Move to Next Question

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10:14 63%

home vulms.vu.edu.pk 1

BC210411740: MISBAH SHAKOOR **Time Left** 89 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) **Quiz Start Time: 10:09 PM, 13 December 2022**

Question # 4 of 10 (Start time: 10:14:47 PM, 13 December 2022) **Total Marks: 1**

In Electronics Workbench, to use a **Connector** component in circuit, we open _____ toolbox.

Select the correct option **Reload Math Equations**

- Instruments
- Basic
- Miscellaneous
- Logic Gates

Click to Save Answer & Move to Next Question

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

10:17

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BC210411740: MISBAH SHAKOOR Time Left 89 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 10:09 PM, 13 December 2022

Question # 6 of 10 (Start time: 10:17:51 PM, 13 December 2022) Total Marks: 1

According to De-Morgan's law, which of the following is TRUE?

Select the correct option

- A. $B = (A \cdot B)'$
- $(A + B)' = (A' + B)'$
- $A \cdot B = (A + B)'$
- $A + B = (A' \cdot B)'$

[Click to Save Answer & Move to Next Question](#)

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

10:19

63%

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BC210411740: MISBAH SHAKOOR Time Left 89 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 10:09 PM, 13 December 2022

Question # 7 of 10 (start time: 10:19:47 PM, 13 December 2022) Total Marks: 1

The ____ gate works opposite to the OR gate. Its Boolean algebra representation is: $C=(A+B)'$

Select the correct option Reload Math Equations

- XOR
- NAND
- NOT
- NOR

Click to Save Answer & Move to Next Question

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

10:20 63%

home vulms.vu.edu.pk 1

BC210411740: MISBAH SHAKOOR **Time Left** 89 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) **Quiz Start Time: 10:09 PM, 13 December 2022**

Question # 8 of 10 (Start time: 10:20:57 PM, 13 December 2022) **Total Marks: 1**

In Electronics Workbench, the default value for the duty cycle of the clock source is _____.

Select the correct option **Reload Math Equations**

- 50%
- 100%
- 2%
- 5%

Click to Save Answer & Move to Next Question

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10:21

62%

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BC210411740: MISBAH SHAKOOR Time Left 89 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 10:09 PM, 13 December 2022

Question # 9 of 10 (Start time: 10:21:42 PM, 13 December 2022) Total Marks: 1

A logic circuit having two inputs A and B can have _____ different input combinations.

Select the correct option Reload Math Equations

4

1

3

2

Click to Save Answer & Move to Next Question

Not sure

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Question # 7 of 10 (Start time: 08:57:11 PM, 16 December 2022)

Which data type from the following will take lesser space in memory?

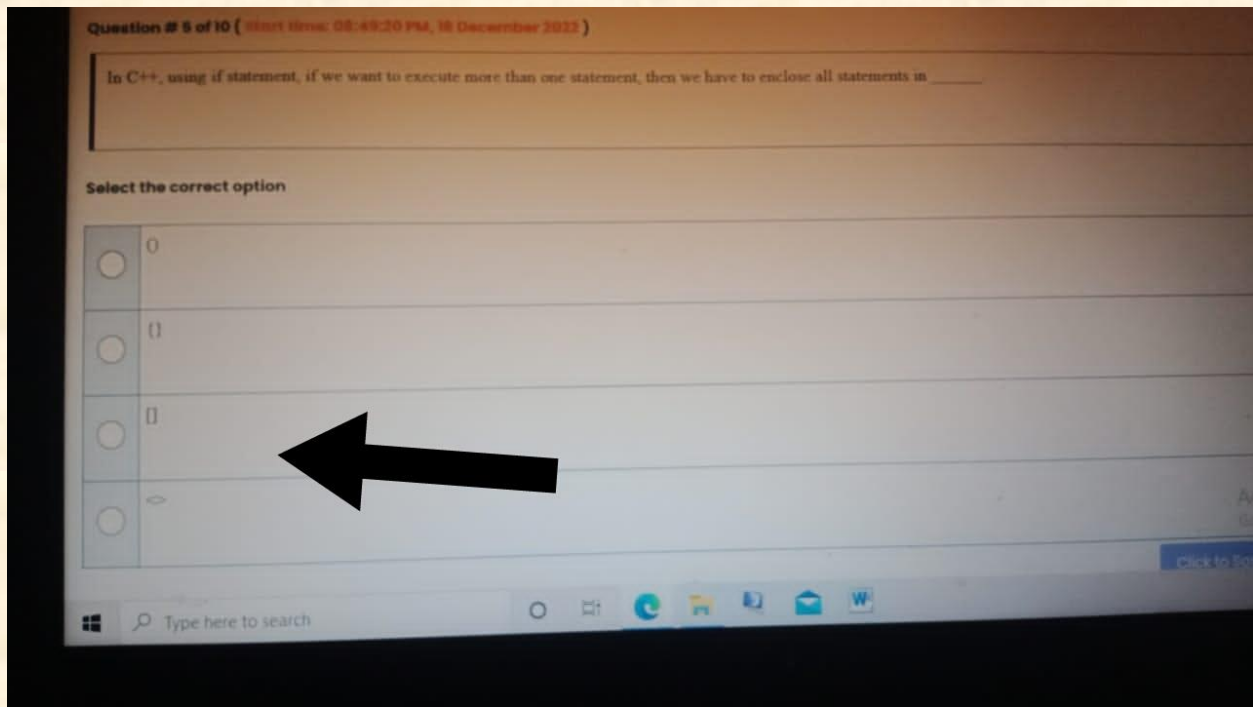
Select the correct option

- short
- char
- int
- float



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CS201P - Introduction to Programming (Practical) (Regular Quiz 1)

Question # 3 of 10 (Start time: 08:47:48 PM, 16 December 2022)

Which of the following is true about the *switch* statement?

Select the correct option

- It works efficiently on compound conditions which use logical operators.
- It is valid for the large programs only.
- It cannot handle the compound conditions which use logical operators.
- It is valid for the small programs only.



Type here to search

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

5:55 📶 44%

🏠 vulms.vu.edu.pk/Quiz/ + 5 ⋮


CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 3 [6 - 7] - (Not Graded))

Question # 4 of 5 (Start time: 05:55:09 PM, 16 December 2022)

There are _____ cells in a 5-variable K-map.

Select the correct option

<input type="radio"/>	24
<input type="radio"/>	64
<input type="radio"/>	16
<input type="radio"/>	32



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5:54 🌙 📶 44% 🔋

🏠 vulms.vu.edu.pk/Quiz/ + 5 ⋮


CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 3 [6 - 7] - (Not Graded))

Question # 3 of 5 (**Start time: 05:53:55 PM, 16 December 2022**)

There are _____ cells in a 4-variable K-map.

Select the correct option

<input type="radio"/>	16
<input type="radio"/>	20
<input type="radio"/>	8
<input type="radio"/>	24



⋮ ○ <

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5:53 45%

home vulms.vu.edu.pk/Quiz/ + 5

BC220205096: HUSSNAIN ZULFIQAR

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 3 [6-7] - (Not Graded)) Quiz Start Time: 05:51

Question # 2 of 5 (Start time: 05:52:55 PM, 16 December 2022)

Each group of adjacent Minterms (group size in powers of two) corresponds to a possible product term of the given ...

Select the correct option

<input type="radio"/>	Set
<input type="radio"/>	Function
<input type="radio"/>	Value
<input type="radio"/>	Word

Click to Save Answer & Move

III O <

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5:51

vulms.vu.edu.pk/Quiz/

Question # 1 of 5 (Start time: 05:51:18 PM, 18 December 2022)

Consider a 3-variable Karnaugh Map. How many groups of 1's can be made from the below given Karnaugh Map?

A/BC	00	01	11	10
0	0	1	1	0
1	1	1	1	1

Select the correct option


1

6

4

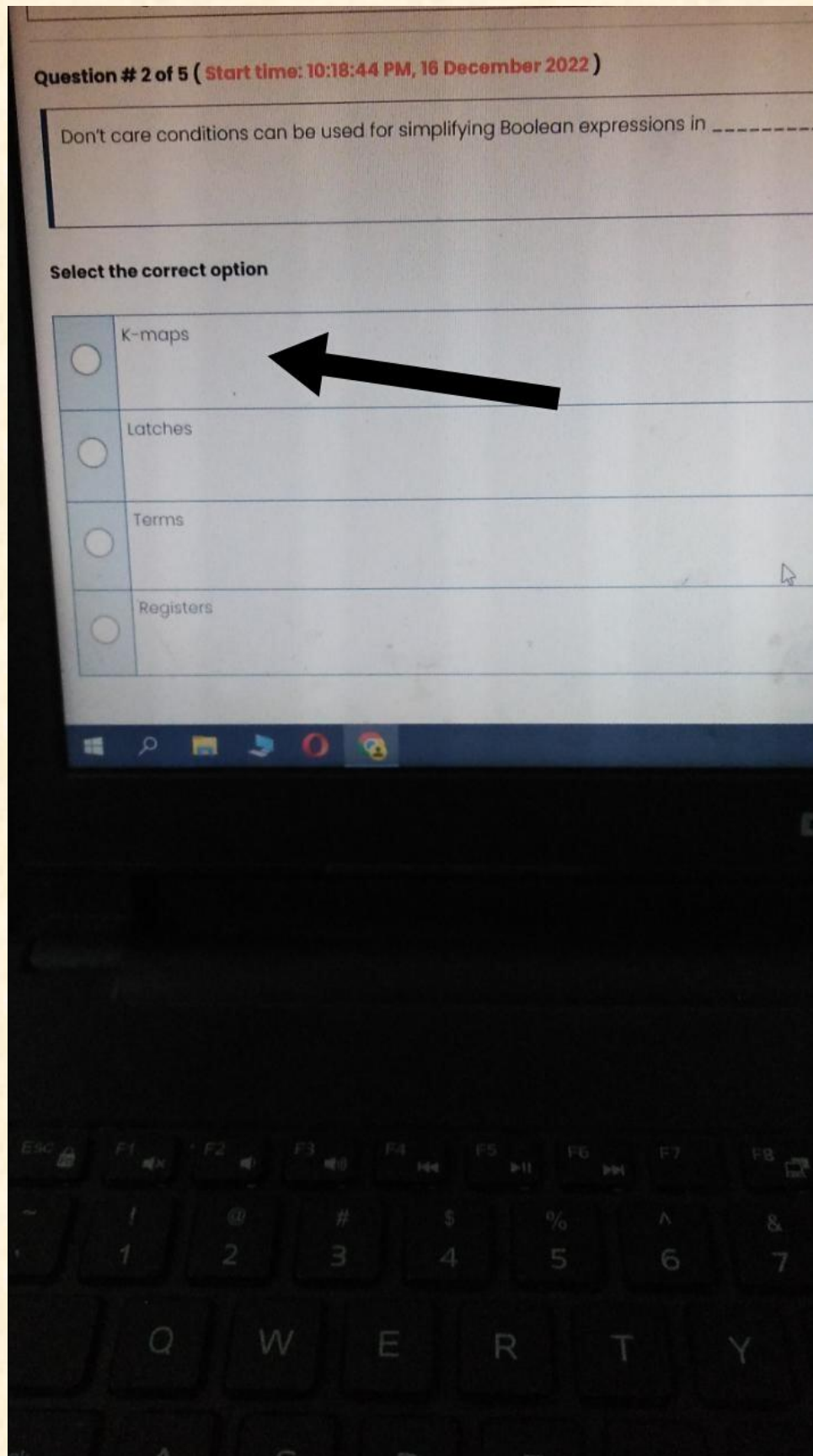
2

Click to Save Answer



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10:31 PM | 37.4KB/s | 🔒 🔊 · 📶 📶 74

🔒 vulms.vu.edu.pk/Quiz/Quiz + 17 ⋮

BC210425662: SALWA Time Left 89 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 3 [6 - 7] - (Not Graded)) Quiz Start Time: 10:35 PM, 16 December 2022

Question # 2 of 5 (Start time: 10:36:06 PM, 16 December 2022) Total Marks: 1

A Karnaugh map (K-map) is an abstract form of _____ diagram organized as a matrix of squares.

Select the correct option 🔄 Reload Math Equations

- Block diagram
- Venn Diagram
- Triangular Diagram
- Cycle Diagram

[Click to Save Answer & Move to Next Question](#)

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10:00 PM 22.1 K/s 22%

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BC210406095: SAMRA

CS302P - Digital Logic Design (Practical) (Semester Quiz # 02 -- (Graded)) Quiz Start Time

Question # 1 of 10 (Start time: 09:59:59 PM, 21 December 2022)

Each product term of a group, w'xy' and wy, represents the _____ in that group.

Select the correct option

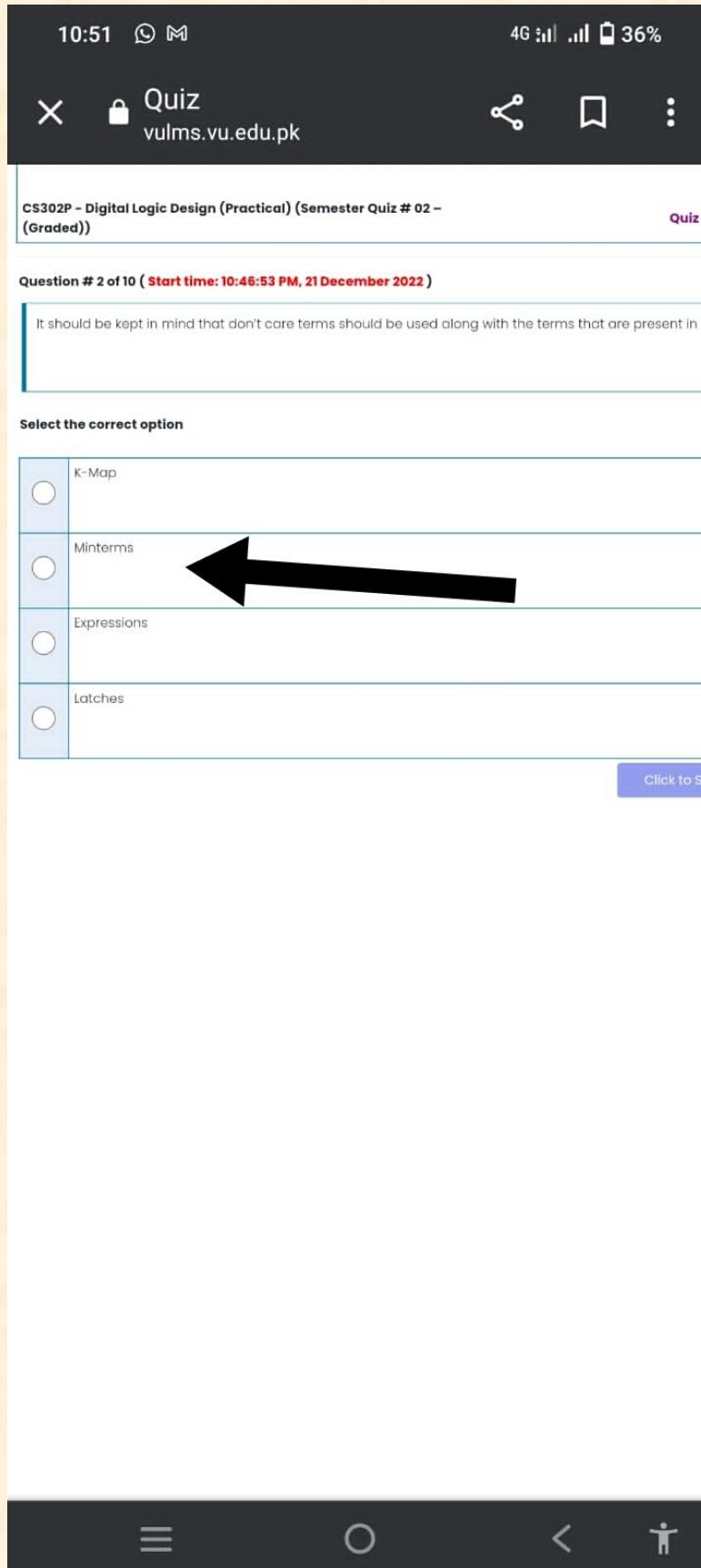
<input type="radio"/>	Sum of Minterms
<input type="radio"/>	Input
<input type="radio"/>	Sum of Maxterms
<input type="radio"/>	POS

[Click to Save Answer](#)

Search...

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10:53 4G 35%

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CS302P - Digital Logic Design (Practical) (Semester Quiz # 02 - (Graded))

Question # 6 of 10 (Start time: 10:49:03 PM, 21 December 2022)

The K-map based Boolean reduction is based on the following Unifying Theorem: $A + A' = 1$.

Select the correct option

- Force
- Complementarity
- Impact
- Non Impact

Click

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Jazz ZONG 4G 73% 8:30 PM

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BC220213510: ADIL ALVI

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 5 [8 - 10] - (Not Graded)) Quiz Star


Question # 2 of 5 (**Start time: 08:30:33 PM, 20 January 2023**)

A half adder can be built by using the combination of _____ and _____ gates

Select the correct option

<input type="radio"/>	OR, XOR
<input type="radio"/>	AND, XOR
<input type="radio"/>	AND, OR
<input type="radio"/>	AND, NOT

Click to Save A



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Jazz ZONG 4G 72% 8:31 PM

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BC220213510: ADIL ALVI

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 5 [8 - 10] - (Not Graded)) Quiz Start Time:

Question # 3 of 5 (Start time: 08:30:54 PM, 20 January 2023)

When implementing a half adder by using gates, which of the following gates is used to give Sum (S) as an output?

Select the correct option

<input type="radio"/>	OR
<input type="radio"/>	NOR
<input type="radio"/>	XOR
<input type="radio"/>	AND

Click to Save Answer



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Jazz
ZONG

4G 72% 8:31 PM



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BC220213510: ADIL ALVI

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 5 [8 - 10] - (Not Graded))

Question # 4 of 5 (Start time: 08:31:11 PM, 20 January 2023)

In order to build an XOR gate using NAND gates only, _____ gate(s) are required.

Select the correct option

- 2
- 3
- 4
- 1



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Jazz
ZONG

4G 72% 8:31 PM



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BC220213510: ADIL ALVI

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 5 [8 - 10] - (Not Graded))

Question # 5 of 5 (Start time: 08:31:23 PM, 20 January 2023)

Which of the following gates is used for parity generation for a binary data string

Select the correct option

- | | |
|-----------------------|-----|
| <input type="radio"/> | NOR |
| <input type="radio"/> | XOR |
| <input type="radio"/> | AND |
| <input type="radio"/> | OR |



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Jazz
ZONG

4G 72% 8:32 PM



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CS302P - Digital Logic Design (Practical) (Lab Attendance - 10] - (Not Graded))

Question # 1 of 5 (Start time: 08:32:48 PM, 20 January 2023

Total number of inputs in a half adder is _____.

Select the correct option

- | | |
|-----------------------|---|
| <input type="radio"/> | 3 |
| <input type="radio"/> | 1 |
| <input type="radio"/> | 2 |
| <input type="radio"/> | 4 |



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CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 5 [8 - 10] - (Not Graded))

Question # 4 of 5 (Start time: 08:34:20 PM, 20 January 2023)

Which of the following expressions represents **S** output in a full adder?

Select the correct option



$$(A + B) \times C_{in}$$



$$(A \times C_{in}) \oplus B$$



$$A \oplus B \oplus C_{in}$$



$$(A \times B) \oplus C_{in}$$

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4G 72% 8:33 PM



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CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # - 10] - (Not Graded))

Question # 2 of 5 (**Start time: 08:33:04 PM, 20 January 2023**)

How many select lines would be required for an 8x1 multiplexer?

Select the correct option

- | | |
|-----------------------|---|
| <input type="radio"/> | 3 |
| <input type="radio"/> | 8 |
| <input type="radio"/> | 2 |
| <input type="radio"/> | 1 |



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Jazz ZONG 4G 72% 8:34 PM

Quiz vulms.vu.edu.pk

C220213511: WAJID ALVI

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 5 [8/10] - (Not Graded)) Quiz Start Time: 08:34:35 PM

Question # 5 of 5 (Start time: 08:34:35 PM, 20 January 2023)

When implementing a half adder by using gates, which of the following gates is used to return carry bit as an output?

Select the correct option Re

- AND
- OR
- XOR
- NAND

Click to Save Answer & Move to Next Question

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BC210402150: DILSHAD AHMAD Time Left 85 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1 - 3] - (Not Graded)) Quiz Start Time: 03:39 PM, 02 December 2022

Question # 1 of 5 (Start time: 03:39:40 PM, 02 December 2022) Total Marks: 1

Which version of Electronics Workbench is being used in this course for creating and simulating digital circuits?

Select the correct option Reload Math Equations

- version 12.5
- version 10
- version 5.12
- version 6

The screenshot shows a Windows taskbar at the bottom with various application icons and a system tray showing the time as 2:22 PM on 02-Dec-22. A small profile picture of a man is visible in the bottom right corner of the quiz interface.

BC210402150: DILSHAD AHMAD Time Left 71 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1 - 3] - (Not Graded)) Quiz Start Time: 03:39 PM, 02 December 2022

Question # 2 of 5 (Start time: 03:40:55 PM, 02 December 2022) Total Marks: 1

You can find the truth table of a gate using the _____

Select the correct option Reload Math Equations

- logic simplifier
- logic converter
- logic manager
- logic extractor

The screenshot shows a Windows taskbar at the bottom with various application icons and a system tray showing the time as 2:23 PM on 02-Dec-22. A small profile picture of a man is visible in the bottom right corner of the quiz interface.

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Screenshot of a quiz question in Electronics Workbench. The user is identified as BC210402150: DILSHAD AHMAD. The quiz is titled "CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1 - 3] - (Not Graded))" and started at 03:39 PM on 02 December 2022. The question is "Question # 3 of 5 (Start time: 03:42:41 PM, 02 December 2022)" and asks: "In Electronics Workbench, to use a Connector component in circuit, we open _____ toolbox." The options are: Logic Gates, Instruments, Basic, and Miscellaneous. The "Basic" option is selected. A "Reload Math Equations" button is visible. The Windows taskbar at the bottom shows the time as 2:23 PM on 02-Dec-22.

Screenshot of a quiz question in Electronics Workbench. The user is identified as BC210402150: DILSHAD AHMAD. The quiz is titled "CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1 - 3] - (Not Graded))" and started at 03:39 PM on 02 December 2022. The question is "Question # 4 of 5 (Start time: 03:46:33 PM, 02 December 2022)" and asks: "In Electronics Workbench, which of the following is the best way to analyze/check each single output of our circuit." The options are: 7-segment display, Red Probe, Ammeter, and Bulb. The "Bulb" option is selected. A "Reload Math Equations" button is visible. The Windows taskbar at the bottom shows the time as 2:24 PM on 02-Dec-22.

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The image shows two screenshots. The top screenshot is a quiz interface for 'CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz #1 [1-3] - (Not Graded))'. The user is 'DILSHAD AHMAD' and the time left is 83 seconds. The question is: 'In Electronics Workbench, the default value for the frequency of the clock source is _____'. The options are: 5 Hz, 2 Hz, 50 Hz, and 2 KHz. The bottom screenshot shows a Google search for '_____ gate will give high output (1) if any input is high (1)'. The search results show a link to 'Logic OR Gate Tutorial' from electronics-tutorials.ws. The search also shows 'People also ask' questions: 'Which gate will give high output 1 if any input is high?', 'Which gate gives output 1 when all of its input is 1?', and 'Which gate will give high output if any output is high?'. The quiz interface also shows a 'Reload Math Equations' button and a 'clideo.co' watermark.

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

The screenshot shows a Google search for "In Electronics Workbench, which of the following is the best way to analyze/check each single output of our circuit." The search results include links to a tutorial, a lab manual, and a PDF. The quiz interface on the right shows the question and four options: Bulb, Ammeter, 7-segment display, and Red Probe. The 7-segment display option is selected.

Question # 3 of 5 (Start time: 11:42:04 AM, 02 December 2022) Total Marks: 1

In Electronics Workbench, which of the following is the best way to analyze/check each single output of our circuit.

Select the correct option

- Bulb
- Ammeter
- 7-segment display
- Red Probe

Click to Save Answer & Move to Next Question

The screenshot shows a Google search for "For inverting an input, we use _____ gate." The search results include a Khan Academy article on logic gates and a website about inverters. The quiz interface on the right shows the question and four options: OR Gate, NOT Gate, AND Gate, and XOR Gate. The NOT Gate option is selected.

Question # 4 of 5 (Start time: 11:42:55 AM, 02 December 2022) Total Marks: 1

For inverting an input, we use _____ gate.

Select the correct option

- OR Gate
- NOT Gate
- AND Gate
- XOR Gate

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

The image displays two screenshots of a quiz interface, likely from a learning management system (LMS) at VU. The top screenshot shows a search for "Electronic WorkBench tutorial" and a quiz question: "In Electronics Workbench, the default value for the duty cycle of the clock source is". The bottom screenshot shows a search for "The ___ gate works opposite to the OR gate" and a quiz question: "The ___ gate works opposite to the OR gate. Its Boolean algebra representation is: $C=(A+B)'$ ".

Top Screenshot:

- Search results for "Electronic WorkBench tutorial" include links to physics.udel.edu, wps.prenhall.com, and slideshare.net.
- Quiz question: "Question # 5 of 5 (Start time: 11:43:16 AM, 02 December 2022) Total Marks: 1".
- Question text: "In Electronics Workbench, the default value for the duty cycle of the clock source is".
- Options: 100%, 5%, 2%, 50%.

Bottom Screenshot:

- Search results for "The ___ gate works opposite to the OR gate" include links to electronics-tutorials.ws and techtarget.com.
- Quiz question: "Question # 2 of 5 (Start time: 11:40:42 AM, 02 December 2022) Total Marks: 1".
- Question text: "The ___ gate works opposite to the OR gate. Its Boolean algebra representation is: $C=(A+B)'$ ".
- Options: NAND, XOR, NOT.

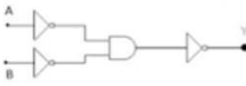
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Screenshot_20221212_053305.png


Question # 1 of 10 (Start time: 05:18:17 PM, 12 December 2022) Total Marks

What will be the Boolean Equation for (Y) for the given Gate Level Diagram.


$$\overline{\overline{A} \cdot \overline{B}} = A + B$$

Select the correct option Reload Math Equations

- A'B'
- A' + B
- A + B
- AB

Click to Save Answer & Move to Next Question 

Windows taskbar: Search, File Explorer, Task View, Microsoft Edge, Chrome, VLC, Zoom, Teams, OneDrive, Outlook, Settings, Power, Network, Volume, 4:42 PM 12-Dec-22

Screenshot_20221212_053344.png

BC210402150: DILSHAD AHMAD Time Left 86 sec(s)


CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 2022

Question # 2 of 10 (Start time: 05:21:15 PM, 12 December 2022) Total Marks

We can simplify a logic circuit using _____ device in Electronic WorkBench.

Select the correct option Reload Math Equations

- Boolean Algebra
- Truth Table
- Circuit Minimizer
- Logic Converter

Click to Save Answer & Move to Next Question 

Windows taskbar: Search, File Explorer, Task View, Microsoft Edge, Chrome, VLC, Zoom, Teams, OneDrive, Outlook, Settings, Power, Network, Volume, 4:43 PM 12-Dec-22

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BC210402150: DILSHAD AHMAD Time Left 81 sec(s)


CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 2022

Question # 3 of 10 (Start time: 05:21:44 PM, 12 December 2022) Total Marks

_____ is used to generate a square wave/pulse.

Select the correct option Reload Math Equations

- Frequency
- Time Interval
- NAND Gate
- Clock

Click to Save Answer & Move to Next Question 

Windows taskbar: Search, 12:43 PM, 12-Dec-22

BC210402150: DILSHAD AHMAD Time Left 86 sec(s)


CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 2022

Question # 4 of 10 (Start time: 05:23:57 PM, 12 December 2022) Total Marks

To draw a logic circuit for boolean expression $AB'+C$, how many gates are used?

Select the correct option Reload Math Equations

- 4
- 2
- 3
- 1

Click to Save Answer & Move to Next Question 

Windows taskbar: Search, 12:44 PM, 12-Dec-22

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Screenshot_20221212_053516.png

BC210402150: DILSHAD AHMAD

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 05:18 PM, 12 December 2022

Question # 5 of 10 (Start time: 05:24:14 PM, 12 December 2022) Total Marks

DE Morgan's Theory is used to convert _____ gates to _____ ones.

Select the correct option

- AND/NAND, OR/NOR
- AND/XOR, XOR/AND
- AND/OR, NOR/NAND
- OR/NOT, NOT/OR

Reload Math Equations

Click to Save Answer & Move to Next Question

Screenshot_20221212_053637.png

Select the correct option

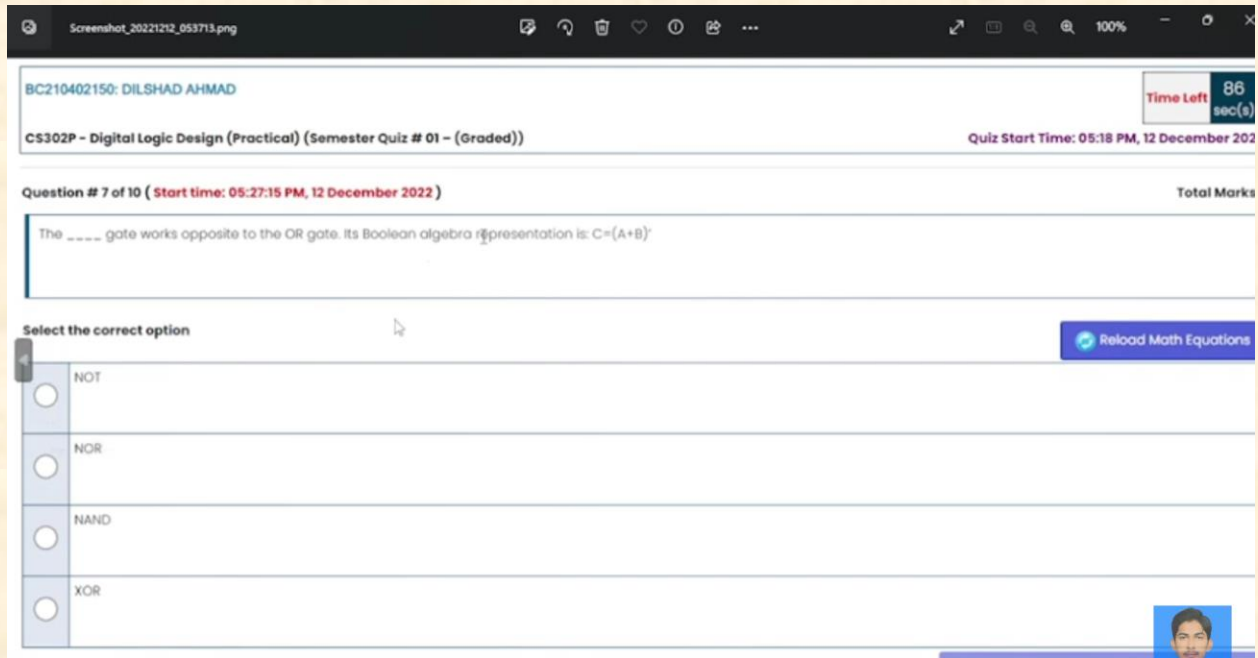
- $A + C'$
- $A + B' + C$
- $A + B'$
- $B + C$

Reload Math Equations

Click to Save Answer & Move to Next Question

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CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 2022

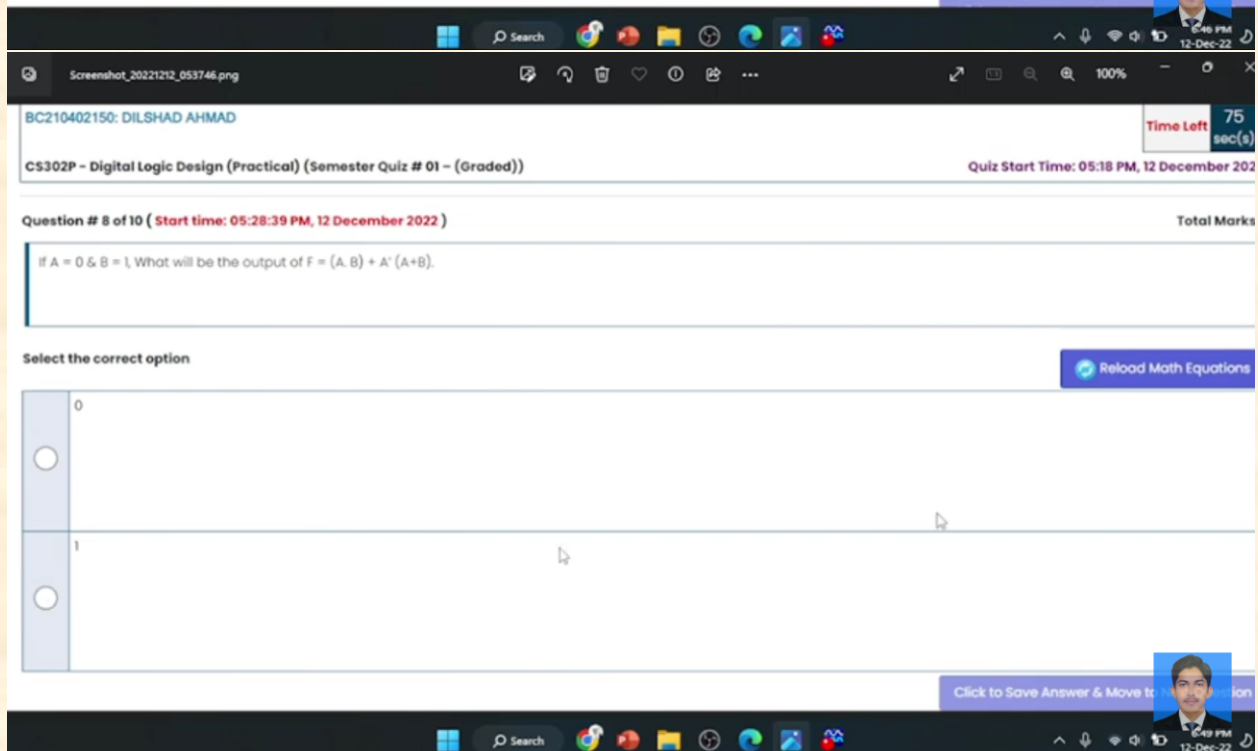
Question # 7 of 10 (Start time: 05:27:15 PM, 12 December 2022) Total Marks

The _____ gate works opposite to the OR gate. Its Boolean algebra representation is: $C = (A+B)'$

Select the correct option Reload Math Equations

- NOT
- NOR
- NAND
- XOR





BC210402150: DILSHAD AHMAD Time Left 75 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded)) Quiz Start Time: 05:18 PM, 12 December 2022


Question # 8 of 10 (Start time: 05:28:39 PM, 12 December 2022) Total Marks

If $A = 0$ & $B = 1$, What will be the output of $F = (A \cdot B) + A' \cdot (A+B)$.

Select the correct option Reload Math Equations

- 0
- 1

Click to Save Answer & Move to Next Question



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Screenshot of a quiz question in a browser window. The browser title is "Screenshot_20221212_053810.png". The page header shows "BC210402150: DILSHAD AHMAD" and "Time Left 80 sec(s)". The quiz title is "CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))" and the start time is "Quiz Start Time: 05:18 PM, 12 December 2022". The question is "Question # 9 of 10 (Start time: 05:30:11 PM, 12 December 2022)" and the total marks are "Total Marks". The question text is "In Electronics Workbench, which of the following is the best way to analyze/check each single output of our circuit." Below the question is a "Select the correct option" section with a "Reload Math Equations" button. The options are: Bulb, Red Probe, 7-segment display, and Ammeter.

Screenshot of a quiz question in a browser window. The browser title is "Screenshot_20221212_053838.png". The page header shows "BC210402150: DILSHAD AHMAD" and "Time Left 81 sec(s)". The quiz title is "CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))" and the start time is "Quiz Start Time: 05:18 PM, 12 December 2022". The question is "Question # 10 of 10 (Start time: 05:31:44 PM, 12 December 2022)" and the total marks are "Total Marks". The question text is "For a Boolean expression: $C = A \cdot B$ _____ gate will be used." Below the question is a "Select the correct option" section with a "Reload Math Equations" button. The options are: XOR, NOR, OR, and AND.

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 03:07 PM, 13 December 2022

Question # 2 of 10 (Start time: 03:08:46 PM, 13 December 2022)

Total Marks: 1

The Boolean function $Z = A + B \cdot C'$ can be implemented in digital logic circuit by connecting _____ gates together.

Select the correct option



<input type="radio"/>	1
<input type="radio"/>	4
<input checked="" type="radio"/>	3
<input type="radio"/>	2



CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 03:07 PM, 13 December 2022

Question # 3 of 10 (Start time: 03:09:40 PM, 13 December 2022)

Total Marks: 1

We can change the number of inputs for a gate by using _____.

Select the correct option



<input checked="" type="radio"/>	Truth Table
<input type="radio"/>	Gate Properties
<input type="radio"/>	Gate Structure
<input type="radio"/>	Gate Diagram



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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 03:07 PM, 13 December 2022

Question # 4 of 10 (Start time: 03:10:31 PM, 13 December 2022)

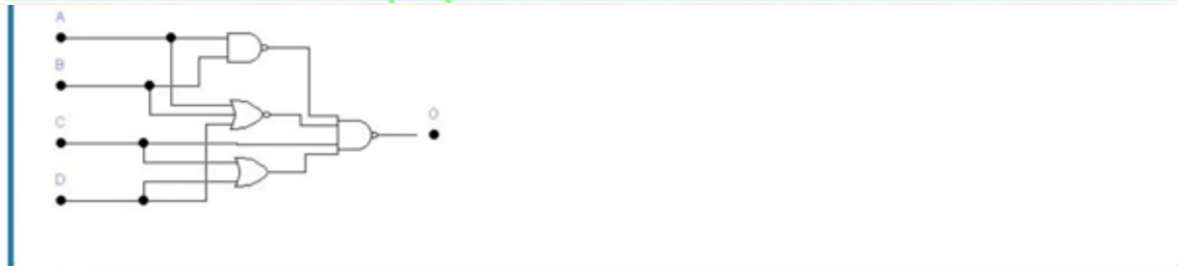
Total Marks: 1

Which simulation package are we using to do our practical labs in CS302P course?

Select the correct option

Reload Math Equations

- Electronic WorkBench V 5.12
- Electronic WorkBench V 10 Pro
- Digital Circuit Simulator V 2.0
- Digital Circuit Designer V 2.0



Select the correct option

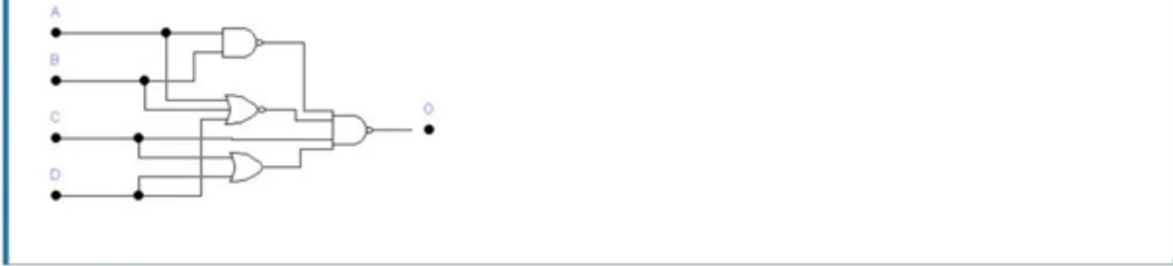
Reload Math Equations

- $A' + B' + C' + D$
- $A + B + C' + D$
- $A + B' + C' + D$
- $A' + B + C + D'$



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Select the correct option

[Reload Math Equations](#)

- $A' + B' + C' + D$
- $A + B + C' + D$
- $A + B' + C' + D$
- $A' + B + C + D'$



CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 03:07 PM, 13 December 2022

Question # 8 of 10 (Start time: 03:15:05 PM, 13 December 2022)

Total Marks: 1

----- gate with a bubble at the output is know as NOR gate.

Select the correct option

[Reload Math Equations](#)

- NAND
- OR
- NOT
- AND



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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 03:07 PM, 13 December 2022

Question # 9 of 10 (Start time: 03:16:16 PM, 13 December 2022)


Total Marks: 1

DE Morgan's Theory is used to convert _____ gates to _____ ones.

Select the correct option

 Reload Math Equations

<input type="radio"/>	AND/OR, NOR/NAND
<input type="radio"/>	AND/XOR, XOR/AND
<input checked="" type="radio"/>	AND/NAND, OR/NOR
<input type="radio"/>	OR/NOT, NOT/OR



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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Jazz ZONG

4G 36% 11:42 PM



vulms.vu.edu.pk/Q



BC220213510: ADIL ALVI

Time Left 55 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:41 PM, 13 December 2022

Question # 1 of 10 (Start time: 11:42:00 PM, 13 December 2022)

Total Marks: 1

----- is also known as a Universal gate.

Select the correct option

Reload Math Equations

- | | |
|----------------------------------|------|
| <input type="radio"/> | XNOR |
| <input type="radio"/> | OR |
| <input type="radio"/> | AND |
| <input checked="" type="radio"/> | NOR |

Click to Save Answer & Move to Next Question



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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Jazz ZONG         4G   36%  11:43 PM



vulms.vu.edu.pk/Q



BC220213510: ADIL ALVI

Time Left 37 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:41 PM, 13 December 2022

Question # 2 of 10 (Start time: 11:42:42 PM, 13 December 2022)

Total Marks: 1

----- gates are economical and easier to fabricate and are the basic gates used in all IC digital logic families.

Select the correct option

 Reload Math Equations

- | | |
|----------------------------------|-----------|
| <input type="radio"/> | Universal |
| <input type="radio"/> | Boolean |
| <input checked="" type="radio"/> | Logic |
| <input type="radio"/> | Inverse |

 Click to Save Answer & Move to Next Question



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Jazz ZONG

4G 36% 11:44 PM



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BC220213510: ADIL ALVI

Time Left 56 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:41 PM, 13 December 2022

Question # 3 of 10 (Start time: 11:43:49 PM, 13 December 2022)

Total Marks: 1

The ____ gate works opposite to the OR gate. Its Boolean algebra representation is: $C=(A+B)$

Select the correct option

Reload Math Equations

- | | |
|----------------------------------|------|
| <input type="radio"/> | NOT |
| <input checked="" type="radio"/> | NOR |
| <input type="radio"/> | XOR |
| <input type="radio"/> | NAND |

Click to Save Answer & Move to Next Question

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Jazz ZONG

4G 36% 11:44 PM



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BC220213510: ADIL ALVI

Time Left 83 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:41 PM, 13 December 2022

Question # 4 of 10 (Start time: 11:44:30 PM, 13 December 2022)

Total Marks: 1

The Boolean function $Z = A + B \cdot C'$ can be implemented in digital logic circuit by connecting _____ gates together.

Select the correct option

Reload Math Equations

- 3
- 2
- 4
- 1

Click to Save Answer & Move to Next Question

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Jazz ZONG           35%  11:46 PM



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BC220213510: ADIL ALVI

Time Left 86 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:41 PM, 13 December 2022

Question # 6 of 10 (Start time: 11:46:37 PM, 13 December 2022)

Total Marks: 1

Which version of Electronics Workbench is being used in this course for creating and simulating digital circuits?

Select the correct option

 Reload Math Equations

- version 5.12
- version 6
- version 10
- version 12.5

Click to Save Answer & Move to Next Question



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Jazz ZONG         4G  11:49 PM 35% 



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BC220213510: ADIL ALVI

Time Left 41 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:41 PM, 13 December 2022

Question # 8 of 10 (Start time: 11:48:23 PM, 13 December 2022)

Total Marks: 1

According to the distributive law of Boolean algebra, which of the following statements is TRUE?

Select the correct option

 Reload Math Equations

$X + (Y Z) = X + Y + Z$

$X + (Y Z) = XYZ$

$X + (Y Z) = (X + Y) (X + Z)$

$X + (Y Z) = XY + XZ$

 Click to Save Answer & Move to Next Question



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Jazz ZONG         4G   35%  11:49 PM



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BC220213510: ADIL ALVI

Time Left 85 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:41 PM, 13 December 2022

Question # 9 of 10 (Start time: 11:49:21 PM, 13 December 2022)

Total Marks:

A K-Map of size 2x2 can be used to map a _____ input boolean expression.

Select the correct option

 Reload Math Equations

- | | |
|----------------------------------|---|
| <input type="radio"/> | 1 |
| <input type="radio"/> | 2 |
| <input type="radio"/> | 3 |
| <input checked="" type="radio"/> | 4 |

Click to Save Answer & Move to Next Question

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Jazz ZONG

4G 11:52 PM 34%



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BC220213511: WAJID ALVI

Time Left 19 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:50 PM, 13 December 2022

Question # 1 of 10 (Start time: 11:50:50 PM, 13 December 2022)

Total Marks: 1

----- gate with a bubble at the output is know as NAND gate.

Select the correct option

Reload Math Equations

- OR
- NOT
- NOR
- AND



Click to Save Answer & Move to Next Question

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Jazz ZONG

4G 33% 11:54 PM



vulms.vu.edu.pk/Q



BC220213511: WAJID ALVI

Time Left 19 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:50 PM, 13 December 2022

Question # 3 of 10 (Start time: 11:53:25 PM, 13 December 2022)

Total Marks: 1

Which of the following is not a valid number for grouping of terms in K-Map?

Select the correct option

Reload Math Equations

- 1
- 2
- 4
- 3

Click to Save Answer & Move to Next Question

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Jaz7
Z

Ringtone

BC22

Time Left: 01 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:50 PM, 13 December 2022

Question # 6 of 10 (Start time: 11:55:33 PM, 13 December 2022)

Total Marks: 1

_____ is known as a universal gate.

Select the correct option

Reload Math Equations

OR

NAND

XNOR

AND

Click to Save Answer & Move to Next Question

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Jazz ZONG

4G 31% 11:59 PM



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BC220213511: WAJID ALVI

Time Left 60 sec(s)

CS302P - Digital Logic Design (Practical) (Semester Quiz # 01 - (Graded))

Quiz Start Time: 11:50 PM, 13 December 2022

Question # 10 of 10 (Start time: 11:58:58 PM, 13 December 2022)

Total Marks: 1

Click the _____ to turn power on. Click switch again to turn power off.

Select the correct option

Reload Math Equations

- Circuit Switch
- Power Switch
- Control Switch
- Simulation Switch

Click to Save Answer & Move to Next Question



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Jazz
ZONG

4G 73% 8:30 PM

BC220213510: ADIL ALVI

Time Left
83
sec(s)

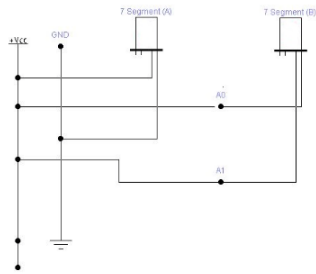
CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 5 [8-10] - (Not Graded))

Quiz Start Time: 08:30 PM, 20 January 2023

Question # 1 of 5 (Start time: 08:30:10 PM, 20 January 2023)

Total Marks: 1

Consider the below given EWB circuit, what values will be displayed on 7-segment displays A and B when the circuit simulation is activated in EWB?



Select the correct option

Reload Math Equations

- 0, 1
- 2, 3
- 1, 3
- 3, 3

Click to Save Answer & Move to Next Question

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Jazz ZONG 4G 28% 11:46 PM

Quiz vulms.vu.edu.pk

BC220213511: WAJID ALVI Time Left 55 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 6 [10 - 11] - (Not Graded)) Quiz Start Time: 11:46 PM, 27 January 2023

Question # 1 of 5 (Start time: 11:46:21 PM, 27 January 2023) Total Marks: 1


To create a Johnson Counter by using D-flip flops, how flip flops should be connected together?

Select the correct option

Reload Math Equations

- Q' output of previous flip flop should be connected to clock input of second flip flop
- Q output of previous flip flop should be connected to D input of second flip flop
- Q output of previous flip flop should be connected to clock input of second flip flop
- Q' output of previous flip flop should be connected to D input of second flip flop

saving...



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The screenshot shows a mobile application interface for a quiz. At the top, the status bar displays 'Jazz ZONG', signal strength, 4G LTE, 28% battery, and the time 11:47 PM. Below the status bar, there is a navigation bar with a close icon (X), a lock icon, the text 'Quiz vulms.vu.edu.pk', a share icon, a bookmark icon, and a menu icon (three dots). The main content area shows the quiz title 'CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 6 [10 - 11] - (Not Graded))' and 'Question # 5 of 5 (Start time: 11:47:22 PM, 27 January 2023)'. The question text is 'How many flop flops are required to create a counter circuit which can count from 0 to 7?'. Below the question, there is a section titled 'Select the correct option' with four radio button options: 2, 1, 3, and 4. The option '4' is selected. A 'Click to Save' button is visible at the bottom right. The bottom of the screen shows the Android navigation bar with back, home, and recent apps icons.

Jazz ZONG 4G LTE 28% 11:47 PM

Quiz vulms.vu.edu.pk

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 6 [10 - 11] - (Not Graded)) Quiz S

Question # 5 of 5 (Start time: 11:47:22 PM, 27 January 2023)

How many flop flops are required to create a counter circuit which can count from 0 to 7?

Select the correct option

2

1

3

4

Click to Save

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CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded))

Qui

Question # 1 of 5 (Start time: subscribe - 1M, 06 February 2023)

A circuit that compares two numbers and determines their magnitude is called _____

Select the correct option

- Magnitude comparator
- Inverter
- Height comparator
- Size comparator

Click

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded))

Qui

Question # 2 of 5 (Start time: subscribe + 1M, 06 February 2023)

In a comparator, if we get input as $A > B$ then the output will be _____

Select the correct option

- 1
- B
- A
- 0

Click

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CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded))

Qui

Question # 3 of 5 (Start time: subscribe - AM, 06 February 2023)

TTL 74LS85 is a _____

Select the correct option

- 1-bit digital comparator
- 8-bit magnitude comparator
- 4-bit magnitude comparator
- 8-bit word comparator

Click

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded))

Qui

Question # 4 of 5 (Start time: subscribe - AM, 06 February 2023)

An identify comparator is defined as a digital comparator which has _____

Select the correct option

- No output terminals
- Only one output terminal
- Three output terminals
- Two output terminals

Click

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CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded))

Qui

Question # 5 of 5 (Start time:  - AM, 06 February 2023)

Comparators are used in _____

Select the correct option

- CPU
- Hard Drive
- Motherboard
- Memory

Click

Question # 1 of 5 (Start time:  +  99K 2023)

Convert the binary number 1100 to Gray code.

Select the correct option

- 0101
- 0111
- 1000
- 1010

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Question # 2 of 5 (Start time FOLLOW + 27K 2023)

How many flop flops are required to create a counter circuit which can count from 0 to 7?

Select the correct option

- 4
- 1
- 3
- 2

Question # 5 of 5 (Start time FOLLOW + 30K 2023)

Convert the Gray code 1011 to binary.

Select the correct option

- 1010
- 0111
- 1011
- 1101

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CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded)) Time Left sec(s)

Quiz Start Time: 03:39 PM, 06 February 2023

Question # 1 of 5 (Start time: 03:39:32 PM, 06 February 2023) Total Marks: 1

Which one is a basic comparator?

Select the correct option Reload Math Equations

- XOR
- OR
- NAND
- AND

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded)) Time Left sec(s)

Quiz Start Time: 03:39 PM, 06 February 2023

Question # 4 of 5 (Start time: 03:41:57 PM, 06 February 2023) Total Marks: 1

In a comparator, if we get input as $A > B$ then the output will be _____

Select the correct option Reload Math Equations

- B
- A
- 0
- 1

Click to Save Answer & Move to Next Question

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CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded)) sec(s)

Quiz Start Time: 03:39 PM, 06 February 2023

Question # 5 of 5 (Start time: 03:42:35 PM, 06 February 2023) Total Marks: 1

An identify comparator is defined as a digital comparator which has -----

Select the correct option Reload Math Equations

- Three output terminals
- Two output terminals
- Only one output terminal
- No output terminals

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Jazz ZONG 4G 31% 9:03 PM

Quiz <https://vulms.vu.edu.pk>

BC220213510: ADIL ALVI

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 9 [13 - 15] - (Not Graded))

Question # 5 of 5 (Start time: 09:02:53 PM, 10 February 2023)

An identify comparator is defined as a digital comparator which has _____

Select the correct option

<input type="radio"/>	Three output terminals
<input type="radio"/>	No output terminals
<input type="radio"/>	Two output terminals
<input checked="" type="radio"/>	Only one output terminal

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Quiz | ChatGPT: Optimizing Language | Row Major Order Traversal | Comparators are used in _____

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=782e680b-74ca-4924-8fab-867bfbee811e

To get future Google Chrome updates, you'll need Windows 10 or later. This computer is using Windows 8.1. [Learn more](#)

BC220213511: WAJID ALVI Time Left 71 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded)) Quiz Start Time: 11:40 PM, 06 February 2023

Question # 1 of 5 (Start time: 11:40:56 PM, 06 February 2023) Total Marks: 1

Comparators are used in _____

Select the correct option Reload Math Equations

- Motherboard
- Memory
- CPU
- Hard Drive

Quiz | ChatGPT: Optimizing Language | Comparator Circuit Definition | Which one is a basic comparator

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=4aa09153-a6b7-415b-9e8d-c8a69f3aa033

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BC220213510: ADIL ALVI Time Left 62 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded)) Quiz Start Time: 11:23 PM, 06 February 2023

Question # 5 of 5 (Start time: 11:31:30 PM, 06 February 2023) Total Marks: 1

Which one is a basic comparator?

Select the correct option Reload Math Equations

- XOR
- AND
- NAND
- OR

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BC220213510: ADIL ALVI

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded))

Quiz Start Time: 11:23 PM, 06 February 2023

Time Left 34 sec(s)

Question # 4 of 5 (Start time: 11:26:00 PM, 06 February 2023) Total Marks: 1

An identify comparator is defined as a digital comparator which has _____

Select the correct option

Reload Math Equations

- No output terminals
- Three output terminals
- Two output terminals
- Only one output terminal

BC220213510: ADIL ALVI

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8[13 - 14] - (Not Graded))

Quiz Start Time: 11:23 PM, 06 February 2023

Time Left 75 sec(s)

Question # 3 of 5 (Start time: 11:25:19 PM, 06 February 2023) Total Marks: 1

In a comparator, if we get input as $A > B$ then the output will be _____

Select the correct option

Reload Math Equations

- 0
- A
- 1
- B

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

Quiz

ChatGPT: Optimizing Language

Comparator Circuit Definition

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=b77f0c2e-6eef-4585-bd98-f55b7975408c

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CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 8 [13 - 14] - (Not Graded))

Quiz Start Time: 11:23 PM, 06 February 2023

Question # 1 of 5 (Start time: 11:24:00 PM, 06 February 2023)

Total Marks: 1

A circuit that compares two numbers and determines their magnitude is called -----

Select the correct option

Reload Math Equations

Height comparator

Inverter

Magnitude comparator

Size comparator

Click to Save Answer & Move to Next Question

ChatGPT: Optimizing Language

Adil Definition Request

Quiz

The parallel outputs of a counter

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=953fbe9e-490c-4997-8ba4-d15488849114

BC220213510: ADIL ALVI

Time Left 62 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 7 [10 - 12] - (Not Graded))

Quiz Start Time: 11:32 PM, 03 February 2023

Question # 5 of 5 (Start time: 11:35:34 PM, 03 February 2023)

Total Marks: 1

The parallel outputs of a counter circuit represent the -----

Select the correct option

Reload Math Equations

Counter modulus

Parallel data word

Clock frequency

Clock count

For any query feel free to ask princealvi9222@gmail.com

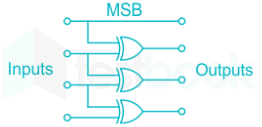
CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

ChatGPT: Optimizing Language | x | Adil Definition Request | x | Quiz | x | gray diagram - Google Search | x | +

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=f936d9b0-f937-41dc-bbfc-313ebe1a1dc3

Question # 4 of 5 (Start time: 11:34:34 PM, 03 February 2023) Total Marks: 1

Following diagram is of _____ code conversion.



Select the correct option Reload Math Equations

<input type="radio"/>	BCD
<input type="radio"/>	Excess-3
<input checked="" type="radio"/>	Gray
<input type="radio"/>	ASCII

Windows | File Explorer | Excel | PDF | Chrome | Word | Edge | Task View | Settings | Network | Volume | ENG 10:35 AM 2/3/2023

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=1701c6ea-312c-4739-85bd-4ca8ed78eec0

BC220213510: ADIL ALVI Time Left 74 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 7 [10 - 12] - (Not Graded)) Quiz Start Time: 11:32 PM, 03 February 2023

Question # 3 of 5 (Start time: 11:33:50 PM, 03 February 2023) Total Marks: 1

Ripple counters are also called _____

Select the correct option Reload Math Equations

<input type="radio"/>	VLSI counters
<input checked="" type="radio"/>	Asynchronous counters
<input type="radio"/>	SSI counters
<input type="radio"/>	Synchronous counters

Windows | File Explorer | Excel | PDF | Chrome | Word | Edge | Task View | Settings | Network | Volume | ENG 10:34 AM 2/3/2023

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

ChatGPT: Optimizing Language | x Adil Definition Request | x Quiz | x Reflected Binary Code is also kno... | +

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=3ebd7ce5-aae8-43cb-9dac-9fbb370ee0b2

BC220213510: ADIL ALVI

Time Left 70 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 7 [10 - 12] - (Not Graded))

Quiz Start Time: 11:32 PM, 03 February 2023

Question # 2 of 5 (Start time: 11:33:06 PM, 03 February 2023)

Total Marks: 1

What is the maximum possible range of bit-count specifically in n-bit binary counter consisting of 'n' number of flip-flops?

Select the correct option

Reload Math Equations

- 0 to $2^n + 1$
- 0 to 2^n
- 0 to $2^n - 1$
- 0 to 2^{n+1}

ChatGPT: Optimizing Language | x Adil Definition Request | x Quiz | x Reflected Binary Code is also kno... | +

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=394d72fd-6449-40a1-ba0f-63cb30ff0c0

BC220213510: ADIL ALVI

Time Left 70 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 7 [10 - 12] - (Not Graded))

Quiz Start Time: 11:32 PM, 03 February 2023

Question # 1 of 5 (Start time: 11:32:11 PM, 03 February 2023)

Total Marks: 1

Reflected Binary Code is also known as _____ code.

Select the correct option

Reload Math Equations

- ASCII
- Gray Code
- BCD
- Excess-3

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

ChatGPT: Optimizing Language | Adil Definition Request | Quiz | Convert the binary number 1100

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=cd197d0e-ccb9-4ef7-9058-cb6a54455618

BC220213511: WAJID ALVI

Time Left 12 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 7 [10 - 12] - (Not Graded))

Quiz Start Time: 11:19 PM, 03 February 2023

Question # 5 of 5 (Start time: 11:24:49 PM, 03 February 2023) Total Marks: 1

Convert the binary number 1100 to Gray code.

Select the correct option

Reload Math Equations

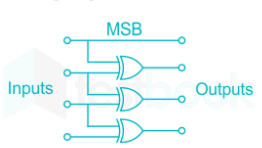
- 1010
- 0101
- 1000
- 0111

ChatGPT: Optimizing Language | Adil Definition Request | Quiz | To create a Johnson Counter by

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=ce514898-f531-4e3e-82a5-4a3729b8022f

Question # 4 of 5 (Start time: 11:23:38 PM, 03 February 2023) Total Marks: 1

Following diagram is of _____ code conversion.



Select the correct option

Reload Math Equations

- ASCII
- Excess-3
- Gray
- BCD

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

ChatGPT: Optimizing Language | Adil Definition Request | Quiz

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=e85d5378-04a3-41f1-aa65-65038254d293

BC220213511: WAJID ALVI

Time Left 37 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 7 [10 - 12] - (Not Graded))

Quiz Start Time: 11:19 PM, 03 February 2023

Question # 1 of 5 (Start time: 11:19:14 PM, 03 February 2023) Total Marks: 1

Convert the Gray code 1011 to binary.

Select the correct option

Reload Math Equations

- 0111
- 1010
- 1011
- 1101

ChatGPT: Optimizing Language | Memory Allocation In C/C++ | Quiz | Student Survey Form

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=9346a4b8-ea77-4413-9d9a-85f67d11f4d4

BC220213510: ADIL ALVI

Time Left 70 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 6 [10 - 11] - (Not Graded))

Quiz Start Time: 11:33 PM, 27 January 2023

Question # 1 of 5 (Start time: 11:33:17 PM, 27 January 2023) Total Marks: 1

Ripple counters are also called _____

Select the correct option

Reload Math Equations

- VLSI counters
- SSI counters
- Asynchronous counters
- Synchronous counters

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

ChatGPT: Optimizing Language | Memory Allocation In C/C++ | Quiz | Student Survey Form

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=8fdacfb-43b2-4e0c-8657-bbaf8345ac0a

BC220213510: ADIL ALVI Time Left 22 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 6 [10 - 11] - (Not Graded)) Quiz Start Time: 11:33 PM, 27 January 2023

Question # 2 of 5 (Start time: 11:34:26 PM, 27 January 2023) Total Marks: 1

Three decade counter would have -----

Select the correct option Reload Math Equations

- 4 BCD counters
- 2 BCD counters
- 3 BCD counters
- 5 BCD counters

ChatGPT: Optimizing Language | Memory Allocation In C/C++ | Quiz | Student Survey Form

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=c07d487e-265b-418b-9e1a-1e7401a9623e

BC220213510: ADIL ALVI Time Left 66 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 6 [10 - 11] - (Not Graded)) Quiz Start Time: 11:33 PM, 27 January 2023

Question # 3 of 5 (Start time: 11:37:27 PM, 27 January 2023) Total Marks: 1

What is the maximum possible range of bit-count specifically in n-bit binary counter consisting of 'n' number of flip-flops?

Select the correct option Reload Math Equations

- 0 to $2^n + 1$
- 0 to 2^{n+1}
- 0 to $2^n - 1$
- 0 to 2^n

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CS302P ALL QUIZZES 2023 SOLVED BY PRINCE ALVI

ChatGPT: Optimizing Language | Memory Allocation In C/C++ | Quiz | Student Survey Form

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=cf00f06d-b394-4cc2-a8c1-24059d27f245

BC220213510: ADIL ALVI

Time Left 68 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 6 [10 - 11] - (Not Graded))

Quiz Start Time: 11:33 PM, 27 January 2023

Question # 4 of 5 (Start time: 11:38:42 PM, 27 January 2023)

Total Marks: 1

A counter circuit is usually constructed of _____

Select the correct option

Reload Math Equations

- A number of NAND gates connected in cascade form
- A number of flip-flops connected in cascade
- A number of NOR gates connected in cascade form
- A number of latches connected in cascade form

ChatGPT: Optimizing Language | Memory Allocation In C/C++ | Quiz | Student Survey Form

vulms.vu.edu.pk/Quiz/QuizQuestion.aspx?ver=91ee4504-2d85-4553-aa3a-a4b4f18c0be4

BC220213510: ADIL ALVI

Time Left 51 sec(s)

CS302P - Digital Logic Design (Practical) (Lab Attendance Quiz # 6 [10 - 11] - (Not Graded))

Quiz Start Time: 11:33 PM, 27 January 2023

Question # 5 of 5 (Start time: 11:39:42 PM, 27 January 2023)

Total Marks: 1

The parallel outputs of a counter circuit represent the _____

Select the correct option

Reload Math Equations

- Parallel data word
- Clock count
- Counter modulus
- Clock frequency

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