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CORRECT ANSWER SOLVED BY HADI
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(Final Term Past Paper)

MADE AND SOLVED BY TEAM HADI

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



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Best of luck!



Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In an indirect addressing mode the address of memory cell

Answer (Please select your correct option)

- is placed in a register
- is directly accessed by instruction
- is constant
- is variable



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In an indirect addressing mode the address of memory cell



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Answer (Please select your correct option)

- is placed in a register
- is directly accessed by instruction
- is constant
- is variable

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In STOSB instruction, when DF is clear, SI is

Answer / Choose your correct option)

Incremented by 1

Incremented by 2

Decrement by 1

Decrement by 2



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IBM AT has how many PICs (Programmable interrupt controller)

Answer (Please select your correct option)

1

2

3

4



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0
3
0
8
7
1
2
9
2
2

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Processor informs the memory that it wants to read data, using :

Answer (Please select your correct option)

- Address bus
- Data bus
- Control bus
- It informs without using any bus



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Memory cell width refers to _____

Answer (Please select your correct option)

- Number of bits in a cell
- The maximum value that can be stored in a cell including sign-bit
- The maximum value that can be stored in a cell excluding sign-bit
- Address of that cell



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In MUL instruction if the source operand is a byte then it is multiplied by register

Answer (Please select your correct option)

AX

AL

CX

DL



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A 32 bit processor has an accumulator of _____

Answer (Please select your correct option)

8 bits

16 bits

32 bits

64 bits



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The parallel port connector is called _____.

Answer (Please select your correct option)

DB-25

BD-25

DB-24

BD-24



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The Single step interrupt is _____.

Answer (Please select your correct option)

- Hardware interrupt
- Like divide by zero interrupt
- Like divide by 1 interrupt
- Like divide by 2 interrupt



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_____ are not used in mathematical operations.

Answer (Please select your correct option)

Carry, Interrupt and Trap flag

Direction, Interrupt and Trap flag

Direction, Overflow and Trap flag

Direction, Interrupt and Sign flag



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In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

- line
- dot
- circle
- rectangle



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In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

Answer (Please select your correct option)

third

fourth

first

second



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VESA VBE 2.0 is a standard for _____

Answer (Please select your correct option)

Ultra resolution mode

High resolution mode

Medium resolution mode

Low resolution mode



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VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

- Video BIOS Extensions
- Video BIOS Emergence
- Video Binary Extensions
- Video Blocked Extensions



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IDTR is a _____ register.

Answer (Please select your correct option)

16 bit

20 bit

32 bit

48 bit

Correct answer solved



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In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

Programming mode

Real time mode

Visualization mode

Memory mode



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In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

1

2

3

4



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In NASM, an imported symbol is declared with the _____ while and exported symbol is declared with the _____.

Answer (Please select your correct option)

Global directive, External directive

External directive, Global directive

Home Directive, Foreign Directive

Foreign Directive, Home Directive



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In Computer Architecture, RISC stands for _____.

Answer (Please select your correct option)

- Reversed Instruction Set Computer
- Reversed Instruction System Computer
- Reduced Instruction Set Computer
- Reduced Instruction System Computer



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In Computer Architecture, CISC stands for _____.

Answer (Please select your correct option)

- Complex Instruction Set Computer
- Complex Instruction System Computer
- Correct Instruction Set Computer
- Correct Instruction System Computer



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Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

global

local

legal

illegal



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"INT 10 Video - Scroll Up Window" is a type of video services used in which mode?

Answer (Please select your correct option)

Numeric

Graphics

Text

Audio



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The first possible segment base value is _____.

Answer (Please select your correct option)

0000

Correct answer solved by Hadi

0001

0010

0100



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BIOS sees the disks as

Answer (Please select your correct option)

- logical storage device
- raw storage device
- in the form of sectors only
- in the form of tracks only



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In device attribute word, which of the following bit decides whether it is a character device or a block device ?

Answer (Please select your correct option)

Bit 12

Bit 13

Bit 14

Bit 15

Correct answer solv



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In 9 pin DB-9, which pin number is assigned to DSR (Data Set Ready) ?



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0
3
0
8
7
1
2
2
9
2
2

Answer (Please select your correct option)

4

5

6

7

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In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer (Please select your correct option)

5

6

7

8



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0
3
0
8
7
1
2
2
9
2
2

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In 9 pin DB-9, which pin number is assigned to RD (Received Data) ?

Answer (Please select your correct option)

1

2

3

4



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0
8
7
1
2
2
9
2
2

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In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer (Please select your correct option)

1

2

3

4



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3
0
8
7
1
2
2
9
2
2

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Which of the following BIOS INT provides serial port services

Answer (Please select your correct option)

INT14

INT11

INT12

INT13



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A 32 bit address register can access up to of memory, so memory access has increased a lot.

Answer (Please select your correct option)

2GB

4GB

6GB

8GB



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The segment address of interrupt n will be at

Answer (Please select your correct option)

nx4

nx4+1

nx4+2

~~not sure~~

nx4+3



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2

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An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

2

4

8

16

Correct answer solved by



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Which of the following interrupts is Non Maskable Interrupt (NMI).

Answer (Please select your correct option)

INT 2

INT 3

INT 0

INT 1



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9
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Which of the following is a special type of interrupt that returns to the same instruction instead of the next instruction?

Answer (Please select your correct option)

Divide overflow interrupt

Debug interrupt

Arithmetic overflow interrupt

Change of sign interrupt



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In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

19

20

21

22



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Which one of the following interrupts is used for arithmetic overflow?

Answer (Please select your correct option)

INT 4

INT 1

INT 2

INT 3



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For which purpose INT 1 is used and describe the functionality of this interrupt?

Answer (Please [click here](#) to Add Answer)



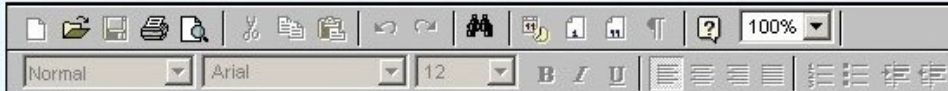
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What is programmer's view of processor?

Answer ([Please click here to Add Answer](#))



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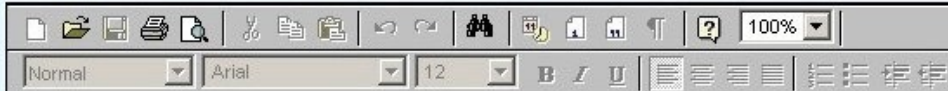
0
3
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8
7
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2
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We have 16 bit (word) information for device attribute. For what specific purpose the fifteenth bit is reserved?

Answer ([Please click here to Add Answer](#))



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Write the complete command to assemble ABC.asm using NASM.

Answer ([Please click here to Add Answer](#))



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What is the range of addressable memory in protected mode and which register is used to reference the descriptor for the Stack segment?

Answer ([Please click here to Add Answer](#))



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How many routines are required to be defined by a device driver and what are their names?

Answer ([Please click here to Add Answer](#))



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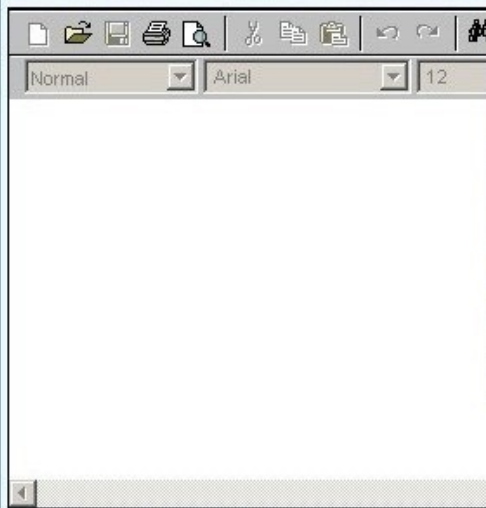
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Suggest a modification in the design of 8088 Processor Architecture such that it can support 16MB RAM?

Hint: It will require increase or decrease in Address bus lines.

Answer ([Please click here to Add Answer](#))



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What are two different modes of video services of BIOS? And differentiate between both of modes.

Answer (Please [click here](#) to Add Answer)



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How many pins are used for serial port communication and which types of communication occur through serial port pins?

Answer (Please [click here](#) to Add Answer)



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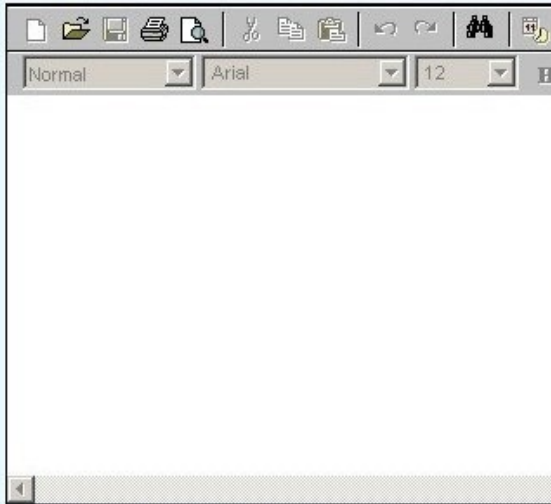
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Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

CLR (A3)
EXG D0, D2
MOVE.B (A1), (A2)
MOVEA (2222).L, A4

Answer ([Please click here to Add Answer](#))



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Write a subroutine to calculate the string length.

Answer ([Please click here to Add Answer](#))



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Explain the purpose of Assembly language instructions from 1 to 4 lines as given below.

1. `mov si, 12`
2. `mov al, [cs:si]`
3. `mov [opcode], al`
4. `mov byte [cs:si], 0xCC`

Answer ([Please click here to Add Answer](#))



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5. 12: inc ax
6. add bx, 2
7. dec cx
8. sub dx, 2
9. jmp 12

Answer (Please [click here](#) to Add Answer)



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