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CORRECT ANSWER SOLVED BY HADI
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(Final Term Past Paper)

MADE AND SOLVED BY TEAM HADI

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



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FACEBOOK ID: <https://www.facebook.com/hadipastpapers/>

Best of luck!



Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSW instruction, when DF is clear, SI is

Answer (Please select your correct option)

Incremented by 1

Incremented by 2

Decrement by 1

Decrement by 2



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Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

IRQ 4

correct

IRQ 5

IRQ 6

IRQ 7



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Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is used by the parallel port?

Answer (Please select your correct option)

IRQ 4

IRQ 5

IRQ 6

IRQ 7

correct



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Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PTI) has input frequency of

Answer (Please select your correct option)

1. 193MHZ

correct

2. 193MHZ

3. 193MHZ

4. 193MHZ



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Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

Key board interrupt

Timer interrupt

correct

INT 2

INT 3



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Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?



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Answer (Please select your correct option)

- Shl AX, 1
- Shr AX, 1
- NOT AX
- AND AX, 0xFFFF

correct

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Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

In the rotate right operation every bit moves one position to the right and the bit dropped from the right is inserted at the left and:

Answer (Please select your correct option)

- The dropped bit is also copied in CF
- The dropped bit is also copied in AF
- The dropped bit is also copied in ZF
- The dropped bit is not copied in any flag

correct



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Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

In string instructions, CX is always

Answer (Please select your correct option)

- Decrement by 1
- Decrement by 2
- Increment by 1
- Increment by 2

correct



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Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

The word size of the processor is defined by the size of _____

Answer (Please select your correct option)

Its accumulator register

Address bus attached

Memory attached

Control Bus



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~~X~~ correct

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Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ cannot predict which element the _____ needs, at a particular instant of time.

Answer (Please select your correct option)

Control bus, Memory

Processor, Control bus
 Processor, Memory

Memory, Processor

correct

Control bus, Memory



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0
3
0
8
7
1
2
2
9
2
2

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Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

The time interval between two timer ticks is _____



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Answer (Please select your correct option)

40ms

45ms

50ms

55ms

correct

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Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

In DOS input buffer, the number of characters actually read on return is stored in _____ byte.



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Answer (Please select your correct option)

third

fourth

first

second

correct

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Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

A thread can not be created _____



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Answer (Please select your correct option)

in static way

correct

in dynamic way

in static and dynamic way at the same time

manually

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Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

control block

Process control block

correct

stack

hard disk



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Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

Variable Electronics Standards Association

Video Electronics Standards Association

correct

Video Energy Standards Association

Vega Energy Standards Association



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Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).



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Answer (Please select your correct option)

Video BIOS Extensions

correct

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

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Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

Programming mode

Real time mode

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Visualization mode

Memory mode



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Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

1

2

3

4

correct



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Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

In Motorola 68K processors, _____ registers can hold addresses in indirect memory accesses.

Answer (Please select your correct option)

A0-A7

correct

B0-B7

C0-C7

D0-D7



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Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

Motorola 68K processors have _____ 32 bit general purpose registers.



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Answer (Please select your correct option)

32

64

8

16

correct

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Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

First Byte

Second Byte

correct

Third Byte

Last Byte



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Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?



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Answer (Please select your correct option)

global

local

correct

legal

illegal

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Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking programming, all threads are



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Answer (Please select your correct option)

Independent to each other

correct

Dependent on each other

Not executed

Not scheduled

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Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Video services are exported via



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0
3
0
8
7
1
2
2
9
2
2

Answer (Please select your correct option)

INT 7

INT 10

correct

INT 9

INT 8

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Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

The maximum memory, IAPX88 can access is _____.

Answer (Please select your correct option)

1 KB

64 KB

1 MB

correct

64 MB



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Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In direct addressing the memory address is _____ and is given in the instruction.



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Answer (Please select your correct option)

Fixed

correct

Empty

A variable

A register

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Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Instruction "mov ax, bl" has a _____ problem.



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Answer (Please select your correct option)

size mismatch

correct

value mismatch

operand mismatch

memory mismatch

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Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

The jump "JC" is taken when



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Answer (Please select your correct option)

CF=1

correct

CF=0

ZF=0

ZF=1

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Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

DOS

RAM

ROM

BIOS



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Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a -----



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Answer (Please select your correct option)

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

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Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?



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0
3
0
8

VU

7
1
2
2

PAST

9
2
2

Answer (Please select your correct option)

5

6

7

correct

8



HADI

0
3
0
8

VU

7
1
2
2

PAST

9
2
2

PAPER

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Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

6

7

8

9

correct



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Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____

Answer (Please select your correct option)

6

7

8

9



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Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

- 16 bit processor
- 32 bit processor
- 64 bit processor
- 128 bit processor



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Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

AX register in extended 32 bit register is renamed as _____

Answer (Please select your correct option)

AXE

EAX

AAX

XAX



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Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

1

0

-1

-2



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Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

8 bit

16 bit

32 bit

64 bit



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Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

- 2
- 4
- 8
- 16



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Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

INT 0

INT 1

INT 2

INT 3



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Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

19

20

21

22



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0
3
0
8
7
1
2
2
9
2
2

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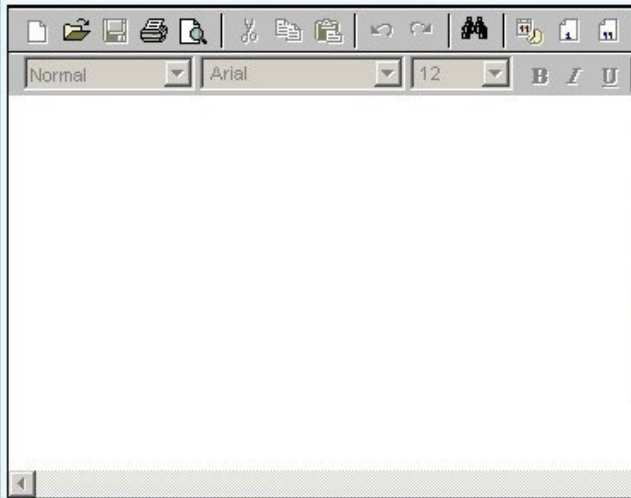
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Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

Device drivers are operating system extensions which extend its services to new devices. Describe the structure of "Device Driver File" which is used for device management.

Answer (Please [click here](#) to Add Answer)



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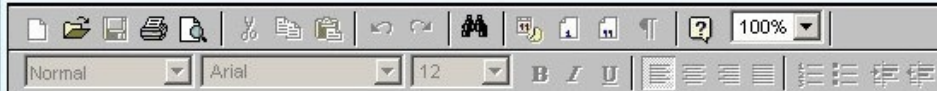
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Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

With reference to branching technique used in assembly language programming, write any two control instructions.

Answer (Please [click here](#) to Add Answer)



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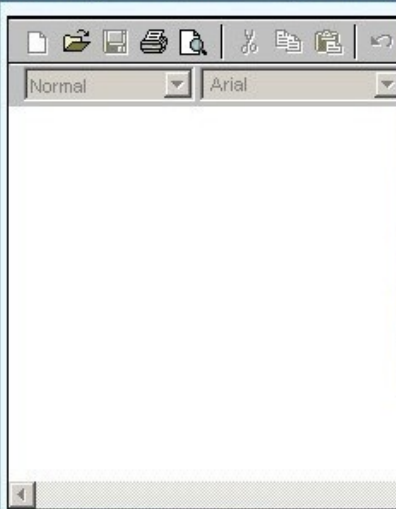
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Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

How local thread variables are created and accessed ?

Answer (Please [click here](#) to Add Answer)



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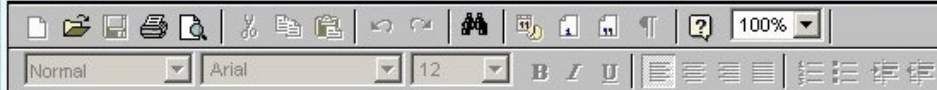
Question No : 44 of 52

Marks: 2 (Budgeted Time 4 Min)

What are the following statements represent?

gdt: dd 0x00000000, 0x00000000

Answer (Please [click here](#) to Add Answer)



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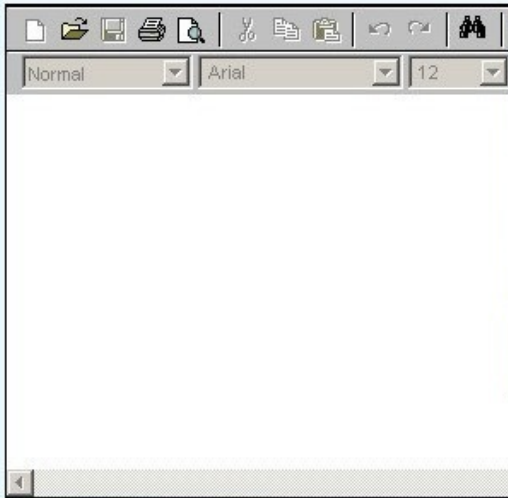
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Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

What are the important "command codes" in request header of device driver?

Answer (Please [click here](#) to Add Answer)



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Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

In order to access Video memory in high resolution (more than a megabyte MB), what standard we have to use?



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Answer (Please [click here](#) to Add Answer)

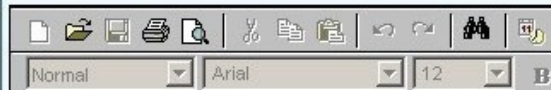
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Describe "Indexed Register Indirect + Offset" addressing mode with an example.

Answer (Please [click here](#) to Add Answer)



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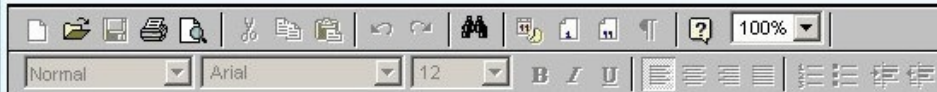
Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

With reference to the multitasking program "TSR caller". Describe what task will be performed by each of these instructions?

- `mov al, [chars+bx]`
- `mov [es:40], al`
- `inc bx`

Answer ([Please click here to Add Answer](#))



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How many types of interrupt are used for writing debugger and also elaborate functionality of these interrupts with proper explanation?

Answer (Please [click here](#) to Add Answer)



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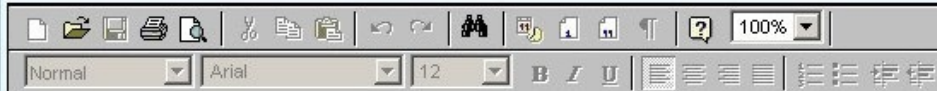
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Question No : 50 of 52

Marks: 5 (Budgeted Time 10 Min)

What are the contents at the starting position of the file designated as device driver and mention important attributes of this driver file?

Answer (Please [click here](#) to Add Answer)



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Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

At which location interrupt gate descriptor table is stored and describe the structure of this table with an example?

Answer (Please [click here](#) to Add Answer)



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0
3
0
8
7
1
2
2
9
2
2



HADI
VU
PAST
PAPER

0
3
0
8
7
1
2
2
9
2
2

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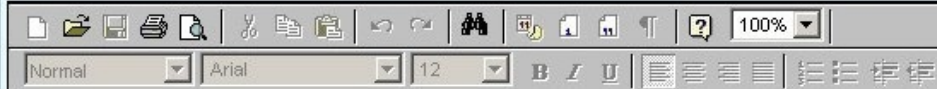
Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

Give the new values of AX and DX for each of the following instructions, or write if the overflow occurs.

- 1. DIV BX, if DX contains 0000h, AX contains 0007h, and BX contains 0002h
- a. DIV BX, if DX contains 0000h, AX contains FFFFh, and BX contains 0010h

Answer (Please [click here](#) to Add Answer)



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0
3
0
8
7
1
2
2
9
2
2

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