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CORRECT ANSWER SOLVED BY HADI
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(Final Term Past Paper)

MADE AND SOLVED BY TEAM HADI

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



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Best of luck!



Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

The operand of PUSH is called

Answer (Please select your correct option)

Source operand

Destination operand

Stack operand

Data operand



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The operand of PUSH is called



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Answer (Please select your correct option)

Source operand

Destination operand

Stack operand

Data operand

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The clear screen operation initializes whole block of video memory to

Answer (Please select your correct option)

0417

0741

0714

0174



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Which of the following IRQs is connected to serial port COM 2?



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Answer (Please select your correct option)

IRQ 0



IRQ 1



IRQ 2



IRQ 3



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Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

IRQ 4

IRQ 5

IRQ 6

IRQ 7



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Which of the following IRQs is used for sound card or network card?



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Answer (Please select your correct option)

IRQ 4



IRQ 5



IRQ 6



IRQ 7



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The programmable interval timer (PIT) has input frequency of



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Answer (Please select your correct option)

1. 193MHZ



2. 193MHZ

3. 193MHZ

4. 193MHZ

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By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?



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Answer (Please select your correct option)

Shl AX, 1



Shr AX, 1



NOT AX



AND AX, 0xFFFF



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8088 processor divides interrupts into ____ classes.



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Answer (Please select your correct option)

2

3

4

5

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In A4FB:4872 Segment:offset pair the physical address is (both segment and offset are in hexadecimal):



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Answer (Please select your correct option)

A4FB0

04872

A9822

A4872

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When the instruction "CMP AX,BX" executes and after execution ZF is set, it indicates that

Answer (Please select your correct option)

- AX is greater
- BX is greater
- Both AX and BX are equal
- ZF has no relation with CMP



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Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

Code Segment register

Data Segment register

Accumulator register

Stack segment register



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Stack is a data structure which behaves as

first in last out manner 59



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Answer (Please select your correct option)

First in first out manner

Last in first out manner

All in no out manner

Last in last out manner

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Segment wraparound occurs when during the effective address calculation _____ is generated.

Answer (Please select your correct option)

carry

error

zero

parity



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The bits of _____ register work independently. If combined all these bits then they are meaningless.

Answer (Please select your correct option)

Accumulator (AX)

Instruction Pointer (IP)

Flag

Base



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IBM PC has separate memory address space and peripheral address space. Memory address space is selected when _____ instruction is given to the processor.

Answer (Please select your correct option)

MOV

DEC

IN

ADD



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INT 10 is used for _____ services.



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Answer (Please select your correct option)

BIOS video

DOS video

RAM

Disk

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_____ are not used in mathematical operations.

Answer (Please select your correct option)

Carry, Interrupt and Trap flag

Direction, Interrupt and Trap flag

Direction, Overflow and Trap flag

Direction, Interrupt and Sign flag



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VESA stands for _____



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Answer (Please select your correct option)

- Variable Electronics Standards Association
- Video Electronics Standards Association
- Video Energy Standards Association
- Vega Energy Standards Association

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In Computer Architecture, the Protected mode is different from _____



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Answer (Please select your correct option)

Programming mode

Real time mode

not sure

Visualization mode

Memory mode

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In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

1

2

3

4



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In Computer Architecture, RISC stands for _____



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Answer (Please select your correct option)

- Reversed Instruction Set Computer
- Reversed Instruction System Computer
- Reduced Instruction Set Computer
- Reduced Instruction System Computer

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In Computer Architecture, CISC stands for _____



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Answer (Please select your correct option)

Complex Instruction Set Computer

Complex Instruction System Computer

Correct Instruction Set Computer

Correct Instruction System Computer

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Which byte of the DOS input buffer holds the number of characters actually read on return?



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Answer (Please select your correct option)

First Byte

Second Byte

Third Byte

Last Byte

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In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

INT 10

INT 09

INT 08

INT 07



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The thread registration code initializes the PCB and adds it to the linked list. so that which will give it a turn?

Answer (Please select your correct option)

scheduler

linker

debugger

assembler



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In multi-tasking programming, all threads are



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Answer (Please select your correct option)

Independent to each other

Dependent on each other

Not executed

Not scheduled

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"INT 10 Video - Scroll Up Window" is a type of video services used in which mode?

Answer (Please select your correct option)

Graphics

Text

Audio

Numeric



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BIOS sees the disks as

Answer (Please select your correct option)

- logical storage device
- raw storage device
- in the form of sectors only
- in the form of tracks only



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Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

DOS

RAM

ROM

BIOS



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In device attribute word, which of the following bit decides whether it is a character device or a block device ?

Answer (Please select your correct option)

Bit 12

Bit 13

Bit 14

Bit 15



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The serial port connection is a ----- connector

Answer (Please select your correct option)

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5



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In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer (Please select your correct option)

- 1
- 2
- 3
- 4



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In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____.

Answer (Please select your correct option)

7

8

9

6



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8088 is a

Answer (Please select your correct option)

16 bit processor

32 bit processor

64 bit processor

128 bit processor



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The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

1

0

-1

-2



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In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

8 bit

16 bit

32 bit

64 bit



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In 68K processors there is a 32 bit that holds the address of currently executing instruction.

Answer (Please select your correct option)

Program counter

Stack pointer

Register

Stack



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An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

2

4

8

16



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Which of the following interrupts is Non Maskable Interrupt (NMI).



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Answer (Please select your correct option)

INT 2

INT 3

INT 0

INT 1

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_____ number of pin(s) of a processor is/are used by the external hardware to generate an interrupt.



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Answer (Please select your correct option)

1

2

3

4

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If num1 is 0005 and the address of num1 is 0117 then after execution of instruction "mov ax, [num1]", what will be contents of AX register ?

Answer ([Please click here to Add Answer](#))



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Following are three common video services for INT 10 used in text mode. Identify the use of each video service which is listed below.

0. AH =00h
1. AH =01h
2. Ah =02h

Answer ([Please click here to Add Answer](#))



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What is the format of input buffer of DOS?

Answer ([Please click here to Add Answer](#))



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In assembly language programming, which information is represented by descriptor's first 16-bits?

Answer ([Please click here to Add Answer](#))



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Which two instructions are used to check whether particular bits are set or not? How these two instructions differ from each other?

Answer ([Please click here to Add Answer](#))



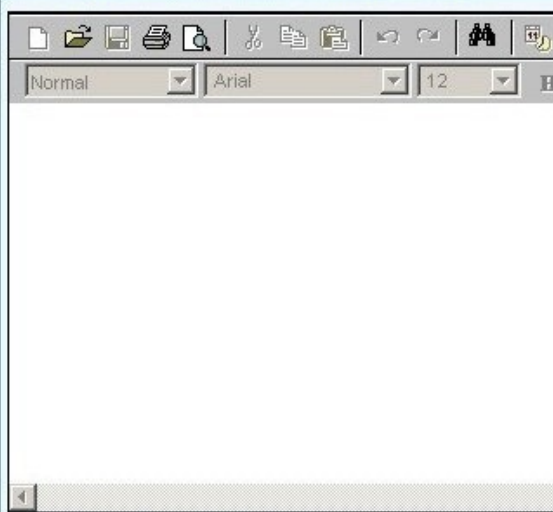
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In case of branching techniques used in Intel's architecture, which instruction is used to provide temporary diversion and how?

Answer ([Please click here to Add Answer](#))



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0
3
0
8
7
1
2
2
9
2
2

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Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

CLR (A3)
EXG D0, D2
MOVE.B (A1), (A2)
MOVEA (2222).L, A4

Answer ([Please click here to Add Answer](#))



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What are attributes of charged video service for getting information about font of the text?

Answer ([Please click here to Add Answer](#))



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In BIOS video services, which specific services in text mode are invoked by the following values of AH register?

- AH = 01
- AH = 02
- AH = 06
- AH = 07
- AH = 09



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