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Past Papers by Waqar Siddhu  
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The operand of PUSH is called

Answer ( Please select your correct option )

**VuAnswers.com**

Source operand

Destination operand

Stack operand

Data operand

**Made by: Waqar Siddhu**

The clear screen operation initializes whole block of video memory to

Answer ( Please select your correct option )

**VuAnswers.com**

0417

0741

0714

0174

**Made by: Waqar Siddhu**

Which of the following IRQs is connected to serial port COM 2?

Answer ( Please select your correct option )

IRQ 0

IRQ 1

IRQ 2

IRQ 3

**VuAnswers.com**

**Made by: Waqar Siddhu**

Which of the following IRQs is connected to serial port COM 1?

Answer ( Please select your correct option )

IRQ 4

IRQ 5

IRQ 6

IRQ 7

**VuAnswers.com**

**Made by: Waqar Siddhu**

Which of the following IRQs is used for sound card or network card?

Answer ( Please select your correct option )

IRQ 4

IRQ 5

IRQ 6

IRQ 7

**VuAnswers.com**

**Made by: Waqar Siddhu**

The programmable interval timer (PIT) has input frequency of

Answer ( Please select your correct option )

1.193MHZ

2.193MHZ

3.193MHZ

4.193MHZ

**VuAnswers.com**

**Made by: Waqar Siddhu**

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer ( Please select your correct option )

Shl AX, 1

Shr AX, 1

NOT AX

AND AX, 0xFFFF

**VuAnswers.com**

**Made by: Waqar Siddhu**

8088 processor divides interrupts into \_\_\_\_ classes.

Answer ( Please select your correct option )

2

3

4

5

**VuAnswers.com**

**Made by: Waqar Siddhu**

In A4FB:4872 Segment:offset pair the physical address is (both segment and offset are in hexadecimal):

Answer ( Please select your correct option )

A4FB0

04872

A9822

A4872

**VuAnswers.com**

**Made by: Waqar Siddhu**

When the instruction "CMP AX,BX" executes and after execution ZF is set, it indicates that

Answer ( Please select your correct option )

AX is greater

BX is greater

Both AX and BX are equal

ZF has no relation with CMP

**VuAnswers.com**

**Made by: Waqar Siddhu**

Traditionally all the mathematical and logical operations are performed in \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

Code Segment register

Data Segment register

Accumulator register

Stack segment register

**Made by: Waqar Siddhu**

Stack is a data structure which behaves as

**first in last out manner 59**

Answer ( Please select your correct option )

**VuAnswers.com**

First in first out manner

Last in first out manner

All in no out manner

Last in last out manner

**Made by: Waqar Siddhu**

Segment wraparound occurs when during the effective address calculation \_\_\_\_\_ is generated.

Answer ( Please select your correct option )

carry

error

zero

parity

**VuAnswers.com**

**Made by: Waqar Siddhu**

The bits of \_\_\_\_\_ register work independently. If combined all these bits then they are meaningless.

Answer ( Please select your correct option )

**VuAnswers.com**

Accumulator (AX)

Instruction Pointer (IP)

Flag

Base

**Made by: Waqar Siddhu**

IBM PC has separate memory address space and peripheral address space. Memory address space is selected when \_\_\_\_\_ instruction is given to the processor.

Answer ( Please select your correct option )

MOV

DEC

IN

ADD

**VuAnswers.com**

**Made by: Waqar Siddhu**

INT 10 is used for \_\_\_\_\_ services.

Answer ( Please select your correct option )

BIOS video

DOS video

RAM

Disk

**VuAnswers.com**

**Made by: Waqar Siddhu**

\_\_\_\_\_ are not used in mathematical operations.

Answer ( Please select your correct option )

**VuAnswers.com**

Carry, Interrupt and Trap flag

Direction, Interrupt and Trap flag

Direction, Overflow and Trap flag

Direction, Interrupt and Sign flag

**Made by: Waqar Siddhu**

VESA stands for \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

**Made by: Waqar Siddhu**

In Computer Architecture, the Protected mode is different from \_\_\_\_\_

Answer ( Please select your correct option )

Programming mode

Real time mode

not sure

Visualization mode

Memory mode

VuAnswers.com

Made by: Waqar Siddhu

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer ( Please select your correct option )

**VuAnswers.com**

1

2

3

4

**Made by: Waqar Siddhu**

In Computer Architecture, RISC stands for \_\_\_\_\_

Answer ( Please select your correct option )

Reversed Instruction Set Computer

Reversed Instruction System Computer

Reduced Instruction Set Computer

Reduced Instruction System Computer

**VuAnswers.com**

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Complex Instruction Set Computer

Complex Instruction System Computer

Correct Instruction Set Computer

Correct Instruction System Computer

**VuAnswers.com**

**Made by: Waqar Siddhu**

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer ( Please select your correct option )

First Byte

Second Byte

Third Byte

Last Byte

**VuAnswers.com**

**Made by: Waqar Siddhu**

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer ( Please select your correct option )

INT 10

INT 09

INT 08

INT 07

**VuAnswers.com**

**Made by: Waqar Siddhu**

The thread registration code initializes the PCB and adds it to the linked list. so that which will give it a turn?

Answer ( Please select your correct option )

scheduler

linker

debugger

assembler

**VuAnswers.com**

**Made by: Waqar Siddhu**

In multi-tasking programming, all threads are

Answer ( Please select your correct option )

Independent to each other

Dependent on each other

Not executed

Not scheduled

**VuAnswers.com**

**Made by: Waqar Siddhu**

"INT 10 Video - Scroll Up Window" is a type of video services used in which mode?

Answer ( Please select your correct option )

Graphics

Text

Audio

Numeric

**VuAnswers.com**

**Made by: Waqar Siddhu**

BIOS sees the disks as

Answer ( Please select your correct option )

logical storage device

raw storage device

in the form of sectors only

in the form of tracks only

**VuAnswers.com**

**Made by: Waqar Siddhu**

Which of the following gives more logical view of the storage medium ?

Answer ( Please select your correct option )

DOS

RAM

ROM

BIOS

**VuAnswers.com**

**Made by: Waqar Siddhu**

In device attribute word, which of the following bit decides whether it is a character device or a block device ?

Answer ( Please select your correct option )

Bit 12

Bit 13

Bit 14

Bit 15

**VuAnswers.com**

**Made by: Waqar Siddhu**

The serial port connection is a ----- connector

Answer ( Please select your correct option )

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

**VuAnswers.com**

**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer ( Please select your correct option )

**VuAnswers.com**

1

2

3

4

**Made by: Waqar Siddhu**

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

7

8

9

6

**Made by: Waqar Siddhu**

8088 is a .....

Answer ( Please select your correct option )

16 bit processor

32 bit processor

64 bit processor

128 bit processor

**VuAnswers.com**

**Made by: Waqar Siddhu**

The table index (TI) is set to \_\_\_\_\_ to access the GDT (Global Descriptor Table).

Answer ( Please select your correct option )

**VuAnswers.com**

1

0

-1

-2

**Made by: Waqar Siddhu**

In 68K processors there is a ..... program counter (PC) that holds the address of currently executing instruction.

Answer ( Please select your correct option )

8 bit

16 bit

32 bit

64 bit

**VuAnswers.com**

**Made by: Waqar Siddhu**

In 68K processors there is a 32 bit ..... that holds the address of currently executing instruction.

Answer ( Please select your correct option )

Program counter

Stack pointer

Register

Stack

**VuAnswers.com**

**Made by: Waqar Siddhu**

An 8 x 16 font is stored in \_\_\_\_\_ bytes.

Answer ( Please select your correct option )

2

4

8

16

**VuAnswers.com**

**Made by: Waqar Siddhu**

Which of the following interrupts is Non Maskable Interrupt (NMI).

Answer ( Please select your correct option )

INT 2

INT 3

INT 0

INT 1

**VuAnswers.com**

**Made by: Waqar Siddhu**

\_\_\_\_\_ number of pin(s) of a processor is/are used by the external hardware to generate an interrupt.

Answer ( Please select your correct option )

**VuAnswers.com**

1



2



3



4



**Made by: Waqar Siddhu**

After the execution of "PUSH AX" statement

VuAnswers.com

Answer ( Please select your correct option )

- AX register will reside on the stack
- A copy of AX will go on the stack
- The value of AX disappear after moving on stack
- Stack will send an acceptance message

Made by: Waqar Siddhu

Which of the following IRQs is connected to serial port COM 1?

**VuAnswers.com**

Answer ( Please select your correct option )

IRQ 4

IRQ 5

IRQ 6

IRQ 7

**Made by: Waqar Siddhu**

An End of Interrupt (EOI) signal is sent by

**VuAnswers.com**

Answer ( Please select your correct option )

Handler

Processor

IRQ

PIC

**Made by: Waqar Siddhu**

The programmable interval timer (PIT) has input frequency of

**VuAnswers.com**

Answer ( Please select your correct option )

1.193MHZ

2.193MHZ

3.193MHZ

4.193MHZ

**Made by: Waqar Siddhu**

Which of the following interrupt is of highest priority interrupt?

**VuAnswers.com**

Answer ( Please select your correct option )

Key board interrupt

Timer interrupt

INT 2

INT 3

**Made by: Waqar Siddhu**

The actual physical address represented by the segment-offset pair "AB01:FFFF" will be:

VuAnswers.com

Answer ( Please select your correct option )

1AB00

001AB

BB00F

F00BB

Made by: Waqar Siddhu

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:  
SHL DH, CL.

VuAnswers.com

Answer ( Please select your correct option )

01010000

10001010

00010100

01010111

Made by: Waqar Siddhu

When two devices in the system want to use the same IRQ line then what will happen?

**VuAnswers.com**

Answer ( Please select your correct option )

An IRQ Collision

An IRQ Conflict

An IRQ Crash

An IRQ Blockage

**Made by: Waqar Siddhu**

After the execution of STOSW, the CX will be

**VuAnswers.com**

Answer ( Please select your correct option )

Decremented by 1

Decremented by 2

Incremented by 1

Incremented by 2

**Made by: Waqar Siddhu**

Which of the following is true ?

**VuAnswers.com**

Answer ( Please select your correct option )

In general the memory cell cannot be wider than data bus

Control bus is unidirectional

Memory can use the address bus to refer to certain register in processor

There is no relation between memory and data bus

**Made by: Waqar Siddhu**

Traditionally all the mathematical and logical operations are performed in \_\_\_\_\_

**VuAnswers.com**

Answer ( Please select your correct option )

Code Segment register

Data Segment register

Accumulator register

Stack segment register

**Made by: Waqar Siddhu**

Relating to the stack in 8088 architecture which of the following statement is true ?

**VuAnswers.com**

Answer ( Please select your correct option )

Single word can not be pushed on the stack

Single word can not be popped from the stack

Single byte can not be popped from the stack

Single quad can be popped from the stack

**Made by: Waqar Siddhu**

The second byte of the word designated for one screen location holds

**VuAnswers.com**

Answer ( Please select your correct option )

- The dimensions of the screen
- Character position on the screen
- Character color on the screen
- ASCII code of the character

**Made by: Waqar Siddhu**

INT 10 is used for \_\_\_\_\_ services.

**VuAnswers.com**

Answer ( Please select your correct option )

BIOS video

DOS video

RAM

Disk

**Made by: Waqar Siddhu**

In graphics mode a location in video memory corresponds to a \_\_\_\_\_ on the screen.

**VuAnswers.com**

Answer ( Please select your correct option )

line



dot



circle



rectangle



**Made by: Waqar Siddhu**

The value of AH register in the write Graphics pixel service is \_\_\_\_\_

**VuAnswers.com**

Answer ( Please select your correct option )

0Ch

0Bh

1Ch

2Ch

**Made by: Waqar Siddhu**

The space where the current state of a task is stored is called \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

control block

Process control block

stack

hard disk

**Made by: Waqar Siddhu**

INT 21 service 01H is used to read character from standard input with echo. It returns the result in \_\_\_\_\_ register.

VuAnswers.com

Answer ( Please select your correct option )

al

bl

cl

bh

Made by: Waqar Siddhu

VESA VBE 2.0 is a standard for \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

Ultra resolution mode

High resolution mode

Medium resolution mode

Low resolution mode

**Made by: Waqar Siddhu**

VESA stands for \_\_\_\_\_

**VuAnswers.com**

Answer ( Please select your correct option )

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

**Made by: Waqar Siddhu**

VBE stands for \_\_\_\_\_, as proposed by VESA (an organization).

**VuAnswers.com**

Answer ( Please select your correct option )

Video BIOS Extensions

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

**Made by: Waqar Siddhu**

IDTR is a \_\_\_\_\_ register.

**VuAnswers.com**

Answer ( Please select your correct option )

16 bit

20 bit

32 bit

48 bit

**Made by: Waqar Siddhu**

In Computer Architecture, the Protected mode is different from \_\_\_\_\_

**VuAnswers.com**

Answer ( Please select your correct option )

Programming mode

Real time mode

Visualization mode

Memory mode

**Made by: Waqar Siddhu**

Which byte of the DOS input buffer holds the number of characters actually read on return?

**VuAnswers.com**

Answer ( Please select your correct option )

First Byte



Second Byte



Third Byte



Last Byte



**Made by: Waqar Siddhu**

What is the size of a Sector of a standard 1.44MB Floppy disk having 80 tracks and 18 Sectors/track?

**VuAnswers.com**

Answer ( Please select your correct option )

128 Bytes

256 Bytes

512 Bytes

1024 Bytes

**Made by: Waqar Siddhu**

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

**VuAnswers.com**

Answer ( Please select your correct option )

INT 10

INT 09

INT 08

INT 07

**Made by: Waqar Siddhu**

Threads can have function calls, parameters and which type of variables?

**VuAnswers.com**

Answer ( Please select your correct option )

global

local

legal

illegal

**Made by: Waqar Siddhu**

Technically IAPX88 stands for “\_\_\_\_\_ Advanced Processor Extensions 88.”

**VuAnswers.com**

Answer ( Please select your correct option )

Intel

Integrated

Intra

Internal

**Made by: Waqar Siddhu**

In Left-Shift-Operation, the left most bit \_\_\_\_\_

**VuAnswers.com**

Answer ( Please select your correct option )

- will be dropped
- will go into CF
- will be moved to the right-most position
- will always be 1

**Made by: Waqar Siddhu**

The serial port connection is a ----- connector

**VuAnswers.com**

Answer ( Please select your correct option )

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

**VuAnswers.com**

Answer ( Please select your correct option )

6



7



8



9



**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to CD (Carrier Detect) ?

**VuAnswers.com**

Answer ( Please select your correct option )

1

2

3

4

**Made by: Waqar Siddhu**

Which of the following BIOS INT provides serial port services

**VuAnswers.com**

Answer ( Please select your correct option )

INT14

INT11

INT12

INT13

**Made by: Waqar Siddhu**

8088 is a .....

**VuAnswers.com**

Answer ( Please select your correct option )

16 bit processor

32 bit processor

64 bit processor

128 bit processor

**Made by: Waqar Siddhu**

A 32 bit address register can access up to ..... of memory, so memory access has increased a lot.

**VuAnswers.com**

Answer ( Please select your correct option )

2GB

4GB

6GB

8GB

**Made by: Waqar Siddhu**

The table index (TI) is set to \_\_\_\_\_ to access the GDT (Global Descriptor Table).

**VuAnswers.com**

Answer ( Please select your correct option )

1

0

-1

-2

**Made by: Waqar Siddhu**

In 68K processors there is a ..... program counter (PC) that holds the address of currently executing instruction.

**VuAnswers.com**

Answer ( Please select your correct option )

8 bit



16 bit



32 bit



64 bit



**Made by: Waqar Siddhu**

In 68K processors there is a 32 bit ..... that holds the address of currently executing instruction.

**VuAnswers.com**

Answer ( Please select your correct option )

Program counter

Stack pointer

Register

Stack

**Made by: Waqar Siddhu**

Programmable Interrupt Controller (PIC) has

**VuAnswers.com**

Answer ( Please select your correct option )

One input signals and eight output signals

One input signal and one output signal

Eight input signals and one output signals

Eight input signals and eight output signals

**Made by: Waqar Siddhu**

Which of the following interrupt is used in debugging with the help of trap flag?

**VuAnswers.com**

Answer ( Please select your correct option )

INT 0

INT 1

INT 2

INT 3

**Made by: Waqar Siddhu**

In an indirect addressing mode the address of memory cell

Answer ( Please select your correct option )

VuAnswers.com

is placed in a register

is directly accessed by instruction

is constant

is variable

Made by: Waqar Siddhu

In an indirect addressing mode the address of memory cell

Answer ( Please select your correct option )

**VuAnswers.com**

is placed in a register

is directly accessed by instruction

is constant

is variable

**Made by: Waqar Siddhu**

In STOSB instruction, when DF is clear, SI is

Answer / Choose your correct option )

Incremented by 1

Incremented by 2

Decrement by 1

Decrement by 2

**VuAnswers.com**

**Made by: Waqar Siddhu**

IBM AT has how many PICs (Programmable interrupt controller)

Answer ( Please select your correct option )

**VuAnswers.com**

1

2

3

4

**Made by: Waqar Siddhu**

Processor informs the memory that it wants to read data, using :

Answer ( Please select your correct option )

Address bus

Data bus

Control bus

It informs without using any bus

**VuAnswers.com**

**Made by: Waqar Siddhu**

Memory cell width refers to \_\_\_\_\_

Answer ( Please select your correct option )

Number of bits in a cell

The maximum value that can be stored in a cell including sign-bit

The maximum value that can be stored in a cell excluding sign-bit

Address of that cell

**VuAnswers.com**

**Made by: Waqar Siddhu**

In MUL instruction if the source operand is a byte then it is multiplied by register

Answer ( Please select your correct option )

AX

AL

CX

DL

**VuAnswers.com**

**Made by: Waqar Siddhu**

In the word designated for one screen location, the higher address contains

Answer ( Please select your correct option )

The character code

The attribute byte

The parameters

The dimensions

VuAnswers.com

Made by: Waqar Siddhu

"mov [bx], ax" moves the two bytes contents of the AX register to the address contained in BX register in the current

Answer ( Please select your correct option )

Stack segment

Code segment

Data segment

Extra segment

VuAnswers.com

Made by: Waqar Siddhu

CALL instruction transfer the control

Answer ( Please select your correct option )

**VuAnswers.com**

Permanently



Temporarily

Permanently when CALL is NEAR

Permanently when CALL is FAR

**Made by: Waqar Siddhu**

A 32 bit processor has an accumulator of \_\_\_\_\_

Answer ( Please select your correct option )

8 bits

16 bits

32 bits

64 bits

**VuAnswers.com**

**Made by: Waqar Siddhu**

The parallel port connector is called \_\_\_\_\_

Answer ( Please select your correct option )

DB-25

BD-25

DB-24

BD-24

**VuAnswers.com**

**Made by: Waqar Siddhu**

The Single step interrupt is \_\_\_\_\_

Answer ( Please select your correct option )

Hardware interrupt

Like divide by zero interrupt

Like divide by 1 interrupt

Like divide by 2 interrupt

**VuAnswers.com**

**Made by: Waqar Siddhu**

\_\_\_\_\_ are not used in mathematical operations.

Answer ( Please select your correct option )

**VuAnswers.com**

Carry, Interrupt and Trap flag

Direction, Interrupt and Trap flag

Direction, Overflow and Trap flag

Direction, Interrupt and Sign flag

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In graphics mode a location in video memory corresponds to a \_\_\_\_\_ on the screen.

Answer ( Please select your correct option )

line

dot

circle

rectangle

**VuAnswers.com**

**Made by: Waqar Siddhu**

In DOS input buffer, the number of characters actually read on return is stored in \_\_\_\_\_ byte.

Answer ( Please select your correct option )

third

fourth

first

second

**VuAnswers.com**

**Made by: Waqar Siddhu**

VESA VBE 2.0 is a standard for \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

Ultra resolution mode

High resolution mode

Medium resolution mode

Low resolution mode

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Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

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48 bit

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Real time mode

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Memory mode

**VuAnswers.com**

**Made by: Waqar Siddhu**

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer ( Please select your correct option )

**VuAnswers.com**

1

2

3

4

**Made by: Waqar Siddhu**

In NASM, an imported symbol is declared with the \_\_\_\_\_ while and exported symbol is declared with the \_\_\_\_\_.

Answer ( Please select your correct option )

Global directive, External directive

External directive, Global directive

Home Directive, Foreign Directive

Foreign Directive, Home Directive

**VuAnswers.com**

**Made by: Waqar Siddhu**

In Computer Architecture, RISC stands for \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

Reversed Instruction Set Computer

Reversed Instruction System Computer

Reduced Instruction Set Computer

Reduced Instruction System Computer

**Made by: Waqar Siddhu**

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Complex Instruction Set Computer

Complex Instruction System Computer

Correct Instruction Set Computer

Correct Instruction System Computer

**VuAnswers.com**

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Threads can have function calls, parameters and which type of variables?

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local

legal

illegal

**VuAnswers.com**

**Made by: Waqar Siddhu**

"INT 10 Video - Scroll Up Window" is a type of video services used in which mode?

Answer ( Please select your correct option )

Numeric

Graphics

Text

Audio

**VuAnswers.com**

**Made by: Waqar Siddhu**

The first possible segment base value is \_\_\_\_\_.

Answer ( Please select your correct option )

0000

0001

0010

0100

**VuAnswers.com**

**Made by: Waqar Siddhu**

BIOS sees the disks as

Answer ( Please select your correct option )

**VuAnswers.com**

logical storage device

raw storage device

in the form of sectors only

in the form of tracks only

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In device attribute word, which of the following bit decides whether it is a character device or a block device ?

Answer ( Please select your correct option )

**VuAnswers.com**

Bit 12

Bit 13

Bit 14

Bit 15

**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to DSR (Data Set Ready) ?

Answer ( Please select your correct option )

4

5

6

7

**VuAnswers.com**

**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer ( Please select your correct option )

5

6

7

8

**VuAnswers.com**

**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to RD (Received Data) ?

Answer ( Please select your correct option )

**VuAnswers.com**

1

2

3

4

**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer ( Please select your correct option )

**VuAnswers.com**

1

2

3

4

**Made by: Waqar Siddhu**

Which of the following BIOS INT provides serial port services

Answer (Please select your correct option )

INT14

INT11

INT12

INT13

**VuAnswers.com**

**Made by: Waqar Siddhu**

A 32 bit address register can access up to ..... of memory, so memory access has increased a lot.

Answer ( Please select your correct option )

2GB

4GB

6GB

8GB

**VuAnswers.com**

**Made by: Waqar Siddhu**

The segment address of interrupt n will be at

Answer ( Please select your correct option )

VuAnswers.com

nx4

nx4+1

nx4+2

~~not sure~~

nx4+3

Made by: Waqar Siddhu

An 8 x 16 font is stored in \_\_\_\_\_ bytes.

Answer ( Please select your correct option )

**VuAnswers.com**

2

4

8

16

**Made by: Waqar Siddhu**

Which of the following interrupts is Non Maskable Interrupt (NMI).

Answer ( Please select your correct option )

INT 2

INT 3

INT 0

INT 1

**VuAnswers.com**

**Made by: Waqar Siddhu**

Which of the following is a special type of interrupt that returns to the same instruction instead of the next instruction?

Answer (Please select your correct option )

**VuAnswers.com**

Divide overflow interrupt

Debug interrupt

Arithmetic overflow interrupt

Change of sign interrupt

**Made by: Waqar Siddhu**

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer ( Please select your correct option )

19

20

21

22

**VuAnswers.com**

**Made by: Waqar Siddhu**

Which one of the following interrupts is used for arithmetic overflow?

Answer ( Please select your correct option )

INT 4

INT 1

INT 2

INT 3

**VuAnswers.com**

**Made by: Waqar Siddhu**

The instruction `mov ax, 0`

VuAnswers.com

Answer ( Please select your correct option )

- will set the ZF
- will not set the ZF
- is a mathematical instruction
- is a logical instruction

Made by: Waqar Siddhu

In STOSB instruction, when DF is set, SI is

Answer (Please select the correct option )

VuAnswers.com

incremented by 1

Incremented by 2

Decrement by 1

Decrement by 2

Made by: Waqar Siddhu

The data on which we want to perform some operations is called

VuAnswers.com

Answer ( Please select your correct option )

Opcode

Operand

Operator

Operation

Made by: Waqar Siddhu

After the execution of instruction "RET 2"

VuAnswers.com

Answer ( Please select your correct option )

SP is incremented by 2

SP is decremented by 2

not sure

SP is incremented by 4

SP is decremented by 4

Made by: Waqar Siddhu

RETF will pop the segment address in the

VuAnswers.com

Answer ( Please select your correct option )

CS register

DS register

SS register

ES register

Made by: Waqar Siddhu

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:  
SHL DH, CL.

VuAnswers.com

Answer ( Please select your correct option )

01010000

10001010

00010100

01010111

Made by: Waqar Siddhu

In STOSB instruction, SI is decremented or incremented by

VuAnswers.com

Answer ( Please select your correct option )

3



2



1



4



Made by: Waqar Siddhu

The OUT instruction uses \_\_\_\_\_ as source register.

VuAnswers.com

Answer ( Please select your correct option )

AL or AX

BL or BX

CL or CX

DL or DX

Made by: Waqar Siddhu

When two devices in the system want to use the same IRQ line then what will happen?

VuAnswers.com

Answer ( Please select your correct option )

An IRQ Collision

An IRQ Conflict

An IRQ Crash

An IRQ Blockage

Made by: Waqar Siddhu

Which bit of the Attribute Byte (counting from right to left, starting from 0) represents the RED component of background color ?

VuAnswers.com

Answer ( Please select your correct option )

3

4

5

6

Made by: Waqar Siddhu

Sign flag is set to \_\_\_\_\_ for positive numbers

VuAnswers.com

Answer ( Please select your correct option )

0

+1

0 or 1

1

Made by: Waqar Siddhu

Computer Architecture and Assembly Language Programming is a course that is designed to study \_\_\_\_\_

**VuAnswers.com**

Answer ( Please select your correct option )

Hardware only

Software only

Computer system more closely

Computer Graphics

**Made by: Waqar Siddhu**

IBM PC has separate memory address space and peripheral address space. Peripheral address space is selected when \_\_\_\_\_ instructions is given to the processor.

Answer ( Please select your correct option )

**VuAnswers.com**

IN

ADD

MOV

DEC

**Made by: Waqar Siddhu**

The parallel port connector is called \_\_\_\_\_

VuAnswers.com

Answer ( Please select your correct option )

DB-25

BD-25

DB-24

BD-24

Made by: Waqar Siddhu

The Single step interrupt is \_\_\_\_\_

VuAnswers.com

Answer ( Please select your correct option )

Hardware interrupt

Like divide by zero interrupt

Like divide by 1 interrupt

Like divide by 2 interrupt

Made by: Waqar Siddhu

\_\_\_\_\_ are not used in mathematical operations.

VuAnswers.com

Answer ( Please select your correct option )

Carry, Interrupt and Trap flag

Direction, Interrupt and Trap flag

Direction, Overflow and Trap flag

Direction, Interrupt and Sign flag

Made by: Waqar Siddhu

In graphics mode a location in video memory corresponds to a \_\_\_\_\_ on the screen.

VuAnswers.com

Answer ( Please select your correct option )

line

dot

circle

rectangle

Made by: Waqar Siddhu

In DOS input buffer, the number of characters actually read on return is stored in \_\_\_\_\_ byte.

VuAnswers.com

Answer ( Please select your correct option )

third

fourth

first

second

Made by: Waqar Siddhu

The value of AH register in the write Graphics pixel service is \_\_\_\_\_

VuAnswers.com

Answer ( Please select your correct option )

0Ch

0Bh

1Ch

2Ch

Made by: Waqar Siddhu

INT 21 service 01H is used to read character from standard input with echo. It returns the result in \_\_\_\_\_ register.

VuAnswers.com

Answer ( Please select your correct option )

a)

b)

c)

~~bh~~

Made by: Waqar Siddhu

VESA (an organization) organizes 16 color bits for every pixel in \_\_\_\_\_ format.

VuAnswers.com

Answer ( Please select your correct option )

5:5:5

5:6:5

5:5:6

5:6:6

Made by: Waqar Siddhu

VESA stands for \_\_\_\_\_

**VuAnswers.com**

Answer ( Please select your correct option )

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

**Made by: Waqar Siddhu**

IDTR is a \_\_\_\_\_ register.

VuAnswers.com

Answer ( Please select your correct option )

16 bit

20 bit

32 bit

48 bit

Made by: Waqar Siddhu

In Motorola 68K processors, \_\_\_\_\_ registers can hold addresses in indirect memory accesses.

VuAnswers.com

Answer ( Please select your correct option )

A0-A7

B0-B7

C0-C7

D0-D7

Made by: Waqar Siddhu

Motorola 68K processors have \_\_\_\_\_ 32 bit general purpose registers.

VuAnswers.com

Answer ( Please select your correct option )

32

64

8

16

Made by: Waqar Siddhu

In Computer Architecture, RISC stands for \_\_\_\_\_

**VuAnswers.com**

Answer ( Please select your correct option )

- Reversed Instruction Set Computer
- Reversed Instruction System Computer
- Reduced Instruction Set Computer
- Reduced Instruction System Computer

**Made by: Waqar Siddhu**

In Computer Architecture, CISC stands for \_\_\_\_\_

VuAnswers.com

Answer ( Please select your correct option )

Complex Instruction Set Computer

Complex Instruction System Computer

Correct Instruction Set Computer

Correct Instruction System Computer

Made by: Waqar Siddhu

Video services are exported via

VuAnswers.com

Answer ( Please select your correct option )

INT 7

INT 10

INT 9

INT 8

Made by: Waqar Siddhu

BIOS sees the disks as

VuAnswers.com

Answer ( Please select your correct option )

- logical storage device
- raw storage device
- in the form of sectors only
- in the form of tracks only

Made by: Waqar Siddhu

Which of the following gives more logical view of the storage medium ?

Answer ( Please select your correct option )

VuAnswers.com

DOS

RAM

ROM

BIOS

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to DSR (Data Set Ready) ?

VuAnswers.com

Answer ( Please select your correct option )

4

5

6

7

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

VuAnswers.com

Answer ( Please select your correct option )

5

6

7

8

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

VuAnswers.com

Answer ( Please select your correct option )

6

7

8

9

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to RD (Received Data) ?

VuAnswers.com

Answer ( Please select your correct option )

1

2

3

4

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

VuAnswers.com

Answer ( Please select your correct option )

1

2

3

4

Made by: Waqar Siddhu

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number \_\_\_\_\_

VuAnswers.com

Answer ( Please select your correct option )

6

7

8

9

Made by: Waqar Siddhu

The table index (TI) is set to \_\_\_\_\_ to access the GDT (Global Descriptor Table).

VuAnswers.com

Answer ( Please select your correct option )

1

0

-1

-2

Made by: Waqar Siddhu

Which of the following interrupt is used in debugging with the help of trap flag?

VuAnswers.com

Answer ( Please select your correct option )

INT 0

INT 1

INT 2

INT 3

Made by: Waqar Siddhu

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

VuAnswers.com

Answer ( Please select your correct option )

19

20

21

22

Made by: Waqar Siddhu

Which one of the following interrupts is used for arithmetic overflow?

VuAnswers.com

Answer ( Please select your correct option )

INT 4

INT 1

INT 2

INT 3

Made by: Waqar Siddhu

The process of sending signals back and forth is called

Answer ( Please select your correct option )

**VuAnswers.com**

Activity

Hand-shaking

Interruption

Time clicking

**Made by: Waqar Siddhu**

Which of the following IRQs is connected to serial port COM 1?

Answer ( Please select your correct option )

**VuAnswers.com**

IRQ 4

IRQ 5

IRQ 6

IRQ 7

**Made by: Waqar Siddhu**

Which of the following interrupt is of highest priority interrupt?

Answer ( Please select your correct option )

**VuAnswers.com**

Key board interrupt

Timer interrupt

INT 2

INT 3

**Made by: Waqar Siddhu**

Regarding assembler, which statement is true:

Answer ( Please select your correct option )

**VuAnswers.com**

Assembler converts mnemonics to the corresponding OP CODE

Assembler converts OP CODE to the corresponding mnemonics

Assembler executes the assembly code all at once

Assembler executes the assembly code step by step

**Made by: Waqar Siddhu**

CMPS instruction subtracts the source location to the destination location. Source location always lies in

Answer ( Please select your correct option )

**VuAnswers.com**

ES:SI

~~ES:DI~~

DS:SI

DS:DI

**Made by: Waqar Siddhu**

For the execution of the instruction "DIV BL", the implied dividend will be stored in

Answer ( Please select your correct option )

**VuAnswers.com**

AX

BX

CX

DX

**Made by: Waqar Siddhu**

When two devices in the system want to use the same IRQ line then what will happen?

Answer ( Please select your correct option )

**VuAnswers.com**

An IRQ Collision

An IRQ Conflict

An IRQ Crash

An IRQ Blockage

**Made by: Waqar Siddhu**

The memory address always move from \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

processor to memory

memory to processor

memory to peripheral

peripheral to processor

**Made by: Waqar Siddhu**

After the execution of following two instructions, what will happen ?

```
PUSH AX  
POP DX
```

Answer ( Please select your correct option )

**VuAnswers.com**

The value of DX will be at the top of stack

The value of AX will be at the top of stack

The value of DX will be copied into AX

The value of AX will be copied into DX

**Made by: Waqar Siddhu**

The second byte of the word designated for one screen location holds

Answer ( Please select your correct option )

**VuAnswers.com**

The dimensions of the screen

Character position on the screen

Character color on the screen

ASCII code of the character

**Made by: Waqar Siddhu**

In Shift instructions the destination can be

Answer ( Please select your correct option )

**VuAnswers.com**

- An 8-bit register only
- A register only
- A memory location only
- A register or memory location

**Made by: Waqar Siddhu**

INT 10 is used for \_\_\_\_\_ services.

Answer ( Please select your correct option )

**VuAnswers.com**

BIOS video

DOS video

RAM

Disk

**Made by: Waqar Siddhu**

In graphics mode a location in video memory corresponds to a \_\_\_\_\_ on the screen.

Answer ( Please select your correct option )

**VuAnswers.com**

line

dot

circle

rectangle

**Made by: Waqar Siddhu**

A thread can not be created \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

in static way

in dynamic way

in static and dynamic way at the same time

manually

**Made by: Waqar Siddhu**

The space where the current state of a task is stored is called \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

control block

Process control block

stack

hard disk

**Made by: Waqar Siddhu**

VESA (an organization) organizes 16 color bits for every pixel in \_\_\_\_\_ format.

Answer ( Please select your correct option )

**VuAnswers.com**

5:5:5

5:6:5

5:5:6

5:6:6

**Made by: Waqar Siddhu**

VESA stands for \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

**Made by: Waqar Siddhu**

The Physical address of Interrupt Descriptor Table (IVT) is stored in \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

GDTR

LGTD

IDTR

IVT

**Made by: Waqar Siddhu**

IDTR is a \_\_\_\_\_ register.

Answer ( Please select your correct option )

**VuAnswers.com**

16 bit

20 bit

32 bit

48 bit

**Made by: Waqar Siddhu**

In Motorola 68K processors, \_\_\_\_\_ registers can hold addresses in indirect memory accesses.

Answer ( Please select your correct option )

**VuAnswers.com**

A0-A7

B0-B7

C0-C7

D0-D7

**Made by: Waqar Siddhu**

Motorola 68K processors have \_\_\_\_\_ 32 bit general purpose registers.

Answer ( Please select your correct option )

**VuAnswers.com**

32

64

8

16

**Made by: Waqar Siddhu**

In Computer Architecture, RISC stands for \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

Reversed Instruction Set Computer

Reversed Instruction System Computer

Reduced Instruction Set Computer

Reduced Instruction System Computer

**Made by: Waqar Siddhu**

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer ( Please select your correct option )

**VuAnswers.com**

INT 10

INT 09

INT 08

INT 07

**Made by: Waqar Siddhu**

Threads can have function calls, parameters and which type of variables?

Answer ( Please select your correct option )

**VuAnswers.com**

global

local

legal

illegal

**Made by: Waqar Siddhu**

In multi-tasking programming, all threads are

Answer ( Please select your correct option )

**VuAnswers.com**

Independent to each other

Dependent on each other

Not executed

Not scheduled

**Made by: Waqar Siddhu**

"Far" jump makes intra \_\_\_\_\_ control possible

Answer ( Please select your correct option )

**VuAnswers.com**

Register

Segment

Memory

Code

**Made by: Waqar Siddhu**

All possible types of Jumps in assembly language are:

Answer ( Please select your correct option )

**VuAnswers.com**

short, far

short, near

short, near, far

short, near, far, long

**Made by: Waqar Siddhu**

BIOS sees the disks as

Answer ( Please select your correct option )

**VuAnswers.com**

logical storage device

raw storage device

in the form of sectors only

in the form of tracks only

**Made by: Waqar Siddhu**

Which of the following gives more logical view of the storage medium ?

Answer ( Please select your correct option )

**VuAnswers.com**

DOS

RAM

ROM

BIOS

**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer ( Please select your correct option )

**VuAnswers.com**

1

2

3

4

**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to DTR (Data Terminal Ready) ?

Answer ( Please select your correct option )

**VuAnswers.com**

1

2

3

4

**Made by: Waqar Siddhu**

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

7

8

9

6

**Made by: Waqar Siddhu**

8088 is a .....

Answer ( Please select your correct option )

**VuAnswers.com**

16 bit processor

32 bit processor

64 bit processor

128 bit processor

**Made by: Waqar Siddhu**

A 32 bit address register can access up to ..... of memory, so memory access has increased a lot.

Answer ( Please select your correct option )

**VuAnswers.com**

2GB

4GB

6GB

8GB

**Made by: Waqar Siddhu**

AX register in extended 32 bit register is renamed as \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

AXE

EAX

AAX

XAX

**Made by: Waqar Siddhu**

The table index (TI) is set to \_\_\_\_\_ to access the GDT (Global Descriptor Table).

Answer ( Please select your correct option )

**VuAnswers.com**

1

0

-1

-2

**Made by: Waqar Siddhu**

The segment address of interrupt n will be at

Answer ( Please select your correct option )

**VuAnswers.com**

nx4

nx4+1

nx4+2

nx4+3

**Made by: Waqar Siddhu**

Programmable Interrupt Controller (PIC) has

Answer ( Please select your correct option )

**VuAnswers.com**

One input signals and eight output signals

One input signal and one output signal

Eight input signals and one output signals

Eight input signals and eight output signals

**Made by: Waqar Siddhu**

\_\_\_\_\_ number of pin(s) of a processor is/are used by the external hardware to generate an interrupt.

Answer ( Please select your correct option )

**VuAnswers.com**

1

2

3

4

**Made by: Waqar Siddhu**

In Programmable Interrupt Controller, Port \_\_\_\_\_ is referred as a Control port.

Answer ( Please select your correct option )

**VuAnswers.com**

19

20

21

22

**Made by: Waqar Siddhu**

In STOSW instruction, when DF is clear, SI is

Answer ( Please select your correct option )

VuAnswers.com

Incremented by 1

Incremented by 2

correct

Decrementd by 1

Decrementd by 2

Made by: Waqar Siddhu

Which of the following IRQs is connected to serial port COM 1?

Answer ( Please select your correct option )

VuAnswers.com

IRQ 4

correct

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Which of the following IRQs is used by the parallel port?

Answer ( Please select your correct option )

**VuAnswers.com**

IRQ 4

IRQ 5

IRQ 6

IRQ 7

correct

**Made by: Waqar Siddhu**

The programmable interval timer (PIT) has input frequency of

Answer ( Please select your correct option )

VuAnswers.com

1.193MHZ

correct

2.193MHZ

3.193MHZ

4.193MHZ

Made by: Waqar Siddhu

Which of the following interrupt is of highest priority interrupt?

Answer ( Please select your correct option )

VuAnswers.com

Key board interrupt

Timer interrupt

correct

INT 2

INT 3

Made by: Waqar Siddhu

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer ( Please select your correct option )

VuAnswers.com

Shl AX, 1

Shr AX, 1

correct

NOT AX

AND AX, 0xFFFF

Made by: Waqar Siddhu

In the rotate right operation every bit moves one position to the right and the bit dropped from the right is inserted at the left and:

Answer ( Please select your correct option )

VuAnswers.com

The dropped bit is also copied in CF

correct

The dropped bit is also copied in AF

The dropped bit is also copied in ZF

The dropped bit is not copied in any flag

Made by: Waqar Siddhu

In string instructions, CX is always

Answer ( Please select your correct option )

VuAnswers.com

Decremented by 1

Decremented by 2

Incremented by 1

correct

Incremented by 2

Made by: Waqar Siddhu

The word size of the processor is defined by the size of \_\_\_\_\_

Answer ( Please select your correct option )

VuAnswers.com

Its accumulator register

Address bus attached

~~X~~ correct

Memory attached

Control Bus

Made by: Waqar Siddhu

\_\_\_\_\_ cannot predict which element the \_\_\_\_\_ needs, at a particular instant of time.

Answer ( Please select your correct option )

VuAnswers.com

Control bus, Memory

Processor, Control bus  
 Processor, Memory

Memory, Processor

correct

Control bus, Memory

Made by: Waqar Siddhu

The time interval between two timer ticks is \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

40ms

45ms

50ms

55ms

correct

**Made by: Waqar Siddhu**

In DOS input buffer, the number of characters actually read on return is stored in \_\_\_\_\_ byte.

Answer ( Please select your correct option )

**VuAnswers.com**

third

fourth

first

second

correct

**Made by: Waqar Siddhu**

A thread can not be created \_\_\_\_\_

Answer ( Please select your correct option )

VuAnswers.com

in static way

correct

in dynamic way

in static and dynamic way at the same time

manually

Made by: Waqar Siddhu

The space where the current state of a task is stored is called \_\_\_\_\_

Answer ( Please select your correct option )

VuAnswers.com

control block

Process control block

correct

stack

hard disk

Made by: Waqar Siddhu

VESA stands for \_\_\_\_\_

Answer ( Please select your correct option )

VuAnswers.com

Variable Electronics Standards Association

Video Electronics Standards Association

correct

Video Energy Standards Association

Vega Energy Standards Association

Made by: Waqar Siddhu

VBE stands for \_\_\_\_\_, as proposed by VESA (an organization).

Answer ( Please select your correct option )

VuAnswers.com

Video BIOS Extensions

**correct**

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

Made by: Waqar Siddhu

In Computer Architecture, the Protected mode is different from \_\_\_\_\_

Answer ( Please select your correct option )

VuAnswers.com

Programming mode

Real time mode

correct

Visualization mode

Memory mode

Made by: Waqar Siddhu

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer ( Please select your correct option )

**VuAnswers.com**

1



2



correct

3



4



**Made by: Waqar Siddhu**

In Motorola 68K processors, \_\_\_\_\_ registers can hold addresses in indirect memory accesses.

Answer ( Please select your correct option )

VuAnswers.com

A0-A7

correct

B0-B7

C0-C7

D0-D7

Made by: Waqar Siddhu

Motorola 68K processors have \_\_\_\_\_ 32 bit general purpose registers.

Answer ( Please select your correct option )

**VuAnswers.com**

32

64

8

16

correct

**Made by: Waqar Siddhu**

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer ( Please select your correct option )

VuAnswers.com

First Byte



Second Byte



correct

Third Byte



Last Byte



Made by: Waqar Siddhu

Threads can have function calls, parameters and which type of variables?

Answer ( Please select your correct option )

**VuAnswers.com**

global

local

correct

legal

illegal

**Made by: Waqar Siddhu**

In multi-tasking programming, all threads are

Answer ( Please select your correct option )

VuAnswers.com

Independent to each other

correct

Dependent on each other

Not executed

Not scheduled

Made by: Waqar Siddhu

Video services are exported via

Answer ( Please select your correct option )

**VuAnswers.com**

INT 7

INT 10

**correct**

INT 9

INT 8

**Made by: Waqar Siddhu**

The maximum memory, IAPX88 can access is \_\_\_\_\_.

Answer ( Please select your correct option )

VuAnswers.com

1 KB

64 KB

1 MB

correct

64 MB

Made by: Waqar Siddhu

In direct addressing the memory address is \_\_\_\_\_ and is given in the instruction.

Answer ( Please select your correct option )

VuAnswers.com

Fixed correct

Empty

A variable

A register

Made by: Waqar Siddhu

Instruction "mov ax, bl" has a \_\_\_\_\_ problem.

Answer ( Please select your correct option )

VuAnswers.com

size mismatch

correct

value mismatch

operand mismatch

memory mismatch

Made by: Waqar Siddhu

The jump "JC" is taken when

Answer ( Please select your correct option )

VuAnswers.com

CF=1

correct

CF=0

ZF=0

ZF=1

Made by: Waqar Siddhu

Which of the following gives more logical view of the storage medium ?

Answer ( Please select your correct option )

**VuAnswers.com**

DOS

RAM

ROM

BIOS

**Made by: Waqar Siddhu**

The serial port connection is a ----- connector

Answer ( Please select your correct option )

[VuAnswers.com](http://VuAnswers.com)

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer ( Please select your correct option )

**VuAnswers.com**

5



6



7



correct

8



**Made by: Waqar Siddhu**

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer ( Please select your correct option )

**VuAnswers.com**

6



7



8



correct

9



**Made by: Waqar Siddhu**

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

6



7



8



9



correct

**Made by: Waqar Siddhu**

8088 is a .....

Answer ( Please select your correct option )

**VuAnswers.com**

16 bit processor

32 bit processor

64 bit processor

128 bit processor

**Made by: Waqar Siddhu**

AX register in extended 32 bit register is renamed as \_\_\_\_\_

Answer ( Please select your correct option )

**VuAnswers.com**

AXE

EAX

AAX

XAX

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The table index (TI) is set to \_\_\_\_\_ to access the GDT (Global Descriptor Table).

Answer ( Please select your correct option )

**VuAnswers.com**

1

0

-1

-2

**Made by: Waqar Siddhu**

In 68K processors there is a ..... program counter (PC) that holds the address of currently executing instruction.

Answer ( Please select your correct option )

**VuAnswers.com**

8 bit

16 bit

32 bit

64 bit

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An 8 x 16 font is stored in \_\_\_\_\_ bytes.

Answer ( Please select your correct option )

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2

4

8

16

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Which of the following interrupt is used in debugging with the help of trap flag?

Answer ( Please select your correct option )

**VuAnswers.com**

INT 0

INT 1

INT 2

INT 3

**Made by: Waqar Siddhu**

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer ( Please select your correct option )

**VuAnswers.com**

19



20



21

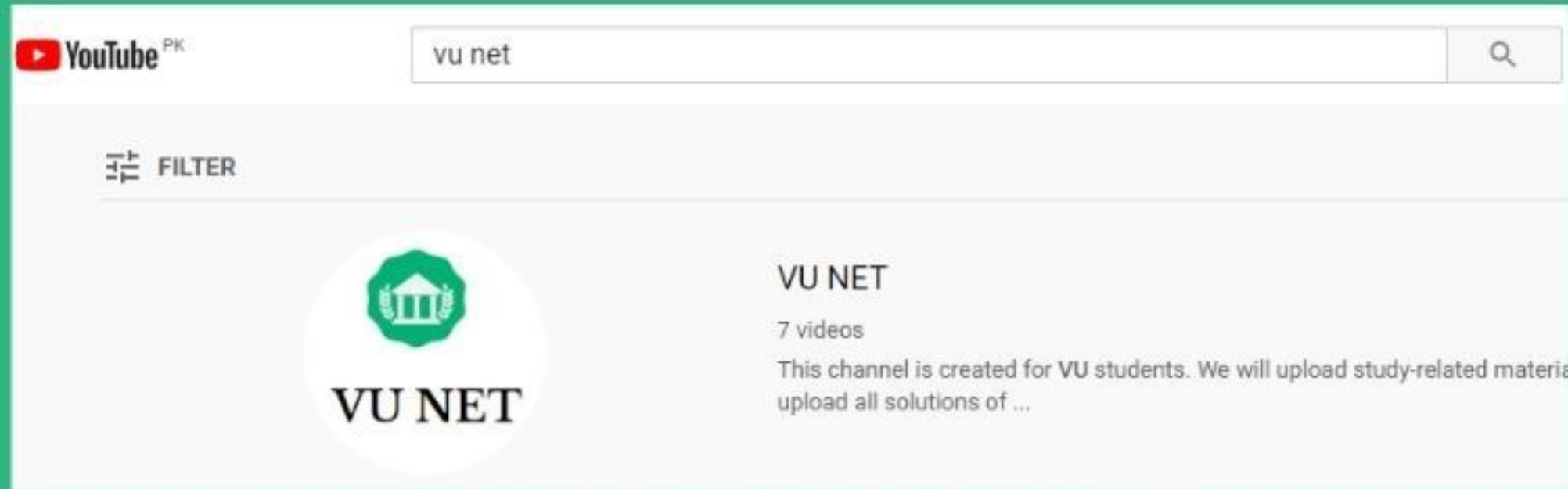


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