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CS401 QUIZ 1 SOLVED

Provide by VU Answer

1. In programmable interrupt controller which of the following ports is referred as interruptmask register?

- a) 19
- b) 20
- c) 21**
- d) 22

2. ___ is the highest priority interrupt in interrupt controller

- a) IRQ 0**
- b) IRQ 1
- c) IRQ 2
- d) IRQ 3

3. IRET returns on the basis of ___ and ___

- a) CS, IP**
- b) DS, IP
- c) CS, SS

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d) IP, SP

4. If ___ is set, the after every instruction a type 1 interrupt will be automatically generated.

a) Parity flag

b) Trap flag

c) Carry flag

d) Overflow flag

5. The interrupt handler uses ___ instruction to return back to the caller.

a) IRET

b) RETI

c) INTR

d) RET

6. Which of the following interrupt is used for Arithmetic overflow?

a) INT 1

b) INT 2

c) INT 3

d) INT 4

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7. During the execution of INT instruction, some contents are pushed on to the stack, the order of pushing then is

- a) CS, IP and then FLAGS register
- b) IP, CS and then FLAGS register
- c) FLAGS register, CS and then IP**
- d) FLAGS register, IP and the CS

8. Each of the bits at port _ corresponding to one of the IRQ lines.

- a) 18
- b) 20
- c) 21**
- b) 19

9. Which of the following pins of the parallel port connector are grounded? a. a) 10-18

- b) 18-25**
- c) 25-32
- d) 32-39

10. Which of the following interrupts plays the most significant part during step debugging of a program?

- a) INT 0

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b) INT 1

c) INT 2

d) INT 3

11. Which of the following IRQs is connected to serial port COM 2?

a) IRQ 0

b) IRQ 1

c) IRQ 2

d) IRQ 3

12. Which of the following IRQs is connected to serial port COM 1?

a) IRQ 4

b) IRQ 5

c) IRQ 6

d) IRQ 7

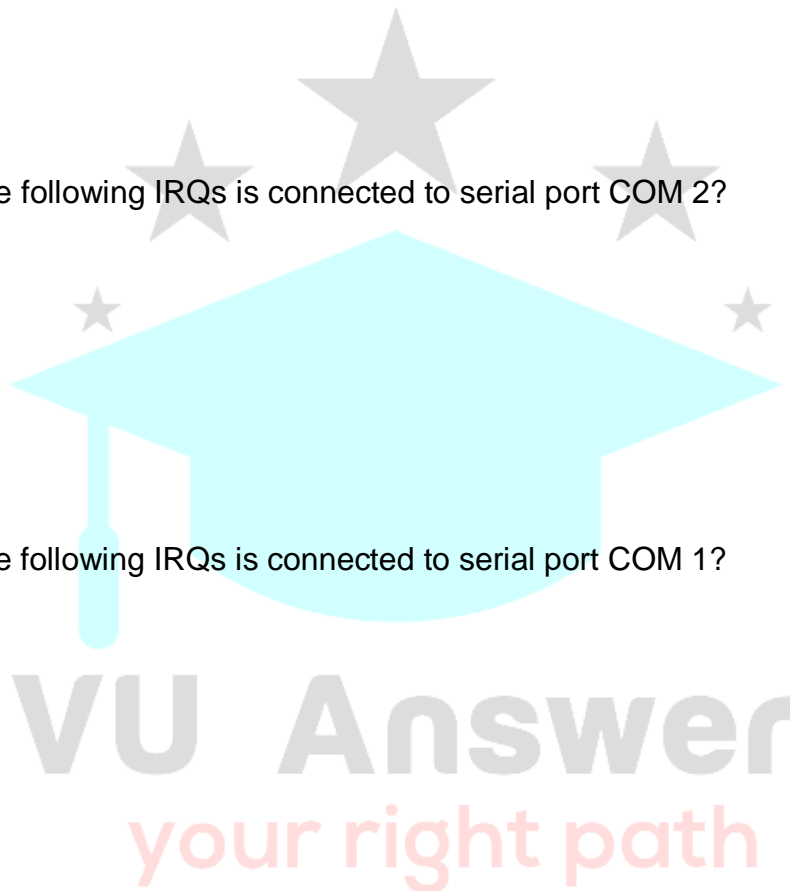
13. Which of the following IRQs is used by the parallel port?

a) IRQ 4

b) IRQ 5

c) IRQ 6

d) IRQ 7



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14. In programmable interrupt controller which of the following ports is used to selectively enabling or disabling interrupts?

a) 19

b) 20

c) 21

d) 22

15. The space where all the registers of task are stored is called ____

a) Control block

b) Process control block

c) Stack

d) Memory

16. Which of the following is the destination register in IN instruction?

a) AL or AX

b) BL or BX

c) CL or CX

d) DL or DX

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17. In multitasking environment, which of the following structure is used to maintain the ordering of active PCBs?

- a) Array
- b) Register
- c) Linked List**
- d) Stack

18. In multitasking which of the following interrupts is used as scheduler during context switching?

- a) INT 21
- b) INT 16
- c) INT 13
- d) INT 8**

19. Which of the following are required for thread entry?

- a) CS and DS
- b) CS and IP**
- c) IP and general-purpose registers
- d) SS and SP

20. The time interval between two timer tick is?

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- a) 40 ms
- b) 45 ms
- c) 50 ms
- d) 55 ms**

21. Which of the following flag can be used for mathematical operations?

- a) Direction Flag
- b) Carry Flag**
- c) Trap Flag
- d) Interrupt Flag

22. In multi-tasking, the process of saving and restoring to values of registers from a process control block (PCB) is called ____

- a) Context switching**
- b) Context saving
- c) Context restoring
- d) Code switching

23. When two devices in a system want to use the same IRQ line, is referred as:

- a) IRQ Collision
- b) IRQ Conflict**

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- c) IRQ Crash
- d) IRQ Blockage

24. An End of Interrupt (EOI) signal is sent by

- a) Handler**
- b) Processor
- c) IRQ
- d) PIC

26. Threads can have function calls, parameters and _____ variables.

- a) Global
- b) Local**
- c) Legal
- d) Illegal

27. Which of the following arranges jobs in a sequence in order to be executed?

- a) Process control block**
- b) Arranger
- c) Control unit
- d) Scheduler

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28. Which of the following flags cannot be cleared using an assembly instruction?

- a) Trap flag
- b) Interrupt flag
- c) Direction flag
- d) Carry flag**

29. Which of the following is the highest priority interrupt?

- a) INT3
- b) INT2
- c) INT1
- d) INT0**

30. At the end of servicing an interrupt, which of the following is used to inform the PIC that it is completed?

- a) RET
- b) EOI**
- c) IRET
- d) RET N

31. Which of the following interrupts is used for maintaining the system time?

- a) INT 0

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b) INT 1

c) INT 8

d) INT 10

32. Which of the following is used for exporting parallel port services?

a) INT 17

b) INT 16

c) INT 15

d) INT 8

33. In programmable interrupt controller which of the following ports is referred as a controlport?

a) 19

b) 20

c) 21

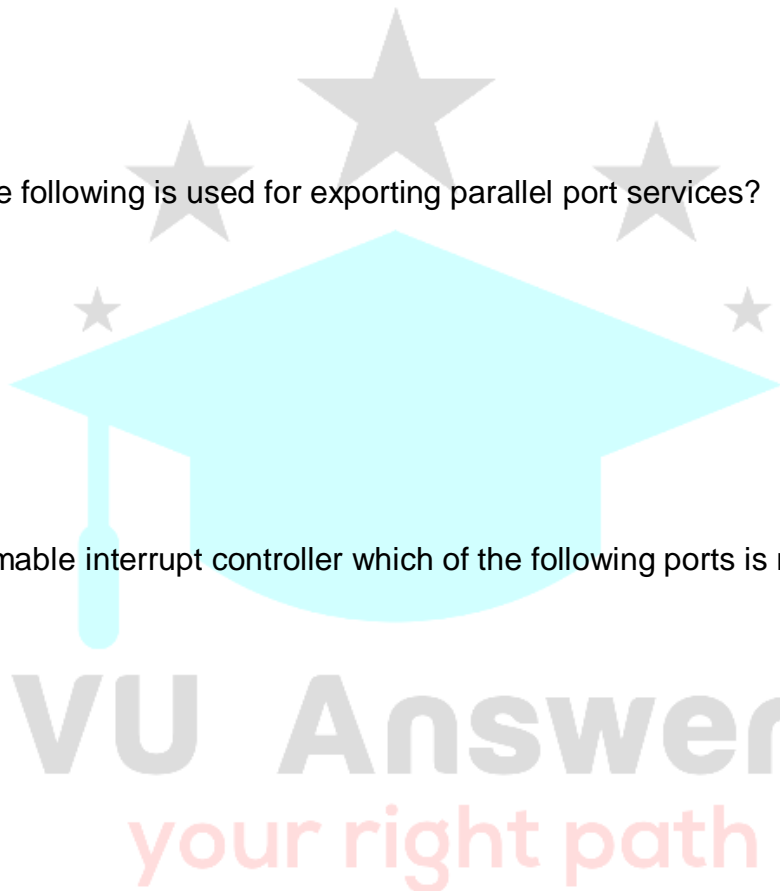
d) 22

34. ___is/are the port number(s) for parallel port.

a) 20 and 21

b) 60 to 64

c) 380



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d) 378

35. The number of pins in a parallel port connector are?

a) 20

b) 25

c) 30

d) 35

36. The offset address of an interrupt n will be at

a) n

b) nx2

c) nx3

d) nx4

37. Programmable interrupt controller has two ports (20 and 21). Port 20 is the control port while port 21 is_.

a) The interrupt mask register

b) Interrupt port

c) Output port

d) Input port

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40. Which of the following instruction selects memory address space?

a) MOV

b) DEC

c) IN

d) ADD

41. The thread registration code initializes the PCB and adds it to the linked list so that the ___ will give it a turn.

a) Assembler

b) Scheduler

c) Linker

d) Debugger

42. Which of the following instruction is used for disabling all the interrupts during a program execution?

a) Cli

b) Sti

c) Reti

d) Iret

43. Programmable interrupt controller (PIC) has

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- a) One input signal and eight output signals
- b) One input signal and one output signal
- c) Eight input signals and one output signals**
- d) Eight input signals and eight output signals

44. In 8088 processor, there can be total possible entries in an interrupt vector table.

- a) 256**
- b) 64
- c) 128
- d) 512

45. Each thread can have their own

- a) Execution area
- b) Stack**
- c) Memory
- d) Array

46. The parallel port connector is called?

- a) DB-25**
- b) BD-25
- c) DB-24

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d) BD-24

47. In 8088, the physical memory address for Interrupt Vector Table is fixed and the table occupies exactly _____ of memory.

a) 1 KB

b) 1 MB

c) 64 KB

d) 4 Bytes

48. Which of the following interrupts is non maskable interrupt?

a) INT 0

b) INT 1

c) INT 2

d) INT 3

49. Using OUT instruction on parallel data port results into a signal of ___ for every 1 bit.

a) 0 V

b) 1 V

c) 5 V

d) 10 V

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50. PCB stands for?

a) Process Control Block

b) Process Cleaning Block

c) Programmable Counter Block

d) Programs Control Block

51. Which of the following port number is used to send an end of interrupt (EOI) signal to the PIC after an interrupt is ended?

a) 0x16

b) 0x20

c) 0x60

d) 0x378

52. Which of the following instruction is used for reading a char from keyboard?

a) out al, 0x60

b) in al, 0x60

c) out dx, al

d) out dx, 0x378

53. In PIC, which of the following port is used for selectively enabling or disabling interrupts?

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- a) 19
- b) 20
- c) 21**
- d) 22

54. DOS has single entry point to access all its services through interrupt__

- a) INT 21**
- b) INT 16
- c) INT 2
- d) INT 10

55. Which of the following pins of parallel port are used to carry the data from processor to the device connected through parallel port?

- a) Pin 2 to pin 9**
- b) Pin 10
- c) Pin 4
- d) Pin 18 to pin 25

56. The input frequency of the programmable interval timer (PIT) is

- a) Fixed**
- b) Depends on processor block

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- c) Variable
- d) Depends on hardware attached

60. All registers of task are stored in space called ____

- a) Process storage block
- b) Process control block**
- c) Process terminal
- d) Swapping space

61. Which of the following IRQ's is derived by a key board

- a) IRQ 0
- b) IRQ 1**
- c) IRQ 2
- d) IRQ 3

62. Changing and restoring the state of Control processing unit (CPU) is referred as

- a) Thrashing
- b) Multitasking**
- c) Context Switching
- d) Multithreading

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63. For the external hardware to generate an interrupt there are how many pins outside the processor?

a) 1

b) 2

c) 3

d) 4

64. Which of the following instruction selects peripheral address space?

a) MOV

b) DEC

c) IN

d) ADD

65. Which of the following is the most commonly used port with printer?

a) Serial Port

b) USB port

c) Parallel Port

d) DVI Port

66. ___ allows us to execute one instruction at a time rather than the whole program at once

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a) INT 0

b) INT 1

c) INT 2

d) INT 3

67. Which of the following is used for exporting keyboard services?

a) INT 16

b) INT 11

c) INT 12

d) INT 8

68. TSR stands for

a) Terminate and store routines

b) Transmits and save resources

c) Terminate and Stay resident

d) Truncate and start recursively

69. Which of the following is the ACK pin in DB-25 Connector

a) 10

b) 11

c) 12

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d) 13

70. Which of the following IRQs is a cascading interrupt?

a) IRQ 0

b) IRQ 1

c) IRQ 2

d) IRQ 3

71. In interrupt masking, we use__to get control from the program without letting the program know about it.

a) IRQ 0

b) IRQ 1

c) IRQ 5

d) IRQ 7

72. The output of programmable timer interval is connected to which line of the PIC?

a) IRQ 0

b) IRQ 1

c) IRQ 2

d) IRQ 3

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73. Which of the following IRQs is used for Floppy disk drive?

- a) IRQ 4
- b) IRQ 5
- c) IRQ 6**
- d) IRQ 7

74. In multitasking, each PCB requires__bytes to store the values of registers.

- a) 14
- b) 28**
- c) 32
- d) 16

75. Which of the following IRQs is used for sound card or network card?

- a) IRQ 4
- b) IRQ 5**
- c) IRQ 6
- d) IRQ 7

76. Programmable interrupt controller has____ports.

- a) 1
- b) 2**
- c) 3

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d) 4

77. Which of the following bits of parallel control port enables the IRQ 7 triggering by the ACKpin?

a) Bit 1

b) Bit 2

c) Bit 3

d) Bit 4

78. Which of the following IRQs is derived by a timer device?

a) IRQ 0

b) IRQ 1

c) IRQ 2

d) IRQ 3

79. ___ is a special type of interrupt that returns to the same instruction instead of the next instruction

a) Divide overflow

b) Debug interrupt

c) Arithmetic overflow interrupt

d) Change the sign interrupt

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80. All the registers & stack are saved in

a) Multitasking

b) Multi-Processing

c) Function call

d) BIOS

81. Which of the following is the source register in OUT instruction?

a) AL or AX

b) BL or BX

c) CL or CX

d) DL or DX

82. Which of the following is the destination register in IN instruction?

a) AL or AX

b) BL or BX

c) CL or CX

d) DL or DX

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