

CS401-Assembly Language

FILE-2

Solved MCQS for Final terms papers

Solved by JUNAID MALIK and Team



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- 2) DD
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- 3) Test phase + viva
- 4) Viva preparation
- 5) Final Deliverable

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ALL answers are verified if found any mistake then Correct ACCORDINGLY

1. In programmable interrupt controller which of the following ports is referred as interrupt mask register?
 - a. 19
 - b. 20
 - c. 21
 - d. 22
2. _____ is the highest priority interrupt in interrupt controller
 - a. IRQ 0
 - b. IRQ 1
 - c. IRQ 2
 - d. IRQ 3
3. IRET returns on the basis of _____ and _____
 - a. CS, IP
 - b. DS, IP
 - c. CS, SS
 - d. IP, SP
4. If _____ is set, the after every instruction a type 1 interrupt will be automatically generated.
 - a. Parity flag
 - b. Trap flag
 - c. Carry flag
 - d. Overflow flag
5. The interrupt handler uses _____ instruction to return back to the caller.
 - a. IRET
 - b. RETI
 - c. INTR
 - d. RET
6. Which of the following interrupt is used for Arithmetic overflow?
 - a. INT 1
 - b. INT 2
 - c. INT 3
 - d. INT 4
7. During the execution of INT instruction , some contents are pushed on to the stack , the order of pushing then is
 - a. CS , IP and then FLAGS register
 - b. IP,CS and then FLAGS register
 - c. FLAGS register, CS and then IP
 - d. FLAGS register, IP and the CS
8. Each of the bits at port _____ corresponding to one of the IRQ lines.
 - a. 18
 - b. 19
 - c. 20

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- d. 21
9. Which of the following pins of the parallel port connector are grounded?
- a. 10-18
 - b. 18-25
 - c. 25-32
 - d. 32-39
10. Which of the following interrupts plays the most significant part during step debugging of a program?
- a. INT 0
 - b. INT 1
 - c. INT 2
 - d. INT 3
11. Which of the following IRQs is connected to serial port COM 2?
- a. IRQ 0
 - b. IRQ 1
 - c. IRQ 2
 - d. IRQ 3
12. Which of the following IRQs is connected to serial port COM 1?
- a. IRQ 4
 - b. IRQ 5
 - c. IRQ 6
 - d. IRQ 7
13. Which of the following IRQs is used by the parallel port?
- a. IRQ 4
 - b. IRQ 5
 - c. IRQ 6
 - d. IRQ 7
14. In programmable interrupt controller which of the following ports is used to selectively enabling or disabling interrupts?
- a. 19
 - b. 20
 - c. 21
 - d. 22
15. The space where all the registers of task are stored is called _____
- a. Control block
 - b. Process control block
 - c. Stack
 - d. Memory
16. Which of the following is the destination register in IN instruction?
- a. AL or AX
 - b. BL or BX
 - c. CL or CX
 - d. DL or DX
17. Which of the following interrupts is used for saving and restoring the registers
- a. INT 6
 - b. INT 7

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- c. INT 8
 - d. INT 0
18. In multitasking environment, which of the following structure is used to maintain the ordering of active PCBs?
- a. Array
 - b. Register
 - c. Linked List
 - d. Stack
19. In multitasking which of the following interrupts is used as scheduler during context switching?
- a. INT 21
 - b. INT 16
 - c. INT 13
 - d. INT 8
20. Which of the following are required for thread entry?
- a. CS and DS
 - b. CS and IP
 - c. IP and general purpose registers
 - d. SS and SP
21. The time interval between two timer tick is?
- a. 40 ms
 - b. 45 ms
 - c. 50 ms
 - d. 55 ms
22. Which of the following flag can be used for mathematical operations?
- a. Direction Flag
 - b. Carry Flag
 - c. Trap Flag
 - d. Interrupt Flag
23. In multi tasking, the process of saving and restoring to values of registers from a process control block (PCB) is called _____
- a. Context switching
 - b. Context saving
 - c. Context restoring
 - d. Code switching
24. When two devices in a system want to use the same IRQ line, is referred as:
- a. IRQ Collision
 - b. IRQ Conflict
 - c. IRQ Crash
 - d. IRQ Blockage
25. An End of Interrupt (EOI) signal is sent by
- a. Handler
 - b. Processor
 - c. IRQ
 - d. PIC
26. Which of the following is the BIOS interrupt providing keyboard service?

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- a. INT 0x13
 - b. INT 0x14
 - c. INT 0x15
 - d. INT 0x16
27. Threads can have function calls, parameters and _____ variables.
- a. Global
 - b. Local
 - c. Legal
 - d. Illegal
28. Which of the following arranges jobs in a sequence in order to be executed?
- a. Process control block
 - b. Arranger
 - c. Control unit
 - d. Scheduler
29. Which of the following flags cannot be cleared using an assembly instruction?
- a. Trap flag
 - b. Interrupt flag
 - c. Direction flag
 - d. Carry flag
30. Which of the following is the highest priority interrupt?
- a. INT3
 - b. INT2
 - c. INT1
 - d. INT0
31. At the end of servicing an interrupt, which of the following is used to inform the PIC that it is completed?
- a. RET
 - b. EOI
 - c. IRET
 - d. RET N
32. Which of the following interrupts is used for maintaining the system time?
- a. INT 0
 - b. INT 1
 - c. INT 8
 - d. INT 10
33. Which of the following is used for exporting parallel port services?
- a. INT 17
 - b. INT 16
 - c. INT 15
 - d. INT 8
34. In programmable interrupt controller which of the following ports is referred as a control port?
- a. 19
 - b. 20
 - c. 21
 - d. 22
35. The first parallel port LPT1 has port number designated from _____.

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- a. 178 to 17A
 - b. 278 to 27A
 - c. 378 to 37A
 - d. 478 to 47A
36. _____ is/are the port number(s) for parallel port.
- a. 20 and 21
 - b. 60 to 64
 - c. 380
 - d. 378
37. The number of pins in a parallel port connector are?
- a. 20
 - b. 25
 - c. 30
 - d. 35
38. The offset address of an interrupt n will be at
- a. n
 - b. nx2
 - c. nx3
 - d. nx4
39. Programmable interrupt controller has two ports (20 and 21). Port 20 is the control port while port 21 is _____.
- a. The interrupt mask register
 - b. Interrupt port
 - c. Output port
 - d. Input port
40. Which of the following instruction selects memory address space?
- a. MOV
 - b. DEC
 - c. IN
 - d. ADD
41. The thread registration code initializes the PCB and adds it to the linked list so that the _____ will give it a turn.
- a. Assembler
 - b. Scheduler
 - c. Linker
 - d. Debugger
42. Which of the following instruction is used for disabling all the interrupts during a program execution?
- a. Cli
 - b. Sti
 - c. Reti
 - d. Iret
43. IBM AT has how many PICs (Programmable interrupt controller)
- a. 1
 - b. 2
 - c. 3

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- d. 4
44. Programmable interrupt controller (PIC) has
- a. One input signals and eight output signals
 - b. One input signal and one output signal
 - c. Eight input signals and one output signals
 - d. Eight input signals and eight output signals
45. In 8088 processor, there can be total _____ possible entries in an interrupt vector table.
- a. 256
 - b. 64
 - c. 128
 - d. 512
46. Each thread can have their own
- a. Execution area
 - b. Stack
 - c. Memory
 - d. Array
47. The parallel port connector is called ?
- a. DB-25
 - b. BD-25
 - c. DB-24
 - d. BD-24
48. In 8088, the physical memory address for Interrupt Vector Table is fixed and the table occupies exactly _____ of memory.
- a. 1 KB
 - b. 1 MB
 - c. 64 KB
 - d. 4 Bytes
49. Which of the following interrupts is non maskable interrupt?
- a. INT 0
 - b. INT 1
 - c. INT 2
 - d. INT 3
50. Using OUT instruction on parallel data port results into a signal of _____ for every 1 bit.
- a. 0 V
 - b. 1 V
 - c. 5 V
 - d. 10 V
51. PCB stands for?
- a. Process Control Block
 - b. Process Cleaning Block
 - c. Programmable Counter Block
 - d. Programs Control Block
52. Which of the following interrupts is used in debugging with the trap flag?
- a. INT 0
 - b. INT 1
 - c. INT 2

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- d. INT 3
53. Which of the following port number is used to send an end of interrupt (EOI) signal to the PIC after an interrupt is ended?
- a. 0x16
 - b. 0x20
 - c. 0x60
 - d. 0x378
54. Which of the following instruction is used for reading a char from keyboard?
- a. out al, 0x60
 - b. in al, 0x60
 - c. out dx, al
 - d. out dx, 0x378
55. In PIC, which of the following port is used for selectively enabling or disabling interrupts?
- a. 19
 - b. 20
 - c. 21
 - d. 22
56. DOS has single entry point to access all its services through interrupt _____
- a. INT 21
 - b. INT 16
 - c. INT 2
 - d. INT 10
57. Which of the following pins of parallel port are used to carry the data from processor to the device connected through parallel port?
- a. Pin 2 to pin 9
 - b. Pin 10
 - c. Pin 4
 - d. Pin 18 to pin 25
58. The input frequency of the programmable interval timer(PIT) is
- a. Fixed
 - b. Depends on processor block
 - c. Variable
 - d. Depends on hardware attached
59. All registers of task are stored in space called _____
- a. Process storage block
 - b. Process control block
 - c. Process terminal
 - d. Swapping space
60. Which of the following IRQ's is derived by a key board
- a. IRQ 0
 - b. IRQ 1
 - c. IRQ 2
 - d. IRQ 3
61. The programmable interval timer (PIT) has input frequency of
- a. 1.193MHz

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- b. 2.193MHz
 - c. 3.193MHz
 - d. 4.193MHz
62. Changing and restoring the state of Control processing unit (CPU) is referred as
- a. Thrashing
 - b. Multitasking
 - c. Context Switching
 - d. Multithreading
63. For the external hardware to generate an interrupt there are how many pins outside the processor?
- a. 1
 - b. 2
 - c. 3
 - d. 4
64. Which of the following instruction selects peripheral address space?
- a. MOV
 - b. DEC
 - c. IN
 - d. ADD
65. Which of the following is the most commonly used port with printer?
- a. Serial Port
 - b. USB port
 - c. Parallel Port
 - d. DVI Port
66. _____ allows us to execute one instruction at a time rather than the whole program at once
- a. INT 0
 - b. INT 1
 - c. INT 2
 - d. INT 3
67. Which of the following is used for exporting keyboard services?
- a. INT 16
 - b. INT 11
 - c. INT 12
 - d. INT 8
68. TSR stands for
- a. Terminate and store routines
 - b. Transmits and save resources
 - c. Terminate and Stay resident
 - d. Truncate and start recursively
69. Which of the following interrupts is of highest priority interrupt?
- a. Key Board Interrupt
 - b. Timer Interrupt
 - c. INT 2
 - d. INT 3
70. In older computers, the port number designed for parallel port LPT2 are _____
- a. 078-07A

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- b. 178-17A
 - c. 278-27A
 - d. 378-37A
71. Which of the following is the ACK pin in DB-25 Connector
- a. 10
 - b. 11
 - c. 12
 - d. 13
72. Which of the following IRQs is a cascading interrupt?
- a. IRQ 0
 - b. IRQ 1
 - c. IRQ 2
 - d. IRQ 3
73. In interrupt masking, we use _____ to get control from the program without letting the program know about it.
- a. IRQ 0
 - b. IRQ 1
 - c. IRQ 5
 - d. IRQ 7
74. The output of programmable timer interval is connected to which line of the PIC?
- a. IRQ 0
 - b. IRQ 1
 - c. IRQ 2
 - d. IRQ 3
75. Which of the following IRQs is used for Floppy disk drive?
- a. IRQ 4
 - b. IRQ 5
 - c. IRQ 6
 - d. IRQ 7
76. In multitasking, each PCB requires _____ bytes to store the values of registers.
- a. 14
 - b. 28
 - c. 32
 - d. 16
77. Which of the following IRQs is used for sound card or network card?
- a. IRQ 4
 - b. IRQ 5
 - c. IRQ 6
 - d. IRQ 7
78. Programmable interrupt controller has _____ ports.
- a. 1
 - b. 2
 - c. 3
 - d. 4
79. Which of the following bits of parallel control port enables the IRQ 7 triggering by the ACK pin?
- a. Bit 1

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- b. Bit 2
 - c. Bit 3
 - d. Bit 4
80. Which of the following IRQs is derived by a timer device?
- a. IRQ 0
 - b. IRQ 1
 - c. IRQ 2
 - d. IRQ 3
81. _____ is a special type of interrupt that returns to the same instruction instead of the next instruction
- a. Divide overflow
 - b. Debug interrupt
 - c. Arithmetic overflow interrupt
 - d. Change the sign interrupt
82. All the registers & stack are saved in
- a. Multitasking
 - b. Multi-Processing
 - c. Function call
 - d. BIOS
83. Which of the following is the source register in OUT instruction?
- a. AL or AX
 - b. BL or BX
 - c. CL or CX
 - d. DL or DX
84. Which of the following is the destination register in IN instruction?
- a. AL or AX
 - b. BL or BX
 - c. CL or CX
 - d. DL or DX
- 85. In the context of video service, if we want to set the cursor at top left corner, which of the values will be stored in DH and DL respectively?**
- a. 00.00 page 141
 - d. 00.80
 - e. 25.80
 - f. 0.01
84. Which of the following INT 21 service is used to read character from standard input?
- e. 00
 - b. 01 page 144
 - b. 02
 - c. 03
85. "INT13-BIOS disk service" generally uses which register to return the "error flag"?
- a. CF page 148
 - a. DL
 - b. AH

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- c. AL
- 2 Which of the following service of INT 10 is used to get the video font information?
- a. 1129h
 - b. 1130h page 142
 - d. 1128h
 - e. 1127h
- 2 Which of the following service of INT 10 is used to write graphic pixel on the screen?
- d. 0x0A
 - e. 0x0B
 - c. 0x0C page 144
 - d. 0x0D
- 2 On executing INT 0x21-service 0x3D, if file is successfully opened then
- d. CF will contain 1
 - e. CF will contain 0
 - f. ZF will contain 1
 - g. ZF will contain 0
- 3 On executing INT 0x21-service 0x3D, if file can't be opened then
- d. CF will contain 1
 - e. CF will contain 0
 - f. ZF will contain 1
 - g. ZF will contain 0
62. Device drivers can be divided into _____ major categories
- a. 5
 - b. 4
 - c. 3
 - d. 2
63. We can set the current file position in DOS using service number _____
- a. 0x38
 - b. 0x40
 - c. 0x42 page 154
 - e. 0x43
71. COM1 is accessible via ports _____ while COM2 is accessible via _____
- a. 3FD, 3F9
 - b. 2F8-2FF, 3F8-3FF
 - c. 3F9, 3FD
 - d. 3F8-3FF, 2F8-2FF page 164
- 2 In the context of vides services, if we want to write string then which of the value will be placed in AH?
- a. 10h
 - b. 12h

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- c. 11h
 - d. 13h page 142
79. In the context of video service, if we want to scroll the window up which of the following will be value of AH?
- a. AH = 05h
 - b. AH = 06h page 141
 - a. AH = 07h
 - b. AH = 08h
13. In the context of video services, if we want to set cursor position which of the following will be the value of AH?
- a. AH = 01h
 - b. AH = 02h page 139
 - c. AH = 03h
 - d. AH = 04h
14. Which standard format is used for sending data on the serial port?
- a. Full duplex
 - b. BIOS INT 14
 - c. RS232 page 163
 - d. COM1
15. INT 0x13 service 0x08 is use to _____
- a. Read disk sector
 - b. Write disk sector
 - c. Reset disk sector
 - d. Get drive parameter page 148
16. Parameters pushed to the routine in _____ order with the rightmost being pushed first using language
- a. Reverse page 179
 - b. Proper
 - c. Forward
 - d. Same
17. Parameters pushed to the routine in _____ order with the left most being pushed first using Pascal language
- a. Proper page 179
 - b. Same
 - c. Reverse
 - d. Forward
18. _____ symbol is added to any function or variable using the C language naming convention
- a. Underscore page 177
 - b. #

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- c. &
 - d. @
19. Which of the following BIOS INT provides serial port services
- a. INT 21
 - b. INT 14 page 163
 - c. INT 10
 - d. INT 08
20. A 32-bit address register can access up to _____ of memory
- a. 2 MB
 - b. 2 GB
 - c. 4 MB
 - d. 4 GB page 167
21. BIOS video services are exported via interrupt
- a. INT 10 page 139
 - b. INT 9
 - c. INT 8
 - d. INT 7
22. Which of the following is used to terminate a string in assembly language?
- a. !
 - b. #
 - c. \$
 - d. ;
23. The bits are sent one by one in specially formatted fashion on the _____
- a. Half duplex
 - b. Parallel port
 - c. Full duplex
 - d. Serial port page 163
24. In VESA defined modes _____ bits for every pixel are organized.
- a. 8 color
 - b. 16 color page 172
 - c. 32 color
 - d. 64 color
25. What is the abbreviation VESA _____ and VBE _____
- a. Video Electrical System Association, VGA BIOS Extensions
 - b. Video Electronics Standards Administration , Video Bus Extensions
 - c. Video Electronics Standard Association, Video BIOS Extensions page 172
 - d. VGA Electronics Standard Association, Video BIOS External
26. Which of the following services of INT 21 is used to write a string on standard output?
- a. 00h
 - b. 02h

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- c. 09h page 144
d. 10h
27. In 9pin DB 9 connector, which pin is assigned to RD (Received Data)?
a. 1
b. 2 page 163
c. 3
d. 4
28. The maximum length of DOS command line parameters is
a. 64 bit
b. 127 bits
c. 256 bits
d. 512 bits
29. In C _____ is responsible for removing the parameters.
a. Clear
b. Callee
c. Caller page 179
d. Stack
30. While using INT 10 for writing graphic pixel on screen, which of the following registers hold the information about pixel color?
a. DH
b. DL
c. AH
d. AL page 144
31. As compared to _____, _____ provides more cooked services.
a. BIOS, DOS page 144
b. DOS, OS
c. DOS, BIOS
d. OS, DOS
32. _____ Register is used for storing the base and limit of GDT.
a. LGDT
b. GDTR page 168
c. Null Descriptor
d. LDT
33. INT13-BIOS disk service" generally uses which register to return the "error code"?
a. CF
b. DL
c. AH page 148
d. AL
34. In case of COM File, first command line parameter is stored at _____
offset of 'Program segment prefix'

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- a. 0x80
 - b. 0x82
 - c. 0x84
 - d. 0x86
35. New devices are allowed to work with the existing operating system with the help of _____.
- a. Sockets
 - b. Port addresses
 - c. BIOS
 - d. Device drivers page 158
36. _____ byte of DOS data buffer stores the maximum characters buffer can hold.
- a. First page 144
 - b. Second
 - c. Third
 - d. Last
37. The register size of 386 architecture is _____
- a. 16 bit
 - b. 32 bit page 167
 - c. 20 bit
 - d. 4 GB
38. Com 1 is connected with
- a. IRQ 2
 - b. IRQ 3
 - c. IRQ 4 page 106
 - d. IRQ 5
39. Which of the following services of INT 10 is used for writing the string?
- a. 0x12
 - b. 0x13 page 142
 - c. 0x11
 - d. 0x10
40. The first entry of the GDT must always be zero, it is called the _____
- a. Null Descriptor page 168
 - b. Data Descriptor
 - c. Code Descriptor
 - d. Global Descriptor Table
41. The first sector on hard disk contains the
- a. Hard disk
 - b. Partition table page 150
 - c. Data size

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- d. Sector size
42. BIOS video services are broadly classified into _____ categories
- a. 1
 - b. 2** page 140
 - c. 3
 - d. 4
43. In case of COM file, maximum length of parameters passed through command line can be _____
- a. 63 bytes
 - b. 127 bytes**
 - c. 255 bytes
 - d. 511 bytes
44. In Pascal _____ is responsibility for removing the parameters
- a. Stack
 - b. Clear
 - c. Caller
 - d. Callee** page 179
45. Hard disk MBR (Master Boot record) is of size
- a. 446 bytes** page 150
 - b. 350 bytes
 - c. 512 bytes
 - d. 256 bytes
46. In 9pin DB 9 connector , which pin is assigned to Signal Ground
- a. 3
 - b. 4
 - c. 5** page 163
 - d. 6
47. Two prevalent calling conventions are the _____ and _____
- a. C# calling convention, Java calling convention
 - b. C calling convention, Pascal Calling convention** page 179
 - c. C++ calling convention, Java calling convention
 - d. C++ calling Convention, Python calling Convention
48. Com2 is connected with
- a. IRQ 2
 - b. IRQ 3** page 106
 - c. IRQ 4
 - d. IRQ 5
49. BPB stands for
- a. Basic Parameter Block

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- b. BIOS Precise Block
 - c. Basic Precise Block
 - d. Bios Parameter Block page 159
50. While writing graphic pixel on the screen, which registers hold the value for setting cursor position?
- a. AX, BX
 - b. AX, CX
 - c. BX, CX
 - d. CX, DX
51. The VESA VBE 2.0 standard include a _____ mode which allows direct access to the whole video memory.
- a. Parallel frame buffer
 - b. Liner frame buffer page 172
 - c. Real
 - d. Protected
52. In 9pin DB 9 connector, which pin is assignment to TD (Transmitted Data)?
- a. 1
 - b. 2
 - c. 3
 - d. 4
53. INT 0x13 service 0x03 is used to _____
- a. Read disk sector
 - b. Write disk sector page 148
 - c. Reset disk sector
 - d. Get drive parameter
54. Serial port services are provided by the _____
- a. BIOS INT 11
 - b. BIOS INT 14 page 163
 - c. BIOS INT 13
 - d. RS232
55. Which of the following register hold the page number for using the write string service of INT 10?
- a. AH
 - b. BL
 - c. CL
 - d. BH page 144
56. The "program segment prefix" for com file is of _____ size
- a. 64bytes
 - b. 128 bytes
 - c. 256 bytes

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- d. 512 bytes
57. Which of the following registers are used as DOS input buffer
- a. DS, AX
 - b. SS, SP
 - c. DS, DX page 144
 - d. CS:BX
58. Which register is generally used to specify the service number of an interrupt
- a. DX
 - b. AX page 100
 - c. BX
 - d. CX
59. The root directory of floppy contains _____ fixed entries
- a. 64
 - b. 256
 - c. 128
 - d. 512 page 156
60. In FAT32 file system directory entry in DOS consist of how many bytes?
- a. 16
 - b. 24
 - c. 32 page 147
 - d. 64
61. _____ is the highest priority interrupt in interrupt controller
- a. IRQ 0 page 106
 - b. IRQ 1
 - c. IRQ 2
 - d. IRQ 3
62. Operating system organize data in form of
- a. Folder
 - b. Batch file
 - c. File page 153
 - d. None of given
63. The higher 16 bit of EAX register are labeled as
- a. AX
 - b. EAH
 - c. AH
 - d. None of given
64. _____ is special type of interrupt that returns to the same instruction instead of the next instruction. (page 107)
- Divide overflow interrupt**
65. In Intel 8088, there are total of _____ possible interrupt vectors in an interrupt

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vector table. (page 105)

256

66. Which of the following pins of DB-256 connector are ground? (page 125)

18-25

67. Which of the following interrupts is used for saving and restoring the register? (page141)

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INT 08

68. Which of the following instructions is used to read a character from the keyboard port? (page

INT al, Ox60

69. In Intel 8088, the interrupt vector Table occupies _____ of memory.

1 **KB** NOT Sure **(GOOGLE)**

70. Which of the following interrupt request is used for a floppy disk drive?

(Page 114)

IRQ 6

71. Which of the following interrupt request is used for a parallel ports? (page 114)

IRQ

7

78.. Which of the following is the order of pushing the contents on stack during the execution of INT instruction?

Flags, CS and Then IP

80. Which of the following interrupt request (IRQ) is connected to the serial Ports COM2? (Page 114)

IRQ 3

81. Which of the following interrupt request (IRQ) is connected to the soundcard or the network card or the modem? (Page 114)

IRQ 5

82. Which of the following interrupt request (IRQ) is connected to the serialPorts COM1? (Page 114)

IRQ 4

83. Which of the following Interrupts play the most significance part during single step debugging of a program? (Page 105)

INT 1

84. Which of the following is the highest priority interrupt? (Page 114) **INT 08**

85. _____ An end of interrupt (EOI) signal is sent by the _____ (Page 114)

Interrupt Handler

86. Which of the following flags can be used in mathematical operation? (Page 133)

Carry flag

87.

The thread registration code initializes the process control block (PCB) and adds it to the linked list. The `_then` gives it a turn. (Page 141)

Scheduler

88. Which of the following IRQs is derived by a keyboard?

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(Page 114) **IRQ 1**

89. Which of the following IRQs is the cascading interrupt connected to the output of the second 8451? (Page 114)

IRQ 2

90. The space where all the registers of task are stored is called _____ (page 140)

Process Control Block OR PCB

91. Which of the following is the destination register in IN instruction? (Page 115)

AL OR AX

92. Which of the following interrupt is used for Arithmetic overflow? (page 106) **INT 4**

93. Which of the following interrupt is used for NMI-Non Mask Able? (Page 105) **INT 2**

94. Which of the following interrupt Trap, Single step? (Page 105) **INT 1**

95. Which of the following interrupt Division by zero? (Page 105) **INT 0**

96. IRET returns on the basis of _____ and _____ (Page 104)

CS and IP

97. Which of the following interrupts is used for saving and restoring the registers? (Page 141)

INT 08 OR INT 8

98. The time interval between two timer tick is. (Page 122) **55ms**

99. When two devices in a system want to use the same IRQ line, is referred as. (Page 114)

IRQ Conflict

100. Which of the following is the BIOS interrupt providing keyboard service? (Page 110)

INT 0x16

101. _____ Threads can have function calls, parameters and _____ variables. (Page 141)

LOCAL

102. Which of the following flags cannot be cleared using an assembly instruction?

Trap Flag

103. In older computers, the port number designed for parallel port LPT2 are _____ (Page 125)

278-27A

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104. Which of the following instruction selects memory address space? (Page 115)

MOV

105. Which of the following port number is used to send an end of interrupt (EOI) signal to the PIC after an interrupt is ended? (Page 124)

0x20

106. The parallel port connector is called?
(Page 125) **DB-25**

107. The interrupt mask register which can be used for enabling or disabling interrupts is associated with. (Page 115)

21

108. Which One of the following instruction is used to return from an interrupt service routine? (page 104)

Iret

109.

The first parallel port LPT1 has ports designed from ___ to ___ (page 125)

378,37A

110. ___ is used for exporting keyboard services. (Page 108)

INT 16

110. Which of the following in the priority of interrupt Request (IRQ) 0?
(Page 114)

Highest

111. Each of Bits of Points ___ corresponds to one of the interrupts Request (IRQ) lines. (page 115)

21

112. If the ___ flag is set, then after every instruction a type 1 interrupt will be generated automatically. (page 133)

Trap OR Interrupt

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113

378 _____ Is/are the point number(s) for parallel point. (page 115)

8. Which of the following stand for TSR? (page 121)

Terminate and Stay Resident

9. Which of the following is correct regarding the input frequency of programmable interval timer (PIT)? (Page 122)

It is fixed

10. At the end of servicing an interrupt _____ signal is used to inform the programmable interrupt controller (PIC) about it. (page

114)

EOI

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