

CS401 final term Mcqs

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WITH PROVE ANSWER

ORANGE MONKEY TEAM

Important NOTE:-

Maybe toppers are successful in school or college carrier but backbenchers are successful in their life because toppers run according to books while backbenchers don't follow rules and regulation that is why backbenchers are successful. For life, there are no books and another reason why toppers can't succeed because they fear failure and don't try anything new. Backbenchers don't fear failure as they have faced the failure in their school or college carrier so that is the reason they try new things and at last gain success.

As Abdul Kalam nicely quoted, "The best brains of the nation may be found on the last benches of the classroom"

Translation into Urdu:

ہوسکتا ہے کہ ٹاپرز اسکول یا کالج کیریئر میں کامیاب ہوں لیکن بیک بینچر اپنی زندگی میں کامیاب ہوتے ہیں کیونکہ ٹاپر کتابوں کے مطابق چلتے ہیں جب کہ بیک بینچر قواعد و ضوابط پر عمل نہیں کرتے اسی وجہ سے بیک بینچر کامیاب ہوتے ہیں۔ زندگی کے لیے، کوئی کتابیں نہیں ہیں اور ایک اور وجہ ہے کہ ٹاپرز کامیاب نہیں ہو پاتے کیونکہ وہ ناکامی سے ڈرتے ہیں اور کچھ نیا کرنے کی کوشش نہیں کرتے۔ بیک بینچر ناکامی سے نہیں ڈرتے کیونکہ انہوں نے اپنے اسکول یا کالج کیریئر میں ناکامی کا سامنا کیا ہے یہی وجہ ہے کہ وہ نئی چیزیں آزما رہے ہیں اور آخر کار کامیابی حاصل کرتے ہیں۔

جیسا کہ عبدالکلام نے اچھی طرح سے نقل کیا ہے، "قوم کے بہترین دماغ کلاس روم کے آخری بینچوں پر مل سکتے ہیں۔"



بِسْمِ اللَّهِ الرَّحْمَنِ الرَّحِيمِ

In the name of Allah, the most gracious, the most merciful

1. There are _____ forms of MOVS instruction.

Two.....confirm

2. The program segment prefix for com files is of _____ size.

256 bytes

3. Which of the following instructions, selects the memory address space?

Movconfirm

4. Which of the following instructions, selects the peripheral address space?

IN.....confirm

5. Which of the following instructions to read and write from the peripheral address space?

IN and OUT.....confirm

6. INT instruction takes a _____ byte(s) argument varying from 0-255.

Single(1).....confirm

7. Which of the following interrupt requests (IRQ) is derived by the keyboard when generates an interrupts when a key is pressed or released.

IRQ 1....confirm

8. Which of the following interrupt requests (IRQ) is the cascading interrupt connected to the output of the second 8451 in the machine?

IRQ 2....confirm

9. Which of the following interrupt requests (IRQ) is connected to serial port COM 2?

IRQ 3.....confirm

10. Which of the following interrupt requests (IRQ) is connected to serial port COM 1?

IRQ 4....confirm

11. Which of the following interrupt requests (IRQ) is used by the sound card or network card.

IRQ 5.....confirm

12. Which of the following interrupt requests (IRQ) is used by the floppy disk drive ?

IRQ 6....confirm

13. Which of the following interrupt requests (IRQ) is used by the parallel port?

IRQ 7confirm

14. _____ register is used for storing the base and limit of GDT.

GDTR.....confirm

15. The first entry of the GDT must always be zero. It is called the _____.

Null descriptor....confirm

16. In intel 8088 there are a total of _____ possible interrupt vectors in an interrupt Vector Table.

256....confirm

17. Which register is generally used to specify the service number of an interrupt?

Ax.....confirm from net

18. In the context of video services, if we want to write string then which of the value will be placed in AH?

13h....confirm

19. In 9pin DB 9 connector, which pin is assigned to Signal Ground?

5....confirm

20. In 9pin DB 9 connector, which pin is assigned to Data terminal?

4....confirm

21. In 9pin DB 9 connector, which pin is assigned to transmitted data?

3.....confirm

22. In 9pin DB 9 connector, which pin is assigned to Received data?

2.....confirm

23. In 9pin DB 9 connector, which pin is assigned to Carrier Detect?

1.....confirm

24. In 9pin DB 9 connector, which pin is assigned to Data Set?

6.....confirm

25. In 9pin DB 9 connector, which pin is assigned to request to send (RTS)?

7.....confirm

26. In 9pin DB 9 connector, which pin is assigned to Clear to send (CTS)?

8.....confirm

27. In 9pin DB 9 connector, which pin is assigned to Ring indicator(RI)?

9.....confirm

28. The first parallel port LPT1 has ports designated from _____ to _____.

378 to 37 A.....confirm

29. A parallel port has _____ views

2.....confirm

30. COM2 is connected with

IRQ3....confirm

31. Which of the following service of INT 21 is used to write a string on standard output?

09h.....confirm

32. Which of the following register hold the page number for using the write string service of INT 10?

BH....confirm

33. New devices are allowed to work with the existing operating system with the help of _____.

Device drivers...confirm

34. Which of the following register hold the cursor start and options for using the write string service of INT 10?

CH.....confirm

35. Which of the following register hold the number of lines by which to scroll up for using the write string service of INT 10?

AL....confirm

36. Which of the following register hold the character to display for using the write string service of INT 10?

AL.....confirm

37. Which of the following register hold write mode for using the write string service of INT 10?

AL...confirm

38. The _____ and DPL have the same meaning as in data and code descriptors.

P.....confirm

39. The routine which executes as a result of INT instruction is called _____.

Interrupt service routine.....confirm

40. To declare a character in assembly language, we store its ASCII code in a _____.

Byte....confirm

41. When two devices in a system want to use the same interrupt request (IRQ) line, is referred as IRQ _____.

Conflict....confirm

42. During multitasking _____ is used to get control from the program without letting the program know about it.

IRQ 0....confirm

43. Which of the following is BIOS interrupt providing keyboard services?

INT 0x16.....confirm

44. REPNZ repeats the following instruction while the _____.

ZF is not set....confirm

45. Which of the following is the source register in OUT instruction?

AL or AX....confirm

46. At the end of servicing an interrupt _____ signal is used to inform the Programmable interrupt Controller (PIC) about it.

EOI...confirm

47. INT 0x13 service 0x03 is used to _____.

Write disk sector...confirm

48. INT 0x13 service 0x00 is used to _____.

RESET disk system....confirm

49. INT 0x13 service 0x02 is used to _____.

READ sector into memory

50. INT 0x13 service 0x08 is used to_____.

GET drive parameters....confirm

51. INT 0x21 service 0x01 is used to _____.

READ character from standard input, with echo.....confirm

52. INT 0x21 service 0x09 is used to _____.

Write string to standard output...confirm

53. INT 0x21 service 0x0A is used to _____.

Buffered input....confirm

54. In programmable interrupt Controller (PIC) which of the following ports is used to selectively enable or disable interrupts?

21....confirm

55. In multitasking, which of the following interrupts is used scheduler?

INT 08....confirm

56. Which of the following interrupts is used for saving and restoring the registers?

INT 08....confirm

57. Scrolling is the process of moving one or more lines towards the top or bottom of the screen and this is _____.

Cleared.....confirm

58. Software interrupts are called by extended _____ call mechanism

Far

59. The root directory of floppy contains _____ fixed entries

512....confirm

60. Programmable interrupt controller has _____ ports.

Two(20, 21)...confirm

61. The space where all registers of a task are stored is called _____.

Process control block(PCB).....confirm

62. In the context of video services, if we want to scroll the window up which of the following will be the value of AH?

AH=06h....confirm

63. In the context of video services, if we want to scroll the window down which of the following will be the value of AH?

AH=07h....confirm

64. In the context of video services, if we want to set text-mode cursor shape which of the following will be the value of AH?

AH=01h....confirm

65. In the context of video services, if we want to set cursor position which of the following will be the value of AH?

AH=02h....confirm

66. In the context of video services, if we want to write character and attribute at cursor position which of the following will be the value of AH?

AH=09h....confirm

67. In the context of video services, if we want to write character only at cursor position which of the following will be the value of AH?

AH=0Ah....confirm

68. In the context of video services, if we want to write string which of the following will be the value of AH?

AH=13h....confirm

69. INT instruction takes a ____ byte(s) argument varying from 0-255.

1....confirm

70. Which of the following is the number of pin(s) outside the processor for generating interrupts by the external hardware?

1.....confirm

71. Each entry of the interrupt vector table is of _____ byte(s).

4....confirm

72. Which of the following is the priority of interrupt request (IRQ) 0?

Highest...confirm

73. The interrupt call loads new values in CS, IP and _____.

FLAgS....confirm

74. REP allows the instruction to be repeated _____ times.

CX ...confirm

75. Which of the following interrupt Requests (IRQ) is derived by a timer device?

IRQ 0confirm

76. An END of interrupt (EOI) signal is sent by the_____

Programmable interrupt controller.....confirm

77. Which of the following INT21 service is used to read character from standard input?

AH= 01h.....confirm

78. Which of the following INT21 service is used to write string to standard output?

AH=09h.....confirm

79. Which of the following INT21 service is used to buffered Input?

AH=0Ah....confirm

80. Which of the following instructions, select the peripheral address space?

IN.....confirm

81. Software interrupts are called by extended _____ call mechanism

Far....confirm

82. Which of the following is the interrupt number for debug interrupt?

Int 3.....confirm

83. In intel 8088, the interrupt vector table occupies _____ of memory

4 bytes....confirm

84. The register size of 386 architecture is _____.

32 bit.....confirm

85. Which of the following instructions is used to disable all interrupts during the execution of a program?

cli.....confirm

86. In case of COM file, first command line parameter is stored at _____ offset of program segment prefix?

0x80....confirm

87. _____ is a special type of interrupt that returns to the same instruction instead of the next instruction.

Divide overflow interrupt.....confirm

88. Which of the following is the order of pushing the contents on stack during the execution of INT instruction?

CS, IP and flags.....confirm

89. The output of Programmable interval Timer (PIT) is connected to the ____ line of programmable interrupt controller (PIC).

IRQ 0....confirm

90. Which of the following formulae is used for calculating the segment address of an interrupt service routine “n”?

$N*4+2$confirm

91. The thread registration code initializes the Process Control Block (PCB) and adds it to the linked list. The ____, then gives it a return.

Scheduler.....confirm

92. Interrupts are _____ and _____

Asynchronous , unpredictable....confirm

93. The execution of the instruction `mov word [ES:160] 0x1230` will print a character on screen at the _____ column of the _____ row.

First, second....confirm

94. Which of the following formulae calculates the desired location on the screen?

$\text{Location} = (\text{rowno} * 80 + \text{column}) * 2$

95. The clear screen operation initializes the whole block of video memory to _____.

0x0720.....confirm

96. In programmable interrupt controller, which of the following ports is the control port?

Port 20....confirm

97. The convention to return a value from a subroutine is to use the _____ register.

AX.....confirm

98. The parallel port connector is called _____

DB-25.....confirm

99. Which of the following is the number of pin(s) outside the processor for generating interrupts by the external hardware?

1.....confirm

100. Which of the following is the time interval between two timer ticks?

55ms.....confirm

101. Which of the following IRQs is used for sound card or network card ?

IRQ 5.....confirm

102. _____ is used for exporting keyboard services.

INT 11....confirm

103. If the _____ flag is set, then after every instruction, a type 1 interrupt will be generated automatically.

Trap....confirm

104. On executing INT 0x21 service 0x3D, if file is successfully opened then

ZF will contain 0....confirm from net

105. On executing INT 0x21 service 0x3D, if file can't be opened then

ZF will contain 1....confirm from net

106. BPB stands for_____.

Bios parameter block.....confirm

107. Which of the following is the destination register in IN instruction?

AL or AX....confirm

108. Interrupts are _____ events

Synchronous

109. The correlation process from the interrupt number to the interrupt handler uses a table called _____.

Interrupt vector table....confirm

110. Which of the following services of INT 10 is used to get the video font information?

1130h....confirm

111. In case of COM file, maximum length of parameters passes through command line can be _____.

255 bytes

112. Programmable interrupt Controller (PIC) has _____ input signal(s) and _____ output signal(s).

Eight, one....confirm

113. Which of the following is used to clear the direction flag?

Cld.....confirm

114. Which of the following is used to set direction flag?

Std.....confirm

115. Which of the following is the ACK pin in DB-25 connector?

10.....confirm

116. BIOS video services are broadly classified into _____ categories.

2.....confirm

117. In multitasking environment, which of the following structure is used to maintain the order of active process control blocks (PCB)?

Register.....confirm

118. _____ is a special instruction that loads a general purpose register, and a segment register from two consecutive memory locations.

LES.....confirm

119. Programmable interrupt Controller (PIC) has _____ input signal(s) and _____ output signal(s).

Eight, one....confirm

120. Which of the following interrupts plays the most significant part during single step debugging of a program?

INT 1.....confirm

121. Iret returns on the basis of _____ and _____.

Cs,IP.....confrim

122. Total number of reserved interrupts in Intel 8088 are

255.....confirm

123. The 8088 processor divides interrupts into _____ classes.

Two...confirm

124. As compared to _____, _____ provides more cooked services.

BIOS, DOS.....confirm

125. The IDTR is a _____ bit register similar in structure to the GDTR

48.....confirm

126. INT13-BIOS disk services generally uses which register to return the error flag?

CF.....confirm

127. DOS has a single entry point to access all of its services through _____.

INT 21.....confirm

128. Which of the following flags can be used in mathematical operations?

Carry flag....confirm

129. What is the purpose of the following two instructions? MOV AX, 0XB800 MOV EX,AX
POINTS ES to video base.....confirm

130. Which of the following instruction is used to return from an interrupt service routine?
Iret....confirm

131. Which of the following pins of DB-25 connector are ground?
18-25.....confirm

132. INT13-BIOS disk services generally uses which register to return the error code?
AH.....confirm

133. IBM AT has _____ Programmable interrupt Controllers (PIC)
2....confirm

134. WHICH OF the following interrupts is used to maintain the system time?
INT 8.....confirm

135. Which of the following flags is cleared with the help of stack?
Direction flag.....confirm

136. The register size of 386 architecture is _____.
32 bit.....confirm

137. After the execution of STOSB and STOSW the CX register will be _____ by _____.
Decrementd by 1.....confirm

138. Which of the following is the highest priority interrupt?
INT 08.....confirm

139. There is an auto-increment mode when the _____ is _____.
DF, set.....confirm

140. Which of the following services of INT 10 is used for writing the string?

0x13.....confirm

141. Changing and restoring the state of Central Processing Unit (CPU) is called.

Multitasking

142. Which of the following stands for TSR?

Terminate and stay resident....confirm

143. After the execution of STOSB and STOSW, the CX register will be _____ by _____.

Decrement by 1....confirm

144. While writing graphics pixel on the screen, which registers hold the value for setting cursor position?

CX, DX...confirm from net

145. Which of the following BIOS INT provides serial port services.

INT 14.....confirm

146. Which of the following stands for TSR?

Terminate and stay resident.....confirm

147. PCB stands for _____.

Process control block....confirm

148. The bits are sent one by one in a specially formatted fashion on the _____.

Serial port...confirm

149. In assembly language _____ is used to load a character in AL.

Lodsb.....confirm

150. Which of the following services of INT 10 is used to write graphic pixel on the screen?

0x0C.....confirm

151. COM1 is accessible via ports _____ while COM2 is accessible via _____.

358-3FF, 258-2FF.....confirm

152. Port ____ is used to send an END of interrupt (EOI) signal to the programmable interrupt Controller (PIC) after an interrupt has ended.

0x20.....confirm

153. _____ is a special prefix that is used to repeat the block instructions.

REP....confirm

154. COM1 is connected with

IRQ4....confirm

155. BIOS video services are exported via interrupt.

INT 10.....confirm

156. In the context of video services, if we want to set the cursor at top left corner, which of the values will be stored in DH and DL respectively?

00.00.....confirm

157. REPE repeats a string instruction while the _____ flag is set.

Zero...confirm

158. Device drivers can be divided into _____ major categories.

2.....confirm

159. A 32-bit address register can access up to ____ of memory

4GB....confirm

160. _____ is used in debugging along with the trap flag.

INT 1.....confirm

161. Push and pop operations always operate on _____.

Words.....confirm

162. Which of the following instructions is used to read a character from the keyboard port?

In al, 0x60.....confirm

163. REP with MOVS utilizes the _____ power of a processor to do scrolling in minimum time.

Full....confirm

164. The programmable interval timer (PIT) has input frequency of _____.

1.19318 MHZ.....confirm



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CS401-Assembly Language FINAL TERM MCQS

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ALL answers are verified if found any mistake then Correct ACCORDINGLY

1. Suppose AL contains 5 decimal then after two left shifts produces the value as

- 5
- 10
- 15
- 20 (Page 52)

2. In graphics mode a location in video memory corresponds to a _____ on the screen.

- Line
- Dot (Page 149)
- Circle
- Rectangle

3. Creation of threads can be

- Static
- Dynamic (Page 141)
- Easy
- Difficult

4. The thread registration code initializes the PCB and adds it to the linked list so that the _____ will give it a turn.

- Assembler
- Scheduler (Page 141)
- linked
- Debugger

5. VESA VBE 2.0 is a standard for

- High resolution Mode (Page 180)
- Low resolution Mode
- Medium resolution Mode
- Very High Resolution Mode

6. Which of the following gives the more logical view of the storage medium

- BIOS
- DOS (Page 155)
- Both
- None

7. Which of the following IRQs is derived by a key board?

- IRQ 0
- IRQ 1 (Page 114)
- IRQ 2

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- IRQ 3

8. Which of the following IRQs is used for Floppy disk drive?

- IRQ 4
- IRQ 5
- IRQ 6 (Page 114)
- IRQ 7

9. Which of the following pins of a parallel port connector are grounded?

- 10-18
- 18-25 (Page 125)
- 25-32
- 32-39

10. The physical address of IDT (Interrupt Descriptor Table) is stored in _____

- GDTR
- IDTR (Page 182)
- IVT
- IDTT

11. In NASM an imported symbol is declared with the while and exported symbol is declared

With the.....

- Global directive, External directive
- External directive, Global directive (Page 189)
- Home Directive, Foreign Directive
- Foreign Directive, Home Directive

12. In 68K processors there is a program counter (PC) that holds the address of currently executing

Instruction

- 8bit
- 16bit
- 32bit (Page 191)
- 64bit

13. To reserve 8-bits in memory ____ directive is used.

- db. (Page 25)
- dw
- dn
- dd

14. In the “mov ax, 5” 5 is the _____ operand.

- Source (Page 18)
- Destination
- Memory

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- Register
15. RETF will pop the segment address in the
- CS register (Page 72)
 - DS register
 - SS register

 - ES register
16. For the execution of the instruction “DIV BL”, the implied dividend will be stored in
- AX (Page 85)
 - BX
 - CX
 - DX
17. When a number is divided by zero “A Division by 0” interrupt is generated. Which instruction is used for this?
- Purpose
- INT 0
 - INT 1
 - INT 2
 - This interrupt is generated automatically (Page 107)
18. INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____
Register.
- AL (Page 152)
 - BL
 - CL
 - BH
19. BIOS sees the disks as
- Logical storage
 - Raw storage (Page 155)
 - In the form of sectors only
 - In the form of tracks only
20. In 9pin DB 9, which pin number is assigned to CD (Carrier Detect)?
- 1 (Page 171)
 - 2
 - 3
 - 4
21. In 9pin DB 9, Signal ground is assigned on pin number
- 4
 - 5 (Page 171)
 - 6
 - 3
22. In 9pin DB 9, RI (Ring Indicator) is assigned on pin number
- ▶ 6
 - ▶ 7

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- ▶ 8
 - ▶ 9 (Page 171)
23. Motorola 68K processors have..... 23bit general purpose registers.
- ▶ 4
 - ▶ 8
 - ▶ 16 (Page 191)
 - ▶ 32
24. When two devices in the system want to use the same IRQ line then what will happen?
- ▶ An IRQ Collision
 - ▶ An IRQ Conflict (Page 114)
 - ▶ An IRQ Crash
 - ▶ An IRQ Blockage
25. in the instruction MOV AX, 5 the number of operands are
- ▶ 1
 - ▶ 2 (Page 25)
 - ▶ 3
 - ▶ 4
26. Which flags are NOT used for mathematical operations?
- ▶ Carry, Interrupt and Trap flag.
 - ▶ Direction, Interrupt and Trap flag. (Page 133)
 - ▶ Direction, Overflow and Trap flag.
 - ▶ Direction, Interrupt and Sign flag
27. The physical address of the stack is obtained by
- ▶ SS: SI combination
 - ▶ SS: SP combination (Page 68)
 - ▶ ES: BP combination
 - ▶ ES: SP combination
28. Value of AH in the write Graphics pixel service is
- ▶ 0Ch (Page 152)
 - ▶ 0Bh
 - ▶ 1Ch
 - ▶ 2Ch
29. Threads can have function calls, parameters and _____ variables.
- ▶ Global
 - ▶ Local (Page 141)
 - ▶ Legal
 - ▶ Illegal
30. Creation of threads can be
- ▶ Static
 - ▶ Dynamic (Page 141) rep
 - ▶ Easy

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- ▶ Difficult
31. How many prevalent calling conventions do exist?
- ▶ 1
 - ▶ 2 (Page 187)
 - ▶ 3
 - ▶ 4
32. VESA VBE 2.0 is a standard for
- ▶ High resolution Mode (Page 180) rep
 - ▶ Low resolution Mode
 - ▶ Medium resolution Mode
 - ▶ Very High resolution Mode
33. The serial port connection is a ----- connector
- ▶ 9pin DB 9 (Page 171)
 - ▶ 8pin DB 9
 - ▶ 3pin DB 9
 - ▶ 9pin DB 5
34. Which of the following gives the more logical view of the storage medium
- ▶ BIOS
 - ▶ DOS (Page 55) rep
 - ▶ Both
 - ▶ None
35. In STOSB instruction, when DF is clear, SI is
- ▶ Incremented by 1 (Page 92)
 - ▶ Incremented by 2
 - ▶ Decrement by 1
 - ▶ Decrement by 2
36. After the execution of STOSW the CX will be
- ▶ Decrement by 1 (Page 92)
 - ▶ Decrement by 2
 - ▶ Increment by 1
 - ▶ Increment by 2
37. IRQ is referred to
- ▶ Eight input signals
 - ▶ One output signal
 - ▶ One input signals
 - ▶ Eight output signals
38. Which of the following IRQs is derived by a key board?
- ▶ IRQ 0
 - ▶ IRQ 1 (Page 113) rep
 - ▶ IRQ 2
 - ▶ IRQ 3
39. Which of the following IRQs is connected to serial port COM 1?
- ▶ IRQ 4 (page 114)
 - ▶ IRQ 5
 - ▶ IRQ 6

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- ▶ IRQ 7
40. The physical address of IDT (Interrupt Descriptor Table) is stored in _____
- ▶ GDTR
 - ▶ IDTR (Page 182) rep
 - ▶ IVT
 - ▶ IDTT
41. Assembly language is:
- ▶ Low-level programming language (Page 3)
 - ▶ High-level programming language
 - ▶ Also known as machine language
 - ▶ not considered closer to the computer
42. The number of bits required to access 1MB of memory are
- ▶ 16 bits
 - ▶ 32 bits
 - ▶ depends on the processor architecture (Page 20)
 - ▶ 20 bits
43. In STOSB instruction, SI is decremented or incremented by
- ▶ 3
 - ▶ 2
 - ▶ 1 (Page 92)
 - ▶ 4
44. In programmable interrupt controller, which of the following ports is referred as a control port.
- ▶ 19
 - ▶ 20 (Page 115)
 - ▶ 21
 - ▶ 22
45. INT 21 service 01H is used to read character from standard input with echo. It returns the result In _____ register.
- ▶ AL (Page 152) rep
 - ▶ BL
 - ▶ CL
 - ▶ BH
46. In device attribute word, which of the following bit decides whether it is a character device or a block device
- ▶ Bit 12
 - ▶ Bit 13
 - ▶ Bit 14
 - ▶ Bit 15 (Page 116)
47. In 9pin DB 9, which pin number is assigned to CTS (Clear to send)?
- ▶ 6
 - ▶ 7
 - ▶ 8 (Page 171)
 - ▶ 9
48. In 9pin DB 9, which pin number is assigned to RD (Received Data)?
- ▶ 1
 - ▶ 2 (Page 171)

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- ▶ 3
 - ▶ 4
49. VESA (Video Electronics Standards Association) organizes 16 color bits for every pixel in
- ▶ 5:5:5 format
 - ▶ 5:6:5 format (Page 180)
 - ▶ 6:5:6 format
 - ▶ 5:6:7 format
50. Motorola 68K processors have..... 23bit general purpose registers.
- ▶ 4
 - ▶ 8
 - ▶ 16 (Page 191) rep
 - ▶ 32
51. Programmable Interrupt Controller (PIC) has
- ▶ One input signals and eight output signals
 - ▶ One input signal and one output signal
 - ▶ Eight input signals and one output signals (Page 113)
 - ▶ Eight input signals and eight output signals
52. Video services are classified into..... broad categories.
- ▶ 5
 - ▶ 4
 - ▶ 3
 - ▶ 2 (Page 149)
53. BL contains 5 decimal then after right shift, BL will become
- 3
 - 2.5 (Page 52)
 - 5
 - 10
54. 8 * 16 font is stored in _____ bytes.
- 3
 - 4
 - 8
 - 16 (Page 150)
55. In DOS input buffer, number of characters actually read on return is stored in
- First byte
 - Second byte (Page 152)
 - Third byte
 - Fourth byte
56. IRQ 0 has priority
- Low
 - High
 - Highest (Page 114)
 - Medium
57. Thread registration code initialize PCB and add to linked list so that _____ will give it turn.
- Assembler
 - Linker

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➤ Scheduler (Page 141) rep

➤ Debugger

58... Traditional calling conventions are in _____ number

➤ 1

➤ 2 (Page 187)

➤ 3

➤ 4

59. VESA VEB 2.0 is standard for

➤ High Resolution Mode (Page 180) rep

➤ Low Resolution Mode

➤ Very High Resolution Mode

➤ Medium Resolution Mode

60. to clear direction flag which instruction is used?

➤ Cld (Page 92)

➤ Clrd

➤ Cl df

➤ Clr df

61. In STOSW instruction, When DI is cleared, SI is

➤ Incremented by 1

➤ Incremented by 2 (Page 92)

➤ Decrement by 1

➤ Decrement by 2

62 Interrupt that is used in debugging with help of trap flag is

➤ INT 0

➤ INT 1 (Page 105)

➤ INT 2

➤ INT 3

63. INT for arithmetic overflow is

➤ INT 1

➤ INT 2

➤ INT 3

➤ INT 4 (Page 106)

64. IRQ referred as

➤ Eight Input signals (Page 113) rep

➤ One Input signal

➤ Eight Output signals

➤ One output signal

65. IRQ for keyboard is

1 (Page 114)

66. IRQ for sound card is

5 (Page 114)

67. IRQ for floppy disk is

6 (Page 114)

68. IRQ with highest priority is

➤ Keyboard IRQ

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➤ Timer IRQ (Page 114)

- Sound Card
- Floppy Disk

69. Pin for parallel port ground is

- 10-18
- 18-25 (Page 125) rep
- 25-32
- 32-39

70. The physical address of Interrupt Descriptor Table (IDT) is stored in

- GDTR
- IDTR (Page 182) rep
- IVT
- IDTT

71. CX register is

- Count register (Page 15)
- Data register
- Index register
- Base register

72. OUT instruction uses

- AX or AL as source register. (Page 115)

73. IN DB-9 connector the Data Set ready pin is at

- 5
- 6 (Page 171)
- 7
- 8

74. If two devices uses same IRQ then there is

- IRQ collision
- IRQ conflict (Page 114) rep
- IRQ drop

75. VESA organizes 16 bit color for every pixel in ratio

- 5:5:5
- 5:6:5 (Page 180) rep
- 6:5:6
- 5:6:

76. SP is associated with..... By default

- ▶ SS (Page 34)
- ▶ DS
- ▶ CS
- ▶ ES

77. Which bit of the attributes byte represents the red component of foreground color?

- ▶ 5
- ▶ 4
- ▶ 3
- ▶ 2 (Page 81)

78. An 8 x 16 font is stored in _____ bytes.

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- ▶ 2
 - ▶ 4
 - ▶ 8
 - ▶ 16 (Page 150) rep
79. In DOS input buffer, the number of characters actually read on return is stored in ___ byte.
- ▶ Third
 - ▶ Fourth
 - ▶ First
 - ▶ Second (Page 152) rep
80. Which of the following gives the more logical view of the storage medium
- ▶ BIOS
 - ▶ DOS
 - ▶ Both
 - ▶ None
81. In STOSW instruction, when DF is clear, SI is
- ▶ Incremented by 1
 - ▶ Incremented by 2 (Page 92)
 - ▶ Decrement by 1
 - ▶ Decrement by 2
82. Which of the following interrupts is Non mask able interrupt
- ▶ INT 2 (Page 105)
 - ▶ INT 3
 - ▶ INT 0
 - ▶ INT 1
83. Which of the following IRQs is connected to serial port COM 2?
- ▶ IRQ 0
 - ▶ IRQ 1
 - ▶ IRQ 2
 - ▶ IRQ 3 (Page 114)
84. The time interval between two timer ticks is?
- ▶ 40ms
 - ▶ 45ms
 - ▶ 50ms
 - ▶ 55ms (Page 122)
85. The physical address of IDT (Interrupt Descriptor Table) is stored in _____
- ▶ GDTR
 - ▶ IDTR (Page 182) rep
 - ▶ IVT
 - ▶ IDTT
86. In NASM an imported symbol is declared with thewhile and exported symbol is declared With the.....
- ▶ Global directive, External directive
 - ▶ External directive, Global directive (Page 189) rep
 - ▶ Home Directive, Foreign Directive
 - ▶ Foreign Directive, Home Directive

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87. In 68K processors there is a 32bit that holds the address of currently executing instruction

- ▶ Program counter (Page 191)
- ▶ Stack pointer
- ▶ Register
- ▶ Stack

88. Single step interrupt is

- ▶ Hardware interrupt
- ▶ like divide by zero interrupt (Page 133)
- ▶ like divide by 1 interrupt
- ▶ Software interrupt

89. Which of the following is NOT true about registers:

- ▶ their operation is very much like memory
- ▶ Intermediate results may also be stored in registers
- ▶ They are also called scratch pad ram
- ▶ None of given options

90. Types of jump are:

- ▶ Short, near
- ▶ Short, near, far (Page 45)
- ▶ near, far
- ▶ short, far

91. MS DOS uses ____ display mode.

- ▶ Character based (Page 79)
- ▶ Graphics based
- ▶ Numeric based
- ▶ Console based

92. Which of the following IRQs is derived by a timer device?

- ▶ IRQ 0 (Page 114)
- ▶ IRQ 1

- ▶ IRQ 2
- ▶ IRQ 3

93. In programmable interrupt controller, which of the following ports is referred as a control port?

- ▶ 19
- ▶ 20 (Page 115) rep
- ▶ 21
- ▶ 22

94. INT 21 service 01H is used to read character from standard input with echo. It returns the result in ____ register.

- ▶ AL (Page 152) rep
- ▶ BL
- ▶ CL
- ▶ BH

95. In 9pin DB 9, which pin number is assigned to DSR (Dataset Ready)?

- ▶ 4
- ▶ 5
- ▶ 6 (Page 171)

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- ▶ 7
96. In 9pin DB 9, which pin number is assigned to TD (Transmitted Data)?
- ▶ 1
 - ▶ 2
 - ▶ 3 (Page 171)
 - ▶ 4
97. In 9pin DB 9, Signal ground is assigned on pin number
- ▶ 4
 - ▶ 5 (Page 171)
 - ▶ 6
 - ▶ 3
98. 8088 is a.....
- ▶ 16 bit processor (Page 14)
 - ▶ 32 bit processor
 - ▶ 64 bit processor
 - ▶ 128 bit processor
99. The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).
- ▶ 1
 - ▶ 0 (Page 175)
 - ▶ -1
 - ▶ -2
100. VESA (Video Electronics Standards Association) organizes 16 color bits for every pixel in
- ▶ 5:5:5 format
 - ▶ 5:6:5 format (Page 180) rep
 - ▶ 6:5:6 format
 - ▶ 5:6:7 format
101. Which flags are NOT used for mathematical operations?
- ▶ Carry, Interrupt and Trap flag.
 - ▶ Direction, Interrupt and Trap flag. (Page 133) rep
 - ▶ Direction, Overflow and Trap flag.
 - ▶ Direction, Interrupt and Sign flag
102. When a 32 bit number is divided by a 16 bit number, the quotient is of
- 32 bits
 - 16 bits (Page 85)
 - 8 bits
 - 4 bits
103. in the instruction MOV AX, 5 the number of operands are
- 1
 - 2 (Page 25) rep
 - 3
 - 4
104. In DOS input buffer, number of characters actually read on return is stored in
- First byte
 - Second byte (Page 152) rep
 - Third byte
 - Fourth byte

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105. VESA VEB 2.0 is standard for

- High Resolution Mode (Page 180) rep
- Low Resolution Mode
- Very High Resolution Mode
- Medium Resolution Mode

106. IN DB-9 connector the Data Set ready pin is at

- 5
- 6 (Page 171) rep
- 7
- 8

107. Threads can have function calls, parameters and variables.

- Global
- Local (Page 141)
- Legal
- Illegal

108. How many prevalent calling conventions do exist?

- 1
- 2 (Page 187) rep
- 3
- 4

109. In 9pin DB 9 DSR is assigned on pin number

- 4
- 5
- 6 (Page 171)
- 7

110. In 9pin DB 9 CTS is assigned on pin number

- 6
- 7
- 8 (Page 171)
- 9

112. In 9pin DB 9 CD is assigned on pin number

- 1 (Page 171)
- 2
- 3
- 4

113. A 32bit address register can access up to ...access has .of memory so memory increased a lot.

- 2GB
- 4GB (Page 175)
- 6GB
- 8GB

114. In device attribute word which of the following bit decides whether it is a character device or a block device

- Bit 12
- Bit 13
- Bit 14
- Bit 15 (Page 166) rep

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115. Which of the following IRQ is cascading interrupt

- IRQ 0
- IRQ 1
- **IRQ 2 (Page 114)**
- IRQ 3

116. Which of the following interrupts is used for Arithmetic overflow

- INT 1
- INT 2
- INT 3
- **INT 4 (Page 106) rep**

117. An End of Interrupt (EOI) signal is sent by

- **Handler (Page 114)**
- Processor
- IRQ
- PIC

118. The number of pins in a parallel port connector are?

- 20
- **25 (Page 125)**
- 30
- 35

119. Which of the following pins of a parallel port connector are grounded?

- 10-18
- **18-25 (Page 125) rep**
- 25-32
- 32-39

120. In NASM an imported symbol is declared with the while and exported symbol is declared With the.....

- Global directive, External directive
- **External directive, Global directive (Page 189) rep**
- Home Directive, Foreign Directive
- Foreign Directive, Home Directive

121. Sun SPARC Processor has a fixed _____ instruction size.

- 16bit
- **32bit (Page 192) rep**
- 64bit
- 20bit

122. When the subprogram finishes, the _____ retrieves the return address from the stack and transfers control to that location.

- **RET instruction Click here for detail**
- CALL instruction
- POP instruction
- Jump instruction

123. A 32 bit address register can access up to _____ of memory.

- 1 GB
- 6 GB
- **4 GB (Page 175) rep**
- 2 GB

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124. The value of a segment register when the processor is running under protected mode is called _____

- segment descriptor
- segment selector
- global descriptor table
- protected register

125. FS and FS and GS are two _____ in protected mode.

- segment registers
- segment selectors (Page 175)
- stack pointers
- register pointers

126. IRQ 0 interrupt have _____ priority

- low
- medium
- highest (Page 114) rep
- lowest

127. IDT stands for _____.

- interrupt descriptor table (Page 182)
- individual descriptor table
- inline data table
- interrupt descriptor table

128. Every bit of line status in serial port conveys _____ information.

- different (Page 171)
- same
- partial
- full

129. There are total _____ bytes in a standard floppy disk.

- 1444k
- 1440k (Page 155)
- 1280k
- 2480k

130. An 8x16 font is stored in _____ bytes.

- 8
- 16 (Page 150) rep
- 4
- 20

131. Memory address space is selected when which of the following instructions is given to the processors Select correct option:

- MOV (page 115)
- DEC
- IN

- ADD

132. PCB stands for?

- Select correct option:
- Process Control Block (Page 140)
- Process Clearing Block
- Programmable Counter Block

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- Programs Control Block

132. The input frequency of the programmable interval timer (PIT) is

- Fixed (Page 122)
- Depends on processor clock
- Variable
- Depends on hardware attached

133. Programmable interrupt controller has two ports 20 and 21. Port 20 is the control port while port 21 is.....

- The Interrupt mask register (Page 115)
- Interrupt port
- Output port
- Input port

134. The time interval between two timer ticks is?

- 40ms
- 45ms
- 50ms
- 55ms (Page 122)

135. Which of the following interrupts is used for saving and restoring the registers

- INT6
- Int7
- Int8 (Page 141)
- Int0

136. In programmable interrupt controller which of the following ports is referred as an interrupt mask register?

- 19
- 20
- 21 (Page 115)
- 22

137. The programmable interval timer (PIT) has input frequency of

- 193MHZ (Page 122)
- 192MHZ
- 196MHZ
- 198MHZ

138. Peripheral address space is selected when which of the following instructions is given to the processor?

- MOV
- IN (Page 155)
- Add
- Out

139. Direction flag, the interrupt flag, and the trap flag are

- used for mathematical operations
- not used for mathematical operations (Page 133)
- status flags
- not status flags

140. The space where all the registers of a task are stored is called the

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➤ control block (Page 140)

- process control block
- stack
- memory

141. Which of the following interrupt is of highest priority interrupt?

- Key board interrupt
- Timer interrupt (Page 122)
- INT 2
- INT 3

142. The instruction used to read a character from the keyboard port is

- in al, 0x60 (Page 125)
- out al, 0x60
- in al, 0x80
- out al, 0x80

143. Each thread can have their own

- execution area
- stack (Page 141)
- memory
- array

144. All the registers & stack are saved in

- Multitasking
- multi-processing
- function call
- BIOS

145. There is no instruction to clear the

- Trap flag (Page 133)
- Interrupt flag
- Direction flag
- None of the above

146. The interrupt mask register which can be used for selectively enabling or disabling interrupts is associated with

- Port 19
- Port 20
- Port 21 (Page 115)
- Port 22

147. The parallel port connector is called?

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➤ DB-25 (Page 125)

➤ BD-25

➤ DB-24

➤ BD-24

148. Which of the following is not true about registers:

➤ Their operation is very much like memory

➤ Intermediate results may also be stored in registers.

➤ They are also called scratch pad ram

➤ None of given options.

149. move [bop], al moves the one byte content of the AL register to the address contained in

➤ BP register in the current

➤ Stack segment (Page 35)

➤ Code segment

➤ Data segment

➤ Extra segment

150. In a rotate through carry right (RCR) instruction applied on a 16 bit word effectively there is

➤ 16 bits rotation

➤ 1 bit rotation

➤ 17 bits rotation (Page 53)

➤ 8 bits rotation

151. The 8088 stack works on

➤ Word sized elements (Page 68)

➤ Byte sized elements

➤ Double sized element

➤ Nibble sized element

152. An 8 x 16 font is stored in _____ bytes.

➤ 4

➤ 8

➤ 16 (Page 150) rep

153. INT 10 is used for _____ services.

➤ RAM

➤ Disk

➤ BIOS video (Page 149)

➤ DOS video

154. Priority of IRQ 0 interrupt is

➤ medium

➤ high

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- highest (Page 114) rep
- low

155. Threads can have function calls, parameters and _____ variables.

- global
- local (Page 141) rep
- legal
- illegal

156. How many prevalent calling conventions do exist?

- 1
- 2 (Page 187) rep
- 3
- 4

157. In 9pin DB 9 DSR is assigned on pin number

- 4
- 5
- 6 (Page 171) rep
- 7

158. In 9pin DB 9 CTS is assigned on pin number

- 6
- 7
- 8 (Page 171) rep
- 9

159. In 9pin DB 9 CD is assigned on pin number

- 1 (Page 171) rep
- 2
- 3
- 4

160. In 9pin DB 9 RD is assigned on pin number

- 1
- 2 (Page 171) rep
- 3
- 4

161. In device attribute word which of the following bit decides whether it is a character device or a block device

- Bit 12
- Bit 13

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- Bit 14
- Bit 15 (Page 166)

162. Video services are classified into _____ broad categories

- 2 (Page 149)
- 3
- 4
- 5

163. In STOSB instruction, when DF is clear, SI is

- Incremented by 1 (Page 92) rep
- Incremented by 2
- Decrement by 1
- Decrement by 2

164. The process of sending signals back and forth is called

- Activity
- Hand-shaking
- Interruption
- Time clicking

165. Which of the following interrupts is used for Arithmetic Overflow?

- INT 1
- INT 2
- INT 3
- INT 4 (Page 106) rep

166. Which of the following is a special type of interrupt that returns to the same instruction instead of the next Instruction?

- Divide overflow interrupt (Page 107)
- Debug interrupt
- Arithmetic overflow interrupt
- Change of sign interrupt

167. Which of the following IRQs is derived by a timer device?

- IRQ 0 (Page 114) rep
- IRQ 1
- IRQ 2
- IRQ 3

168. Which of the following IRQs is connected to serial port COM 2?

- IRQ 0
- IRQ 1

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- IRQ 2
- IRQ 3 (Page 114) rep

169. An End of Interrupt (EOI) signal is sent by

- Handler (Page 114) rep
- Processor
- IRQ
- PIC

170. The source registers in OUT is

- AL or AX (Page 115) rep
- BL or BX
- CL or CX
- DL or DX

171. In programmable interrupt controller which of the following ports is used for selectively enabling or disabling interrupts

- 19
- 20
- 21 (Page 115)
- 22

172. The number of pins in a parallel port connector are?

- 20
- 25 (Page 125) rep
- 30
- 35

173. Which of the following pins of a parallel port connector are grounded?

- 10-18
- 18-25 (Page 125) rep
- 25-32
- 32-39

174. If the decimal number “35” is shifted by two bit to left, the new value will be

- 35
- 70
- 140 (00100011 = 35, 10001100=140)
- 17

175. A 32bit address register can access unto..... Of memory so memory access has increased a lot.

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- 2GB
- 4GB (Page 175) rep
- 6GB
- 8GB

176. In NASM an imported symbol is declared with the while and exported symbol is declared With the.....

- Global directive, External directive
- External directive, Global directive (Page 189) rep
- Home Directive, Foreign Directive
- Foreign Directive, Home Directive

177. Single step interrupt is

- Hardware interrupt
- Like divide by zero interrupt (Page 133) rep
- Like divide by 1 interrupt
- Software interrupt

176. In programmable interrupt controller which of the following ports is referred as interrupt mask register?

- a. 19
- b. 20
- c. 21
- d. 22

177. _____ is the highest priority interrupt in interrupt controller?

- a. IRQ 0
- b. IRQ 1
- c. IRQ 2
- d. IRQ 3

178. IRET returns on the basis of _____ and _____

- a. CS, IP
- b. DS, IP
- c. CS, SS
- d. IP, SP

170. _____ If is set, the after every instruction a type 1 interrupt will be automatically generated.

- e. Parity flag
- b. Trap flag
- c. Carry flag
- d. Overflow flag

180. The interrupt handler uses _____ instruction to return back to the caller.

- a. IRET
- b. RETI

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- c. INTR
- d. RET

179. Which of the following interrupt is used for Arithmetic overflow?

- a. INT 1
- b. INT 2
- c. INT 3
- d. INT 4

180. During the execution of INT instruction , some contents are pushed on to the stack , the order of pushing then is

- a. CS , IP and then FLAGS register
- b. IP,CS and then FLAGS register
- c. c. FLAGS register, CS and then IP
- d. FLAGS register, IP and the CS

181. Each of the bits at port _____ corresponding to one of the IRQ lines.

- d. 18
- e. 19
- f. 20
- d. 21

181. Which of the following pins of the parallel port connector are grounded?

- a. 10-18
- b. 18-25
- c. 25-32
- d. 32-39

182. Which of the following interrupts plays the most significant part during step debugging of a program?

- a. INT 0
- b. INT 1
- c. INT 2
- d. INT 3

183. Which of the following IRQs is connected to serial port COM 2?

- a. IRQ 0
- b. IRQ 1
- c. IRQ 2
- d. IRQ 3

184. Which of the following IRQs is connected to serial port COM 1?

- o IRQ 4
- o IRQ 5
- o IRQ 6
- o IRQ 7

185. Which of the following IRQs is used by the parallel port?

- a. IRQ 4
- b. IRQ 5
- c. IRQ 6
- d. IRQ 7

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186. In programmable interrupt controller which of the following ports is used to selectively enabling or disabling interrupts?

- a. 19
- b. 20
- c. 21
- d. 22

187.

The space where all the registers of task are stored is called _____

- a. Control block
- b. Process control block
- c. Stack
- d. Memory

188. Which of the following is the destination register in IN instruction?

- a. AL or AX
- b. BL or BX
- c. CL or CX
- d. DL or DX

189. Which of the following interrupts is used for saving and restoring the registers

- a. INT 6
- b. INT 7
- c. INT 8
- d. INT 0

190. In multitasking environment, which of the following structure is used to maintain the ordering of active PCBs?

- a. Array
- b. Register
- c. Linked List
- d. Stack

191. In multitasking which of the following interrupts is used as scheduler during context switching?

- a. INT 21
- b. INT 16
- c. INT 13
- d. INT 8

192. Which of the following are required for thread entry?

- a. CS and DS
- b. CS and IP
- c. IP and general purpose registers
- d. SS and SP

193. Which of the following flag can be used for mathematical operations?

- a. Direction Flag
- b. Carry Flag
- c. Trap Flag
- d. Interrupt Flag

194.

In multi-tasking, the process of saving and restoring to values of registers from a

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process control block (PCB) is called __

- a. Context switching
- b. Context saving
- c. Context restoring
- d. Code switching

195. When two devices in a system want to use the same IRQ line, is referred as:

- a. IRQ Collision
- b. IRQ Conflict
- c. IRQ Crash
- d. IRQ Blockage

196. An End of Interrupt (EOI) signal is sent by

- a. Handler
- b. Processor
- c. IRQ
- d. PIC

197. Which of the following is the BIOS interrupt providing keyboard service?

- a. INT 0x13
- b. INT 0x14
- c. INT 0x15
- d. INT 0x16

198.

Threads can have function calls, parameters and _____ variables.

- a. Global
- b. Local
- c. Legal
- d. Illegal

199. Which of the following arranges jobs in a sequence in order to be executed?

- a. Process control block
- b. Arranger
- c. Control unit
- d. Scheduler

200. Which of the following flags cannot be cleared using an assembly instruction?

- a. Trap flag
- b. Interrupt flag
- c. Direction flag
- d. Carry flag

201. Which of the following is the highest priority interrupt?

- a. INT3
- b. INT2
- c. INT1
- d. INT0

202. At the end of servicing an interrupt, which of the following is used to inform the PIC that it is completed?

- a. RET
- b. EOI
- c. IRET
- d. RET N

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- 203.** Which of the following interrupts is used for maintaining the system time?
- INT 0
 - INT 1
 - INT 8
 - INT 10
- 204.** Which of the following is used for exporting parallel port services?
- INT 17
 - INT 16
 - INT 15
 - INT 8
- 205.** In programmable interrupt controller which of the following ports is referred as a control port?
- 19
 - 20
 - 21
 - 22
- 206.** The first parallel port LPT1 has port number designated from_____.
- 178 to 17A
 - 278 to 27A
 - 378 to 37A
 - 478 to 47A
- 207.** _____is/are the port number(s) for parallel port.
- 20 and 21
 - 60 to 64
 - 380
 - 378
- 208.** The number of pins in a parallel port connector are?
- 20
 - 25
 - 30
 - 35
- 209.** The offset address of an interrupt n will be at
- n
 - $nx2$
 - $nx3$
 - $nx4$
- 210.** Programmable interrupt controller has two ports (20 and 21). Port 20 is the control port while port 21 is.
- The interrupt mask register
 - Interrupt port
 - Output port
 - Input port
- 211.** Which of the following instruction selects memory address space?
- MOV

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- b. DEC
- c. IN
- d. ADD

212. The thread registration code initializes the PCB and adds it to the linked list so that the _____ will give it a turn.

- a. Assembler
- b. Scheduler
- c. Linker
- d. Debugger

213. Which of the following instruction is used for disabling all the interrupts during a program execution?

- a. Cli
- b. Sti
- c. Reti
- d. Iret

214. IBM AT has how many PICs (Programmable interrupt controller)

- a. 1
- b. 2
- c. 3
- d. 4

215. Programmable interrupt controller (PIC) has

- a. One input signals and eight output signals
- b. One input signal and one output signal
- c. Eight input signals and one output signals
- d. Eight input signals and eight output signals

216. In 8088 processor, there can be total possible entries in an interrupt vector table.

- a. 256
- b. 64
- c. 128
- d. 512

217. Each thread can have their own

- a. Execution area
- b. Stack
- c. Memory
- d. Array

218. The parallel port connector is called?

- a. DB-25
- b. BD-25
- c. DB-24
- d. BD-24

219.

In 8088, the physical memory address for Interrupt Vector Table is fixed and the table occupies exactly _____ of memory.

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- a. 1 KB
- b. 1 MB
- c. 64 KB
- d. 4 Bytes

220. Which of the following interrupts is non maskable interrupt?

- a. INT 0
- b. INT 1
- c. INT 2
- d. INT 3

222.

Using OUT instruction on parallel data port results into a signal of _____ for every 1 bit.

- e. 0 V
- f. 1 V
- g. 5 V
- h. 10 V

221. PCB stands for?

- a. Process Control Block
- b. Process Cleaning Block
- c. Programmable Counter Block
- d. Programs Control Block

222. Which of the following interrupts is used in debugging with the trap flag?

- a. INT 0
- b. INT 1
- c. INT 2
- d. INT 3

223. Which of the following port number is used to send an end of interrupt (EOI) signal to the PIC after an interrupt is ended?

- a. 0x16
- b. 0x20
- c. 0x60
- d. 0x378

224. Which of the following instruction is used for reading a char from keyboard?

- a. out al, 0x60
- b. in al, 0x60
- c. out dx, al
- d. out dx, 0x378

225. In PIC, which of the following port is used for selectively enabling or disabling interrupts?

- a. 19
- b. 20
- c. 21
- d. 22

226. DOS has single entry point to access all its services through interrupt_

- a. INT 21
- b. INT 16

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- c. INT 2
 - d. INT 10
- 227.** Which of the following pins of parallel port are used to carry the data from processor to the device connected through parallel port?
- a. Pin 2 to pin 9
 - b. Pin 10
 - c. Pin 4
 - d. Pin 18 to pin 25
- 228.** The input frequency of the programmable interval timer (PIT) is
- a. Fixed
 - b. Depends on processor block
 - c. Variable
 - d. Depends on hardware attached
- 229.** All registers of task are stored in space called _____
- a. Process storage block
 - b. Process control block
 - c. Process terminal
 - d. Swapping space
- 230.** Which of the following IRQ's is derived by a key board
- a. IRQ 0
 - b. IRQ 1
 - c. IRQ 2
 - d. IRQ 3
- 231.** The programmable interval timer (PIT) has input frequency of
- a. 1.193MHz
 - b. 2.193MHz
 - c. 3.193MHz
 - d. 4.193MHz
- 232.** Changing and restoring the state of Control processing unit (CPU) is referred as
- a. Thrashing
 - b. Multitasking
 - c. Context Switching
 - d. Multithreading
- 233.** For the external hardware to generate an interrupt there are how many pins outside the processor?
- a. 1
 - b. 2
 - c. 3
 - d. 4
- 234.** Which of the following instruction selects peripheral address space?
- a. MOV
 - b. DEC
 - c. IN
 - d. ADD
- 235.** Which of the following is the most commonly used port with printer?

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- a. Serial Port
- b. USB port
- c. **Parallel Port**
- d. DVI Port

236. _____ allows us to execute one instruction at a time rather than the whole program at once

- a. INT 0
- b. **INT 1**
- c. INT 2
- d. INT 3

237. Which of the following is used for exporting keyboard services?

- a. **INT 16**
- b. INT 11
- c. INT 12
- d. INT 8

238. TSR stands for

- a. Terminate and store routines
- b. Transmits and save resources
- c. **Terminate and Stay resident**
- d. Truncate and start recursively

239. In older computers, the port number designed for parallel port LPT2 area.

- 078-07A
- 178-17A
- 378-37A
- **278-27A**

240. Which of the following is the ACK pin in DB-25 Connector

- a. **10**
- b. 11
- c. 12
- d. 13

241. Which of the following IRQs is a cascading interrupt?

- IRQ 0
- IRQ 1
- **IRQ 2**
- IRQ 3

242.

In interrupt masking, we use _____ to get control from the program without letting the program know about it.

- a. **IRQ 0**
- b. IRQ 1
- c. IRQ 5
- d. IRQ 7

243. The output of programmable timer interval is connected to which line

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of the PIC?

- a. IRQ 0
- b. IRQ 1
- c. IRQ 2
- d. IRQ 3

244.

In multitasking, each PCB requires _____ bytes to store the values of registers.

- a. 14
- b. 28
- c. 32
- d. 16

245. Which of the following IRQs is used for sound card or network card?

- a. IRQ 4
- b. IRQ 5
- c. IRQ 6
- d. IRQ 7

246.

Programmable interrupt controller has _____ ports.

- a. 1
- b. 2
- c. 3
- d. 4

247. Which of the following bits of parallel control port enables the IRQ 7 triggering by the ACKpin?

- a. Bit 1
- b. Bit 2
- c. Bit 3
- d. Bit 4

248.

All the registers & stack are saved in

- Multitasking
- Multi-Processing
- Function call
- BIOS

249.

Which of the following is the source register in OUT instruction?

- a. AL or AX
- b. BL or BX
- c. CL or CX
- d. DL or DX

250. Which of the following is the destination register in IN instruction?

- a. AL or AX
- b. BL or BX

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c. CL or CX

d. DL or DX



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**CS401- Computer Architecture and Assembly
Language Programming**
Solved MCQS From Final term Papers

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Question No: 1 (Marks: 1) - Please choose one

Suppose AL contains 5 decimal then after two left shifts produces the value as

- ▶ 5
- ▶ 10
- ▶ 15
- ▶ **20 (Page 52)**

Question No: 2 (Marks: 1) - Please choose one

In graphics mode a location in video memory corresponds to a _____ on the screen.

- ▶ line
- ▶ **dot (Page 149)**
- ▶ circle
- ▶ rectangle

Question No: 3 (Marks: 1) - Please choose one

Creation of threads can be

- ▶ static
- ▶ **dynamic (Page 141)**
- ▶ easy
- ▶ difficult

Question No: 4 (Marks: 1) - Please choose one

The thread registration code initializes the PCB and adds it to the linked list so that the _____ will give it a turn.

- ▶ assembler
- ▶ **scheduler (Page 141)**
- ▶ linker
- ▶ debugger

Question No: 5 (Marks: 1) - Please choose one

VESA VBE 2.0 is a standard for

- ▶ **High resolution Mode (Page 180)**
- ▶ Low resolution Mode
- ▶ Medium resolution Mode
- ▶ Very High resolution Mode

Question No: 6 (Marks: 1) - Please choose one

Which of the following gives the more logical view of the storage medium

- ▶ BIOS
- ▶ **DOS (Page 155)**
- ▶ Both
- ▶ None

Question No: 7 (Marks: 1) - Please choose one

Which of the following IRQs is derived by a key board?

- ▶ IRQ 0
- ▶ **IRQ 1 (Page 114)**
- ▶ IRQ 2
- ▶ IRQ 3

Question No: 8 (Marks: 1) - Please choose one

Which of the following IRQs is used for Floppy disk derive?

- ▶ IRQ 4
- ▶ IRQ 5
- ▶ **IRQ 6 (Page 114)**
- ▶ IRQ 7

Question No: 9 (Marks: 1) - Please choose one

Which of the following pins of a parallel port connector are grounded?

- ▶ 10-18
- ▶ **18-25 (Page 125)**
- ▶ 25-32
- ▶ 32-39

Question No: 10 (Marks: 1) - Please choose one

The physical address of IDT(Interrupt Descriptor Table) is stored in _____

- ▶ GDTR
- ▶ **IDTR (Page 182)**
- ▶ IVT
- ▶ IDTT

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Question No: 11 (Marks: 1) - Please choose one

In NASM an imported symbol is declared with the while and exported symbol is declared with the.....

- ▶ Global directive, External directive
- ▶ **External directive, Global directive (Page 189)**
- ▶ Home Directive, Foreign Directive
- ▶ Foreign Directive, Home Directive

Question No: 12 (Marks: 1) - Please choose one

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction

- ▶ 8bit
- ▶ 16bit
- ▶ **32bit (Page 191)**
- ▶ 64bit

Question No: 13 (Marks: 1) - Please choose one

To reserve 8-bits in memory ___ directive is used.

- ▶ **db (Page 25)**
- ▶ dw
- ▶ dn
- ▶ dd

Question No: 14 (Marks: 1) - Please choose one

In the “mov ax, 5” 5 is the _____ operand.

- ▶ **source (Page 18)**
- ▶ destination
- ▶ memory
- ▶ register

Question No: 15 (Marks: 1) - Please choose one

RETF will pop the segment address in the

- ▶ **CS register (Page 72)**
- ▶ DS register
- ▶ SS register
- ▶ ES register

Question No: 16 (Marks: 1) - Please choose one

For the execution of the instruction “DIV BL”, the implied dividend will be stored in

- ▶ **AX (Page 85)**
- ▶ BX
- ▶ CX
- ▶ DX

Question No: 17 (Marks: 1) - Please choose one

When a number is divided by zero "A Division by 0" interrupt is generated. Which instruction is used for this purpose

- ▶ INT 0
- ▶ INT 1
- ▶ INT 2
- ▶ **This interrupt is generated automatically (Page 107)**

Question No: 18 (Marks: 1) - Please choose one

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

- ▶ **AL (Page 152)**
- ▶ BL
- ▶ CL
- ▶ BH

Question No: 19 (Marks: 1) - Please choose one

BIOS sees the disks as

- ▶ logical storage
- ▶ **raw storage (Page 155)**
- ▶ in the form of sectors only
- ▶ in the form of tracks only

Question No: 20 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to CD (Carrier Detect)?

- ▶ **1 (Page 171)**
- ▶ 2
- ▶ 3
- ▶ 4

Question No: 21 (Marks: 1) - Please choose one

In 9pin DB 9, Signal ground is assigned on pin number

- ▶ 4
- ▶ **5 (Page 171)**
- ▶ 6
- ▶ 3

Question No: 22 (Marks: 1) - Please choose one

In 9pin DB 9, RI (Ring Indicator) is assigned on pin number

- ▶ 6
- ▶ 7
- ▶ 8
- ▶ **9 (Page 171)**

Question No: 23 (Marks: 1) - Please choose one

Motorola 68K processors have..... 23bit general purpose registers.

- ▶ 4
- ▶ 8
- ▶ **16 (Page 191)**
- ▶ 32

Question No: 24 (Marks: 1) - Please choose one

When two devices in the system want to use the same IRQ line then what will happen?

- ▶ An IRQ Collision
- ▶ **An IRQ Conflict (Page 114)**
- ▶ An IRQ Crash
- ▶ An IRQ Blockage

Question No: 25 (Marks: 1) - Please choose one

In the instruction **MOV AX, 5** the number of operands are

- ▶ 1
- ▶ **2 (Page 25)**
- ▶ 3
- ▶ 4

Question No: 26 (Marks: 1) - Please choose one

Which flags are NOT used for mathematical operations ?

- ▶ Carry, Interrupt and Trap flag.
- ▶ **Direction, Interrupt and Trap flag. (Page 133)**
- ▶ Direction, Overflow and Trap flag.
- ▶ Direction, Interrupt and Sign flag.

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Question No: 1 (Marks: 1) - Please choose one

The physical address of the stack is obtained by

- ▶ SS:SI combination
- ▶ **SS:SP combination (Page 68)**
- ▶ ES:BP combination
- ▶ ES:SP combination

Question No: 2 (Marks: 1) - Please choose one

Value of AH in the write Graphics pixel service is

- ▶ **0Ch (Page 152)**
- ▶ 0Bh
- ▶ 1Ch
- ▶ 2Ch

Question No: 3 (Marks: 1) - Please choose one

Threads can have function calls, parameters and _____ variables.

- ▶ global
- ▶ **local (Page 141)**
- ▶ legal
- ▶ illegal

Question No: 4 (Marks: 1) - Please choose one

Creation of threads can be

- ▶ static
- ▶ **dynamic (Page 141) rep**
- ▶ easy
- ▶ difficult

Question No: 5 (Marks: 1) - Please choose one

How many prevalent calling conventions do exist

- ▶ 1
- ▶ **2 (Page 187)**
- ▶ 3
- ▶ 4

Question No: 6 (Marks: 1) - Please choose one

VESA VBE 2.0 is a standard for

- ▶ **High resolution Mode (Page 180) rep**
- ▶ Low resolution Mode
- ▶ Medium resolution Mode
- ▶ Very High resolution Mode

Question No: 7 (Marks: 1) - Please choose one

The serial port connection is a ----- connector

- ▶ **9pin DB 9 (Page 171)**
- ▶ 8pin DB 9
- ▶ 3pin DB 9
- ▶ 9pin DB 5

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Question No: 8 (Marks: 1) - Please choose one

Which of the following gives the more logical view of the storage medium

- ▶ BIOS
- ▶ **DOS (Page 55) rep**
- ▶ Both
- ▶ None

Question No: 9 (Marks: 1) - Please choose one

In STOSB instruction, when DF is clear, SI is

- ▶ **Incremented by 1 (Page 92)**
- ▶ Incremented by 2
- ▶ Decremented by 1
- ▶ Decremented by 2

Question No: 10 (Marks: 1) - Please choose one

After the execution of STOSW the CX will be

- ▶ **Decremented by 1 (Page 92)**
- ▶ Decremented by 2
- ▶ Incremented by 1
- ▶ Incremented by 2

Question No: 11 (Marks: 1) - Please choose one

IRQ is referred to

- ▶ **Eight input signals**
- ▶ One output signal
- ▶ One input signals
- ▶ Eight output signals

Question No: 12 (Marks: 1) - Please choose one

Which of the following IRQs is derived by a key board?

- ▶ IRQ 0
- ▶ **IRQ 1 (Page 113) rep**
- ▶ IRQ 2
- ▶ IRQ 3

Question No: 13 (Marks: 1) - Please choose one

Which of the following IRQs is connected to serial port COM 1?

- ▶ **IRQ 4 (page 114)**
- ▶ IRQ 5
- ▶ IRQ 6
- ▶ IRQ 7

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Question No: 14 (Marks: 1) - Please choose one

The physical address of IDT(Interrupt Descriptor Table) is stored in _____

- ▶ GDTR
- ▶ **IDTR (Page 182) rep**
- ▶ IVT
- ▶ IDTT

Question No: 15 (Marks: 1) - Please choose one

Assembly language is:

- ▶ **Low-level programming language (Page 3)**
- ▶ High-level programming language
- ▶ Also known as machine language
- ▶ Not considered closer to the computer

Question No: 16 (Marks: 1) - Please choose one

The number of bits required to access 1MB of memory are

- ▶ 16 bits
- ▶ 32 bits
- ▶ **Depends on the processor architecture (Page 20)**
- ▶ 20 bits

Question No: 17 (Marks: 1) - Please choose one

In STOSB instruction, SI is decremented or incremented by

- ▶ 3
- ▶ 2
- ▶ **1 (Page 92)**
- ▶ 4

Question No: 18 (Marks: 1) - Please choose one

In programmable interrupt controller, which of the following ports is referred as a *control port*.

- ▶ 19
- ▶ **20 (Page 115)**
- ▶ 21
- ▶ 22

Question No: 19 (Marks: 1) - Please choose one

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

- ▶ **AL (Page 152) rep**
- ▶ BL
- ▶ CL
- ▶ BH

Question No: 20 (Marks: 1) - Please choose one

In device attribute word, which of the following bit decides whether it is a *character device* or a *block device*

- ▶ Bit 12
- ▶ Bit 13
- ▶ Bit 14
- ▶ **Bit 15 (Page 116)**

Question No: 21 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to CTS (Clear To Send) ?

- ▶ 6
- ▶ 7
- ▶ **8 (Page 171)**
- ▶ 9

Question No: 22 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to RD (Received Data) ?

- ▶ 1
- ▶ **2 (Page 171)**
- ▶ 3
- ▶ 4

Question No: 23 (Marks: 1) - Please choose one

VESA(Video Electronics Standards Association) organizes 16 color bits for every pixel in

- ▶ 5:5:5 format
- ▶ **5:6:5 format (Page 180)**
- ▶ 6:5:6 format
- ▶ 5:6:7 format

Question No: 24 (Marks: 1) - Please choose one

Motorola 68K processors have 23bit general purpose registers.

- ▶ 4
- ▶ 8
- ▶ **16 (Page 191) rep**
- ▶ 32

Question No: 25 (Marks: 1) - Please choose one

Programmable Interrupt Controller (PIC) has

- ▶ One input signals and eight output signals
- ▶ One input signal and one output signal
- ▶ **Eight input signals and one output signals (Page 113)**
- ▶ Eight input signals and eight output signals

Question No: 26 (Marks: 1) - Please choose one

Video services are classified into..... broad categories.

- ▶ 5
- ▶ 4
- ▶ 3
- ▶ **2 (Page 149)**

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1. BL contains 5 decimal then after right shift , BL will become
 - 3
 - **2.5 (Page 52)**
 - 5
 - 10
2. 8 * 16 font is stored in _____ bytes.
 - 3
 - 4
 - 8
 - **16 (Page 150)**
3. In DOS input buffer , number of characters actually read on return is stored in
 - First byte
 - **Second byte (Page 152)**
 - Third byte
 - Fourth byte
4. IRQ 0 has priority
 - Low
 - High
 - **Highest (Page 114)**
 - Medium

5. Thread registration code initialize PCB and add to linked list so that _____ will give it turn.

- Assembler
- Linker
- **Scheduler (Page 141) rep**
- Debugger

6. Traditional calling conventions are in _____ number

- 1
- **2 (Page 187)**
- 3
- 4

7. VESA VEB 2.0 is standard for

- **High Resolution Mode (Page 180) rep**
- Low Resolution Mode
- Very High Resolution Mode
- Medium Resolution Mode

8. To clear direction flag which instruction is used

- **Cld (Page 92)**
- Clrd
- Cl df
- Clr df

9. In STOSW instruction , When DI is cleared , SI is

- Incremented by 1
- **Incremented by 2 (Page 92)**
- Decremented by 1
- Decremented by 2

10. Interrupt that is used in debugging with help of trap flag is

- INT 0
- **INT 1 (Page 105)**
- INT 2
- INT 3

11. INT for arithmetic overflow is

- INT 1
- INT 2
- INT 3
- **INT 4 (Page 106)**

12. IRQ referred as

- **Eight Input signals (Page 113) rep**
- One Input signal
- Eight Output signals
- One output signal

13. IRQ for keyboard is 1 (Page 114)

14. IRQ for sound card is 5 (Page 114)

15. IRQ for floppy disk is 6 (Page 114)

16. IRQ with highest priority is

- Keyboard IRQ
- **Timer IRQ (Page 114)**
- Sound Card
- Floppy Disk

17. Pin for parallel port ground is

- 10-18
- **18-25 (Page 125) rep**
- 25-32
- 32-39

18. The physical address of Interrupt Descriptor Table (IDT) is stored in

- GDTR
- **IDTR (Page 182) rep**
- IVT
- IDTT

20. CX register is

- **Count register (Page 15)**
- Data register
- Index register
- Base register

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21. OUT instruction uses AX or AL as source register. (Page 115)

22. IN DB-9 connector the Data Set ready pin is at

- 5
- **6 (Page 171)**
- 7
- 8

23. If two devices uses same IRQ then there is

- IRQ collision
- **IRQ conflict (Page 114) rep**
- IRQ drop

24. VESA organizes 16 bit color for every pixel in ratio

- 5:5:5
- **5:6:5 (Page 180) rep**
- 6:5:6
- 5:6:7

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Question No: 1 (Marks: 1) - Please choose one

SP is associated with..... By default

- ▶ **SS (Page 34)**
- ▶ DS
- ▶ CS
- ▶ ES

Question No: 2 (Marks: 1) - Please choose one

Which bit of the attributes byte represents the red component of foreground color

- ▶ 5
- ▶ 4
- ▶ 3
- ▶ **2 (Page 81)**

Question No: 3 (Marks: 1) - Please choose one

An 8 x 16 font is stored in _____ bytes.

- ▶ 2
- ▶ 4
- ▶ 8
- ▶ **16 (Page 150) rep**

Question No: 4 (Marks: 1) - Please choose one

In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

- ▶ third
- ▶ fourth
- ▶ first
- ▶ **second (Page 152) rep**

Question No: 5 (Marks: 1) - Please choose one

Which of the following gives the more logical view of the storage medium

- ▶ BIOS
- ▶ **DOS**
- ▶ Both
- ▶ None

Question No: 6 (Marks: 1) - Please choose one

In STOSW instruction, when DF is clear, SI is

- ▶ Incremented by 1
- ▶ **Incremented by 2 (Page 92)**
- ▶ Decremented by 1
- ▶ Decremented by 2

Question No: 7 (Marks: 1) - Please choose one

Which of the following interrupts is Non maskable interrupt

- ▶ **INT 2 (Page 105)**
- ▶ INT 3
- ▶ INT 0
- ▶ INT 1

Question No: 8 (Marks: 1) - Please choose one

Which of the following IRQs is connected to serial port COM 2?

- ▶ IRQ 0
- ▶ IRQ 1
- ▶ IRQ 2
- ▶ **IRQ 3 (Page 114)**

Question No: 9 (Marks: 1) - Please choose one

The time interval between two timer ticks is?

- ▶ 40ms
- ▶ 45ms
- ▶ 50ms
- ▶ **55ms (Page 122)**

Question No: 10 (Marks: 1) - Please choose one

The physical address of IDT(Interrupt Descriptor Table) is stored in _____

- ▶ GDTR
- ▶ **IDTR (Page 182) rep**
- ▶ IVT
- ▶ IDTT

Question No: 11 (Marks: 1) - Please choose one

In NASM an imported symbol is declared with the while and exported symbol is declared with the.....

- ▶ Global directive, External directive
- ▶ **External directive, Global directive (Page 189) rep**
- ▶ Home Directive, Foreign Directive
- ▶ Foreign Directive, Home Directive

Question No: 12 (Marks: 1) - Please choose one

In 68K processors there is a 32bit that holds the address of currently executing instruction

- ▶ **Program counter (Page 191)**
- ▶ Stack pointer
- ▶ Register
- ▶ Stack

Question No: 13 (Marks: 1) - Please choose one

Single step interrupt is

- ▶ Hardware interrupt
- ▶ **Like divide by zero interrupt (Page 133)**
- ▶ Like divide by 1 interrupt
- ▶ Software interrupt

Question No: 14 (Marks: 1) - Please choose one

Which of the following is NOT true about registers:

- ▶ Their operation is very much like memory
- ▶ Intermediate results may also be stored in registers
- ▶ They are also called scratch pad ram
- ▶ **None of given options**

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Question No: 15 (Marks: 1) - Please choose one

Types of jump are:

- ▶ short, near
- ▶ **short, near, far (Page 45)**
- ▶ near, far
- ▶ short, far

Question No: 16 (Marks: 1) - Please choose one

MS DOS uses ____ display mode.

- ▶ **Character based (Page 79)**
- ▶ Graphics based
- ▶ Numeric based
- ▶ Console based

Question No: 17 (Marks: 1) - Please choose one

Which of the following IRQs is derived by a timer device?

- ▶ **IRQ 0 (Page 114)**
- ▶ IRQ 1
- ▶ IRQ 2
- ▶ IRQ 3

Question No: 18 (Marks: 1) - Please choose one

In programmable interrupt controller, which of the following ports is referred as a *control port*?

- ▶ 19
- ▶ **20 (Page 115) rep**
- ▶ 21
- ▶ 22

Question No: 19 (Marks: 1) - Please choose one

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

- ▶ **AL (Page 152) rep**
- ▶ BL
- ▶ CL
- ▶ BH

Question No: 20 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to DSR (DataSet Ready) ?

- ▶ 4
- ▶ 5
- ▶ **6 (Page 171)**
- ▶ 7

Question No: 21 (Marks: 1) - Please choose one

In 9pin DB 9, which pin number is assigned to TD (Transmitted Data) ?

- ▶ 1
- ▶ 2
- ▶ **3 (Page 171)**
- ▶ 4

Question No: 22 (Marks: 1) - Please choose one

In 9pin DB 9, Signal ground is assigned on pin number

- ▶ 4
- ▶ **5 (Page 171)**
- ▶ 6
- ▶ 3

Question No: 23 (Marks: 1) - Please choose one

8088 is a

- ▶ **16 bit processor (Page 14)**
- ▶ 32 bit processor
- ▶ 64 bit processor
- ▶ 128 bit processor

Question No: 24 (Marks: 1) - Please choose one

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

- ▶ 1
- ▶ **0 (Page 175)**
- ▶ -1
- ▶ -2

Question No: 25 (Marks: 1) - Please choose one

VESA(Video Electronics Standards Association) organizes 16 color bits for every pixel in

- ▶ 5:5:5 format
- ▶ **5:6:5 format (Page 180) rep**
- ▶ 6:5:6 format
- ▶ 5:6:7 format

Question No: 26 (Marks: 1) - Please choose one

Which flags are NOT used for mathematical operations?

- ▶ Carry, Interrupt and Trap flag.
- ▶ **Direction, Interrupt and Trap flag. (Page 133) rep**
- ▶ Direction, Overflow and Trap flag.
- ▶ Direction, Interrupt and Sign flag.

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Question No: 1 (Marks: 1) - Please choose one

When a 32 bit number is divided by a 16 bit number, the quotient is of

\$ 32 bits

\$ 16 bits (Page 85)

\$ 8 bits

\$ 4 bits

Question No: 2 (Marks: 1) - Please choose one

In the instruction MOV AX, 5 the number of operands are

\$ 1

\$ 2 (Page 25) rep

\$ 3

\$ 4

Question No: 3 (Marks: 1) - Please choose one

3. In DOS input buffer , number of characters actually read on return is stored in

\$ First byte

\$ Second byte (Page 152) rep

\$ Third byte

\$ Fourth byte

Question No: 4 (Marks: 1) - Please choose one

7. VESA VEB 2.0 is standard for

\$ High Resolution Mode (Page 180) rep

\$ Low Resolution Mode

\$ Very High Resolution Mode

\$ Medium Resolution Mode

Question No: 5 (Marks: 1) - Please choose one

22. IN DB-9 connector the Data Set ready pin is at

\$ 5

\$ 6 (Page 171) rep

\$ 7

\$ 8

Question No: 6 (Marks: 1) - Please choose one

Threads can have function calls, parameters and _____ variables.

\$ global

\$ local (Page 141)

\$ legal

\$ illegal

Question No: 7 (Marks: 1) - Please choose one

How many prevalent calling conventions do exist

\$ 1

\$ 2 (Page 187) rep

\$ 3

\$ 4

Question No: 8 (Marks: 1) - Please choose one

In 9pin DB 9 DSR is assigned on pin number

\$ 4

\$ 5

\$ 6 (Page 171)

\$ 7

Question No: 9 (Marks: 1) - Please choose one

In 9pin DB 9 CTS is assigned on pin number

\$ 6

\$ 7

\$ 8 (Page 171)

\$ 9

Question No: 10 (Marks: 1) - Please choose one

In 9pin DB 9 CD is assigned on pin number

\$ 1 (Page 171)

\$ 2

\$ 3

\$ 4

Question No: 11 (Marks: 1) - Please choose one

A 32bit address register can access upto ...access has .of memory so memory increased a lot.

\$2GB

\$4GB (Page 175)

\$6GB

\$8GB

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Question No: 12 (Marks: 1) - Please choose one

in device attribute word which of the following bit decides whether it is a character device or a block device

\$ Bit 12

\$ Bit 13

\$ Bit 14

\$ Bit 15 (Page 166) rep

Question No: 13 (Marks: 1) - Please choose one

9. Which of the following IRQ is cascading interrupt

\$ IRQ 0

\$ IRQ 1

\$ IRQ 2 (Page 114)

\$ IRQ 3

Question No: 14 (Marks: 1) - Please choose one

Which of the following interrupts is used for Arithmetic overflow

\$ INT 1

\$ INT 2

\$ INT 3

\$ INT 4 (Page 106) rep

Question No: 15 (Marks: 1) - Please choose one

An End of Interrupt (EOI) signal is sent by

\$ Handler (Page 114)

\$ Processor

\$ IRQ

\$ PIC

Question No: 16 (Marks: 1) - Please choose one

The number of pins in a parallel port connector are?

\$ 20

\$ 25 (Page 125)

\$ 30

\$ 35

Question No: 17 (Marks: 1) - Please choose one

Which of the following pins of a parallel port connector are grounded?

\$ 10-18

\$ 18-25 (Page 125) rep

\$ 25-32

\$ 32-39

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Question No: 18 (Marks: 1) - Please choose one

In NASM an imported symbol is declared with the while and exported symbol is declared with the.....

- \$ Global directive, External directive
- \$ External directive, Global directive (Page 189) rep**
- \$ Home Directive, Foreign Directive
- \$ Foreign Directive, Home Directive

FINAL TERM EXAMINATION

Fall 2008

CS401- Computer Architecture and Assembly Language Programming (Session - 1)

Question No: 1 (Marks: 1) - Please choose one

Which of the following is not true about registers:

- Their operation is very much like memory
- Intermediate results may also be stored in registers.
- They are also called scratch pad ram

None of given options.

Question No: 2 (Marks: 1) - Please choose one

move [bp], al moves the one byte content of the AL register to the address contained in BP register in the current

- Stack segment (Page 35)**
- Code segment
- Data segment
- Extra segment

Question No: 3 (Marks: 1) - Please choose one

In a rotate through carry right (RCR) instruction applied on a 16 bit word effectively there is

- 16 bits rotation
- 1 bit rotation
- 17 bits rotation (Page 53)**
- 8 bits rotation

Question No: 4 (Marks: 1) - Please choose one

The 8088 stack works on

- Word sized elements (Page 68)**
- Byte sized elements

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Double sized element
Nibble sized element

Question No: 5 (Marks: 1) - Please choose one

An 8 x 16 font is stored in _____ bytes.

- 2
- 4
- 8

16 (Page 150) rep

Question No: 6 (Marks: 1) - Please choose one

INT 10 is used for _____ services.

- RAM
- Disk

BIOS video (Page 149)

DOS video

Question No: 7 (Marks: 1) - Please choose one

Priority of IRQ 0 interrupt is

- medium
- high

highest (Page 114) rep

low

Question No: 8 (Marks: 1) - Please choose one

Threads can have function calls, parameters and _____ variables.

global

local (Page 141) rep

legal

illegal

Question No: 9 (Marks: 1) - Please choose one

How many prevalent calling conventions do exist

- 1
- 2
- 3
- 4

2 (Page 187) rep

Question No: 10 (Marks: 1) - Please choose one

In 9pin DB 9 DSR is assigned on pin number

- 4
- 5
- 6
- 7

6 (Page 171) rep

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Question No: 11 (Marks: 1) - Please choose one

In 9pin DB 9 CTS is assigned on pin number

6

7

8 (Page 171) rep

9

Question No: 12 (Marks: 1) - Please choose one

In 9pin DB 9 CD is assigned on pin number

1 (Page 171) rep

2

3

4

Question No: 13 (Marks: 1) - Please choose one

In 9pin DB 9 RD is assigned on pin number

1

2 (Page 171)

3

4

Question No: 14 (Marks: 1) - Please choose one

in device attribute word which of the following bit decides whether it is a character device or a block device

Bit 12

Bit 13

Bit 14

Bit 15 (Page 166)

Question No: 15 (Marks: 1) - Please choose one

Video services are classified into _____ broad categories

2 (Page 149)

3

4

5

Question No: 16 (Marks: 1) - Please choose one

In STOSB instruction, when DF is clear, SI is

Incremented by 1 (Page 92) rep

Incremented by 2

Decrement by 1

Decrement by 2

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Question No: 17 (Marks: 1) - Please choose one

The process of sending signals back and forth is called

- Activity
- Hand-shaking
- Interruption
- Time clicking**

Question No: 18 (Marks: 1) - Please choose one

Which of the following interrupts is used for Arithmetic Overflow?

- INT 1
- INT 2
- INT 3
- INT 4 (Page 106) rep**

Question No: 19 (Marks: 1) - Please choose one

Which of the following is a special type of interrupt that returns to the same instruction instead of the next instruction?

- Divide overflow interrupt (Page 107)**
- Debug interrupt
- Arithmetic overflow interrupt
- Change of sign interrupt

Question No: 20 (Marks: 1) - Please choose one

Which of the following IRQs is derived by a timer device?

- IRQ 0 (Page 114) rep**
- IRQ 1
- IRQ 2
- IRQ 3

Question No: 21 (Marks: 1) - Please choose one

Which of the following IRQs is connected to serial port COM 2?

- IRQ 0
- IRQ 1
- IRQ 2
- IRQ 3 (Page 114) rep**

Question No: 22 (Marks: 1) - Please choose one

An End of Interrupt (EOI) signal is sent by

- Handler (Page 114) rep**
- Processor
- IRQ
- PIC

Question No: 23 (Marks: 1) - Please choose one

The source registers in OUT is

AL or AX (Page 115) rep

BL or BX

CL or CX

DL or DX

Question No: 24 (Marks: 1) - Please choose one

In programmable interrupt controller which of the following ports is used for selectively enabling or disabling interrupts

19

20

21 (Page 115)

22

Question No: 25 (Marks: 1) - Please choose one

The number of pins in a parallel port connector are?

20

25 (Page 125) rep

30

35

Question No: 26 (Marks: 1) - Please choose one

Which of the following pins of a parallel port connector are grounded?

10-18

18-25 (Page 125) rep

25-32

32-39

Question No: 27 (Marks: 1) - Please choose one

If the decimal number “35” is shifted by two bit to left, the new value will be

35

70

140 (00100011 = 35 , 10001100=140)

17

Question No: 28 (Marks: 1) - Please choose one

A 32bit address register can access upto..... of memory so memory access has increased a lot.

2GB

4GB (Page 175) rep

6GB

8GB

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Question No: 29 (Marks: 1) - Please choose one

In NASM an imported symbol is declared with the while and exported symbol is declared with the.....

Global directive, External directive

External directive, Global directive (Page 189) rep

Home Directive, Foreign Directive

Foreign Directive, Home Directive

Question No: 30 (Marks: 1) - Please choose one

Single step interrupt is

Hardware interrupt

Like divide by zero interrupt (Page 133) rep

Like divide by 1 interrupt

Software interrupt

**CS401- Computer Architecture and Assembly Language Programming
FINAL TERM EXAMINATION
Spring 2007**

Question No: 1 (Marks: 1) - Please choose one

Sun SPARC Processor has a fixed _____ instruction size.

? 16bit

? 32bit (Page 192) rep

? 64bit

? 20bit

Question No: 2 (Marks: 1) - Please choose one

When the subprogram finishes, the _____ retrieves the return address from the stack and transfers control to that location.

? RET instruction [Click here for detail](#)

? CALL instruction

? POP instruction

? Jump instruction

Question No: 3 (Marks: 1) - Please choose one

A 32 bit address register can access upto _____ of memory.

? 1 GB

? 6 GB

? 4 GB (Page 175) rep

? 2 GB

Question No: 4 (Marks: 1) - Please choose one

The value of a segment register when the processor is running under protected mode is called _____

? **segment descriptor**

? segment selector

? global descriptor table

? protected register

Question No: 5 (Marks: 1) - Please choose one

FS and FS and GS are two _____ in protected mode.

? segment registers

? **segment selectors (Page 175)**

? stack pointers

? register pointers

Question No: 6 (Marks: 1) - Please choose one

IRQ 0 interrupt have _____ priority

? low

? medium

? **highest (Page 114) rep**

? lowest

Question No: 7 (Marks: 1) - Please choose one

IDT stands for _____.

? **interrupt descriptor table (Page 182)**

? individual descriptor table

? inline data table

? interrupt descriptor table

Question No: 8 (Marks: 1) - Please choose one

Every bit of line status in serial port conveys _____ information.

? **different (Page 171)**

? same

? partial

? full

Question No: 9 (Marks: 1) - Please choose one

There are total _____ bytes in a standard floppy disk.

? 1444k

? **1440k (Page 155)**

? 1280k

? 2480k

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Question No: 10 (Marks: 1) - Please choose one

An 8x16 font is stored in _____ bytes.

? 8

? **16 (Page 150) rep**

? 4

? 20

Cs401 – Quiz No.3 (June 2012)

Question No: 1(Marks: 1)

Memory address space is selected when which of the following instructions is given to the processor?s

Select correct option:

MOV (page 115)

DEC

IN

ADD

Question No: 2(Marks: 1)

PCB stands for?

Select correct option:

Process Control Block (Page 140)

Process Clearing Block

Programmable Counter Block

Programs Control Block

Question No: 3(Marks: 1)

The input frequency of the programmable interval timer (PIT) is

Fixed (Page 122)

Depends on processor clock

Variable

Depends on hardware attached

Question No: 4(Marks: 1)

Programmable interrupt controller has two ports 20 and 21. Port 20 is the control port while port 21 is.....

The Interrupt mask register (Page 115)

Interrupt port

Output port

Input port

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Question No: 5(Marks: 1)

The time interval between two timer ticks is ?

40ms

45ms

50ms

55ms (Page 122)

Question No: 6(Marks: 1)

Which of the following interrupts is used for saving and restoring the registers

INT6

Int7

Int8 (Page 141)

Int0

Question No: 7(Marks: 1)

In programmable interrupt controller which of the following ports is referred as a interrupt mask register?

19

20

21 (Page 115)

22

Question No: 8(Marks: 1)

The programmable interval timer (PIT) has input frequency of

1.193MHZ (Page 122)

2.192MHZ

3.196MHZ

4.198MHZ

Question No: 9(Marks: 1)

Peripheral address space is selected when which of the following instructions is given to the processor?

MOV

IN (Page 155)

Add

Out

Question No: 10(Marks: 1)

Direction flag, the interrupt flag, and the trap flag are used for mathematical operations

not used for mathematical operations (Page 133)

status flags

not status flags

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Cs401 – Quiz No.3 (June 2012)

Question No: 1(Marks: 1)

The space where all the registers of a task are stored is called the
control block (Page 140)
process control block
stack
memory

Question No: 2(Marks: 1)

Which of the following interrupt is of highest priority interrupt?
Select correct option:
Key board interrupt
Timer interrupt (Page 122)
INT 2
INT 3

Question No: 3(Marks: 1)

The instruction used to read a character from the keyboard port is
in al, 0x60 (Page 125)
out al, 0x60
in al, 0x80
out al, 0x80

Question No: 4(Marks: 1)

Each thread can have their own
execution area
stack (Page 141)
memory
array

Question No: 5(Marks: 1)

All the registers & stack are saved in
Multitasking
multi-processing
function call
BIOS

Question No: 6(Marks: 1)

There is no instruction to clear the

Select correct option:

Trap flag (Page 133)

Interrupt flag

Direction flag

None of the above

Question No: 7(Marks: 1)

The interrupt mask register which can be used for selectively enabling or disabling interrupts is associated with

Select correct option:

Port 19

Port 20

Port 21 (Page 115)

Port 22

Question No: 8(Marks: 1)

The parallel port connector is called?

Select correct option:

DB-25 (Page 125)

BD-25

DB-24

BD-24

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