



CS401 - Computer Architecture and Assembly Language Programming



CS401 - Computer Architecture and Assembly Language Programming

100% Correct Solve (Alhamdulillah)

01-02-2021

BY Virtual Assistance Sonu Ilyas Mughal



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https://www.youtube.com/channel/UCjboEX65C_yN7as5di10uIQ/playlists Sadiq Ahmed



03063548328 Sonu Ilyas Mughal



CS401:Quiz No. 1

Question # 1 of 10 (Start time: 01:21:49 PM, 31 January 2021)

The offset address of an interrupt service routine "n" is at _____.

Select the correct option

- n
- nx2
- nx3
- nx4

Click to Save





CS401:Quiz No. 1

Quiz

Question # 2 of 10 (Start time: 01:22:42 PM, 31 January 2021)

When two devices in a system want to use the same Interrupt Request (IRQ) line, is referred as IRQ _____.

Select the correct option

- collision
- conflict
- crash
- blockage





CS401:Quiz No. 1

Question # 3 of 10 (Start time: 01:23:06 PM, 31 January 2021)

An End of Interrupt (EOI) signal is sent by the _____.

Select the correct option

- | | |
|----------------------------------|-----------------------------------|
| <input checked="" type="radio"/> | interrupt handler |
| <input type="radio"/> | processor |
| <input type="radio"/> | interrupt request |
| <input type="radio"/> | programmable interrupt controller |





Question # 4 of 10 (Start time: 01:23:28 PM, 31 January 2021)

Which of the following is the order of pushing the contents on stack during the execution of INT instruction?

Select the correct option

- CS, IP, and then FLAGS
- IP, CS, and then FLAGS
- FLAGS, CS, and then IP
- FLAGS, IP, and then CS

Click to Save A



Question # 5 of 10 (Start time: 01:23:54 PM, 31 January 2021)

Which of the following instructions is used to disable all interrupts during the execution of a program?

Select the correct option

- | | |
|----------------------------------|------|
| <input checked="" type="radio"/> | cli |
| <input type="radio"/> | sti |
| <input type="radio"/> | reti |
| <input type="radio"/> | cld |

Click to Save Answer





CS401:Quiz No. 1

Question # 6 of 10 (Start time: 01:24:21 PM, 31 January 2021)

All registers and stack of a program are saved in _____.

Select the correct option

- | | |
|----------------------------------|------------------------|
| <input checked="" type="radio"/> | multitasking |
| <input type="radio"/> | multiprocessing |
| <input type="radio"/> | function call |
| <input type="radio"/> | return from a function |





CS401:Quiz No. 1

Question # 7 of 10 (Start time: 01:24:40 PM, 31 January 2021)

Which of the following is the ACK pin in DB-25 connector?

Select the correct option

- 10
- 11
- 12
- 13

Click





CS401:Quiz No. 1

Quiz

Question # 8 of 10 (Start time: 01:25:01 PM, 31 January 2021)

Which of the following is the priority of Interrupt Request (IRQ) 0?

Select the correct option

- | | |
|----------------------------------|---------|
| <input type="radio"/> | High |
| <input checked="" type="radio"/> | Highest |
| <input type="radio"/> | Low |
| <input type="radio"/> | Medium |

Click to Save





CS401:Quiz No. 1

Question # 9 of 10 (Start time: 01:25:24 PM, 31 January 2021)

PCB stands for _____.

Select the correct option

- | | |
|-------------------------------------|----------------------------|
| <input checked="" type="checkbox"/> | Process Control Block |
| <input type="checkbox"/> | Process Caching Block |
| <input type="checkbox"/> | Programmable Counter Block |
| <input type="checkbox"/> | Program Control Block |





CS401:Quiz No. 1

Question # 10 of 10 (Start time: 01:25:44 PM, 31 January 2021)

Which of the following IRQs is used for sound card or network card?

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | IRQ 4 |
| <input checked="" type="radio"/> | IRQ 5 |
| <input type="radio"/> | IRQ 6 |
| <input type="radio"/> | IRQ 7 |

Click to





CS401:Quiz No. 1

Question # 1 of 10 (Start time: 12:47:34 PM, 31 January 2021)

iret returns on the basis of _____ and _____.

Select the correct option

- | | |
|----------------------------------|--------|
| <input checked="" type="radio"/> | CS, IP |
| <input type="radio"/> | DS, IP |
| <input type="radio"/> | CS, SS |
| <input type="radio"/> | IP, SP |





CS401:Quiz No. 1

Question # 2 of 10 (Start time: 12:47:54 PM, 31 January 2021)

DOS has a single entry point to access all of its services through _____.

Select the correct option

- | | |
|----------------------------------|--------|
| <input checked="" type="radio"/> | INT 21 |
| <input type="radio"/> | INT 16 |
| <input type="radio"/> | INT 2 |
| <input type="radio"/> | INT 10 |





CS401:Quiz No. 1

Question # 3 of 10 (Start time: 12:48:20 PM, 31 January 2021)

The space where all registers of a task are stored is called _____.

Select the correct option

- cache memory
- process control block
- local variable
- heap





CS401:Quiz No. 1

Question # 4 of 10 (Start time: 12:48:52 PM, 31 January 2021)

Which of the following Interrupt Requests (IRQ) is connected to serial port COM 2?

Select the correct option

- IRQ 0
- IRQ 1
- IRQ 2
- IRQ 3



CS401:Quiz No. 1

Question # 5 of 10 (Start time: 12:49:22 PM, 31 January 2021)

Which of the following is the source register in OUT instruction?

Select the correct option

- | | |
|----------------------------------|----------|
| <input checked="" type="radio"/> | AL or AX |
| <input type="radio"/> | BL or BX |
| <input type="radio"/> | CL or CX |
| <input type="radio"/> | DL or DX |

Click





CS401:Quiz No. 1

Question # 6 of 10 (Start time: 12:49:59 PM, 31 January 2021)

PCB stands for _____.

Select the correct option

- | | |
|----------------------------------|----------------------------|
| <input checked="" type="radio"/> | Process Control Block |
| <input type="radio"/> | Process Caching Block |
| <input type="radio"/> | Programmable Counter Block |
| <input type="radio"/> | Program Control Block |





CS401:Quiz No. 1

Question # 7 of 10 (Start time: 12:51:00 PM, 31 January 2021)

In programmable interrupt controller, which of the following ports is the control port?

Select the correct option

- Port 19
- Port 20
- Port 21
- Port 22





CS401:Quiz No. 1

Question # 8 of 10 (Start time: 12:51:20 PM, 31 January 2021)

_____ is used for exporting keyboard services.

Select the correct option

- INT 16
- INT 11
- INT 12
- INT 8

Click t





CS401:Quiz No. 1

Quiz Start Time

Question # 9 of 10 (Start time: 12:51:45 PM, 31 January 2021)

In Programmable Interrupt Controller (PIC), which of the following ports is used to selectively enable or disable interrupts?

Select the correct option

- 19
- 20
- 21
- 22

Click to Save Answer &



CS401:Quiz No. 1

Question # 10 of 10 (Start time: 12:52:07 PM, 31 January 2021)

Each of the bits at port _____ corresponds to one of the Interrupt Request (IRQ) lines.

Select the correct option

- | | |
|----------------------------------|----|
| <input type="radio"/> | 18 |
| <input type="radio"/> | 19 |
| <input type="radio"/> | 20 |
| <input checked="" type="radio"/> | 21 |





CS401:Quiz No. 1

Question # 1 of 10 (Start time: 12:02:30 PM, 31 January 2021)

Which of the following IRQs is used for sound card or network card?

Select the correct option

- IRQ 4
- IRQ 5
- IRQ 6
- IRQ 7



CS401:Quiz No. 1

Question # 2 of 10 (Start time: 12:03:40 PM, 31 January 2021)

_____ is/are the port number(s) for parallel port.

Select the correct option

- 20 and 21
- 60 to 64
- 380
- 378





CS401:Quiz No. 1

Question # 3 of 10 (Start time: 12:04:01 PM, 31 January 2021)

Which of the following is BIOS interrupt providing keyboard services?

Select the correct option

- | | |
|----------------------------------|----------|
| <input type="radio"/> | INT 0x13 |
| <input type="radio"/> | INT 0x14 |
| <input type="radio"/> | INT 0x15 |
| <input checked="" type="radio"/> | INT 0x16 |





CS401:Quiz No. 1

Qui

Question # 4 of 10 (Start time: 12:04:26 PM, 31 January 2021)

At the end of servicing an interrupt, _____ signal is used to inform the Programmable Interrupt Controller (PIC) about it.

Select the correct option

- IOE
- EOI
- IRET
- RET n

Click to Save



CS401:Quiz No. 1

Question # 5 of 10 (Start time: 12:04:49 PM, 31 January 2021)

iret returns on the basis of _____ and _____.

Select the correct option

- CS, IP
- DS, IP
- CS, SS
- IP, SP

Click to s





CS401:Quiz No. 1

Question # 6 of 10 (Start time: 12:05:13 PM, 31 January 2021)

_____ is used in debugging along with the trap flag.

Select the correct option

- INT 0
- INT 1
- INT 2
- INT 3





Question # 7 of 10 (Start time: 12:05:40 PM, 31 January 2021)

Changing and restoring the state of Central Processing Unit (CPU) is called _____.

Select the correct option

- Thrashing
- Multitasking
- Context Switching
- Multithreading

Click to Save A





CS401:Quiz No. 1

Question # 8 of 10 (Start time: 12:05:59 PM, 31 January 2021)

Programmable interrupt controller has _____ ports.

Select the correct option

- 1
- 2
- 3
- 4





CS401:Quiz No. 1

Question # 9 of 10 (Start time: 12:06:22 PM, 31 January 2021)

Which of the following instructions, selects the peripheral address space?

Select the correct option

- | | |
|----------------------------------|-----|
| <input type="radio"/> | MOV |
| <input type="radio"/> | DEC |
| <input checked="" type="radio"/> | IN |
| <input type="radio"/> | ADD |





CS401:Quiz No. 1

Question # 10 of 10 (Start time: 12:06:45 PM, 31 January 2021)

DOS has a single entry point to access all of its services through _____.

Select the correct option

- | | |
|----------------------------------|--------|
| <input checked="" type="radio"/> | INT 21 |
| <input type="radio"/> | INT 16 |
| <input type="radio"/> | INT 2 |
| <input type="radio"/> | INT 10 |





CS401:Quiz No. 1

Question # 1 of 10 (Start time: 11:26:15 AM, 31 January 2021)

Which of the following instructions is used to read a character from the keyboard port?

Select the correct option

- | | |
|----------------------------------|--------------|
| <input type="radio"/> | out al, 0x60 |
| <input checked="" type="radio"/> | in al, 0x60 |
| <input type="radio"/> | in al, 0x21 |
| <input type="radio"/> | in al, dx |





CS401:Quiz No. 1

Question # 2 of 10 (Start time: 11:26:37 AM, 31 January 2021)

In Intel 8088, there are a total of _____ possible interrupt vectors in an Interrupt Vector Table.

Select the correct option

- 256
- 64
- 128
- 512

Click to





CS401:Quiz No. 1

Qui

Question # 3 of 10 (Start time: 11:27:04 AM, 31 January 2021)

The Programmable Interval Timer (PIT) has input frequency of _____.

Select the correct option

- | | |
|----------------------------------|-------------|
| <input checked="" type="radio"/> | 1.19318 MHZ |
| <input type="radio"/> | 2.19318 MHZ |
| <input type="radio"/> | 3.19318 MHZ |
| <input type="radio"/> | 4.19318 MHZ |

Click to Save





CS401:Quiz No. 1

Question # 4 of 10 (Start time: 11:27:28 AM, 31 January 2021)

Which of the following interrupts plays the most significant part during single step debugging of a program?

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | INT 0 |
| <input checked="" type="radio"/> | INT 1 |
| <input type="radio"/> | INT 2 |
| <input type="radio"/> | INT 3 |





CS401:Quiz No. 1

Question # 5 of 10 (Start time: 11:28:32 AM, 31 January 2021)

The offset address of an interrupt service routine "n" is at _____.

Select the correct option

- n
- nx2
- nx3
- nx4

Click to s





CS401:Quiz No. 1

Question # 6 of 10 (Start time: 11:29:04 AM, 31 January 2021)

In Programmable Interrupt Controller (PIC), which of the following ports is used to selectively enable or disable interrupts?

Select the correct option

- 19
- 20
- 21
- 22

Click to





CS401:Quiz No. 1

Question # 7 of 10 (Start time: 11:29:31 AM, 31 January 2021)

Which of the following Interrupt Requests (IRQ) is derived by a keyboard?

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | IRQ 0 |
| <input checked="" type="radio"/> | IRQ 1 |
| <input type="radio"/> | IRQ 2 |
| <input type="radio"/> | IRQ 3 |





CS401:Quiz No. 1

Question # 8 of 10 (Start time: 11:30:00 AM, 31 January 2021)

iret returns on the basis of _____ and _____.

Select the correct option

- CS, IP
- DS, IP
- CS, SS
- IP, SP

Click





Question # 9 of 10 (Start time: 11:30:25 AM, 31 January 2021)

Which of the following are the data pins used to take data from the processor to the device connected through parallel port?

Select the correct option

- 2 to 9
- 20 to 27
- 10 to 17
- 18 to 25

Click to Save



CS401:Quiz No. 1

Question # 10 of 10 (Start time: 11:30:58 AM, 31 January 2021)

Which of the following instructions, selects the peripheral address space?

Select the correct option

- | | |
|----------------------------------|-----|
| <input type="radio"/> | MOV |
| <input type="radio"/> | DEC |
| <input checked="" type="radio"/> | IN |
| <input type="radio"/> | ADD |





CS401 - Computer Architecture and Assembly Language Programming



Sonu Ilyas Mughal





CS401:Quiz No. 1

Question # 1 of 10 (Start time: 10:51:05 AM, 31 January 2021)

The Programmable Interval Timer (PIT) has input frequency of _____.

Select the correct option

- | | |
|----------------------------------|-------------|
| <input checked="" type="radio"/> | 1.19318 MHZ |
| <input type="radio"/> | 2.19318 MHZ |
| <input type="radio"/> | 3.19318 MHZ |
| <input type="radio"/> | 4.19318 MHZ |





CS401:Quiz No. 1

Question # 2 of 10 (Start time: 10:51:27 AM, 31 January 2021)

Which of the following instructions, selects the memory address space?

Select the correct option

- | | |
|----------------------------------|-----|
| <input checked="" type="radio"/> | MOV |
| <input type="radio"/> | DEC |
| <input type="radio"/> | IN |
| <input type="radio"/> | ADD |





CS401:Quiz No. 1

Question # 3 of 10 (Start time: 10:51:50 AM, 31 January 2021)

Which of the following instructions, selects the peripheral address space?

Select the correct option

- | | |
|----------------------------------|-----|
| <input type="radio"/> | MOV |
| <input type="radio"/> | DEC |
| <input checked="" type="radio"/> | IN |
| <input type="radio"/> | ADD |

Click





CS401:Quiz No. 1

Question # 4 of 10 (Start time: 10:52:10 AM, 31 January 2021)

Which of the following is the source register in OUT instruction?

Select the correct option

- | | |
|----------------------------------|----------|
| <input checked="" type="radio"/> | AL or AX |
| <input type="radio"/> | BL or BX |
| <input type="radio"/> | CL or CX |
| <input type="radio"/> | DL or DX |

Click





CS401:Quiz No. 1

Question # 5 of 10 (Start time: 10:52:30 AM, 31 January 2021)

Programmable Interrupt Controller (PIC) has _____ input signal(s) and _____ output signal(s).

Select the correct option

- | | |
|----------------------------------|--------------|
| <input type="radio"/> | one, eight |
| <input type="radio"/> | one, one |
| <input checked="" type="radio"/> | eight, one |
| <input type="radio"/> | eight, eight |

Click to





CS401:Quiz No. 1

Quiz Start

Question # 6 of 10 (Start time: 10:52:50 AM, 31 January 2021)

The thread registration code initializes the Process Control Block (PCB) and adds it to the linked list. The _____ then gives it a turn.

Select the correct option

- | | |
|----------------------------------|-------------|
| <input type="radio"/> | assembler |
| <input checked="" type="radio"/> | scheduler |
| <input type="radio"/> | linker |
| <input type="radio"/> | interpreter |

Click to Save Answer





CS401:Quiz No. 1

Quiz :

Question # 7 of 10 (Start time: 10:53:17 AM, 31 January 2021)

In programmable interrupt controller, which of the following ports is the control port?

Select the correct option

- | | |
|----------------------------------|---------|
| <input type="radio"/> | Port 19 |
| <input checked="" type="radio"/> | Port 20 |
| <input type="radio"/> | Port 21 |
| <input type="radio"/> | Port 22 |

Click to Save





CS401:Quiz No. 1

Quiz Start Time: 10:5

Question # 8 of 10 (Start time: 10:53:41 AM, 31 January 2021)

Which of the following interrupts plays the most significant part during single step debugging of a program?

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | INT 0 |
| <input checked="" type="radio"/> | INT 1 |
| <input type="radio"/> | INT 2 |
| <input type="radio"/> | INT 3 |

Click to Save Answer & Move



CS401:Quiz No. 1

Qui

Question # 9 of 10 (**Start time: 10:54:06 AM, 31 January 2021**)

IBM AT has _____ Programmable Interrupt Controllers (PIC).

Select the correct option

- 1
- 2
- 3
- 4

Click to Save





Question # 10 of 10 (Start time: 10:54:27 AM, 31 January 2021)

Which of the following stands for TSR?

Select the correct option

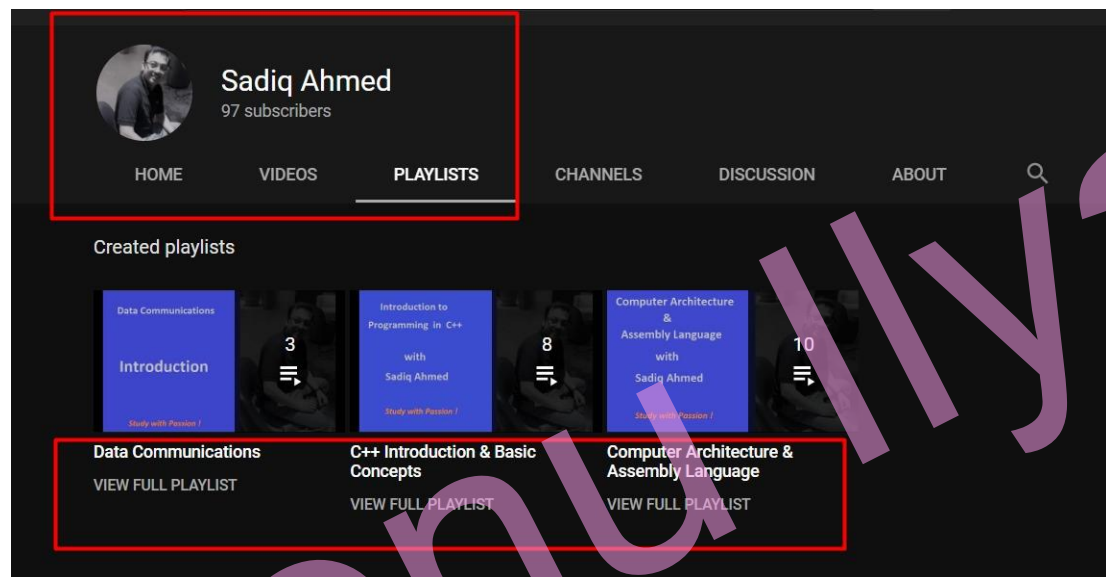
- | | |
|-------------------------------------|--------------------------------|
| <input type="radio"/> | Terminate and Store Routines |
| <input type="radio"/> | Transmit and Save Resources |
| <input checked="" type="checkbox"/> | Terminate and Stay Resident |
| <input type="radio"/> | Truncate and Start Recursively |

Click to Save A





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Subjects Enrolment

- 1) Lecture live on Zoom
- 2) Written Lectures (pdf)
- 3) Recorded Videos
- 4) Topic wise Notes
- 5) Short QA Solved
- 6) MCQs Solved
- 7) Solved Past and Current paper
- 8) Solved Graded Activities (Assignment Quiz GDB)

Activity
Detailed
Information

**Project Enrolment Complete training with
Live classes on Zoom**

- 1) SRS (Software Requirements Specification)
- 2) DD (Design Document)
- 3) Test phase
- a) Test phase viva (Live Classes for viva preparation)
- 4) Final Deliverable
 - a) Final coding
 - b) Final presentation
 - c) Final report
 - d) Pre final viva (Coding Viva)
 - e) Final viva (Overall Presentation)

*First See Our Result
Than Join Us!*

Subjects Enrolment

- 1) Lecture live on Zoom
- 2) Written Lectures (pdf)
- 3) Recorded Videos
- 4) Topic wise Notes
- 5) Short QA Solved
- 6) MCQs Solved
- 7) Solved Past and Current paper
- 8) Solved Graded Activities (Assignment Quiz GDB)

Activity
Detailed
Information

**Project Enrolment Complete training with
Live classes on Zoom**

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 - a) Final coding
 - b) Final presentation
 - c) Final report
 - d) Pre final viva (Coding Viva)
 - e) Final viva (Overall Presentation)

*First See Our Result
Than Join Us!*