


How jmp is different from jnz?

Select the correct option

 Retroad Math Equations

- jmp is a conditional jump, however jnz is an unconditional jump
- jnz permanently diverts the program flow, but jmp does not
- jmp permanently diverts the program flow, but jnz does not
- jmp is an unconditional jump, however jnz is a conditional jump

MC210203744: TEHREEM UJALA

Time Left 60  
sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 10:59 AM, 31 December 2021

Question # 8 of 10 ( Start time: 11:14:48 AM, 31 December 2021 )

Total Marks: 1

We can convert a digit into its ASCII representation by adding ..... to it.

Select the correct option

0x10

0x05

0x30

0x20

Question # 1 of 10 (start time: 09:01:45 PM, 30 December 2021)

Total Marks: 1

The \_\_\_\_\_ flag has a special role in debugging.

Select the correct option

 Reload Math Equations

- sign
- trap
- zero
- parity

Click to Save Answer & Move to Next Question



Question # 2 of 10 ( start time: 09:03:21 PM, 30 December 2021 )

Total Marks: 1

IAPX88 stands for \_\_\_\_\_

Select the correct option


- Intel Advanced Processor External 88
- Intel Active Processor Extensions 88
- Intel Advanced Process Extensions 88
- Intel Advanced Processor Extensions 88

Click to Save Answer & Move to Next Question



In \_\_\_\_\_, every bit moves one position to the right, and the bit dropped from the right is inserted at the left. The dropped bit is also copied into the carry flag.


Select the correct option

 Reload Math Equations

- Rotate Left (ROL)
- Rotate Right (ROR)
- Rotate through Carry Right (RCR)
- Rotate through Carry Left (RCL)

In \_\_\_\_\_, every bit moves one position to the right, and the bit dropped from the right is inserted at the left. The dropped bit is also copied into the carry flag.

Select the correct option

 Reload Math Equations

- Rotate Left (ROL)
- Rotate Right (ROR)
- Rotate through Carry Right (RCR)
- Rotate through Carry Left (RCL)

MC210205744: TEHREEM IQBALA

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Question # 5 of 10 ( Start time: 11:10:48 AM, 31 December 2021 )

Which of the following registers hold the address of data in Intel 8086?

Select the correct option

- |                       |                   |
|-----------------------|-------------------|
| <input type="radio"/> | ES, BP, CS and DI |
| <input type="radio"/> | ES, BP, SI and DI |
| <input type="radio"/> | ES, BP, SI and CS |
| <input type="radio"/> | ES, SP, SI and DI |

MC210205744: TEHREEM UJALA

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Question # 5 of 10 ( Start time: 11:10:48 AM, 31 December 2021 )

Which of the following registers hold the address of data in Intel 8088?

Select the correct option

- |                       |                   |
|-----------------------|-------------------|
| <input type="radio"/> | BX, BP, ES and DI |
| <input type="radio"/> | BX, BP, SI and DI |
| <input type="radio"/> | BX, BP, SI and ES |
| <input type="radio"/> | BX, SP, SI and DI |

Question # 4 of 10 ( Start time: 11:04:09 AM, 31 December 2021 )

Which of the following is the general form of addressing?

Select the correct option

- |                                  |                          |
|----------------------------------|--------------------------|
| <input type="radio"/>            | Base + Index             |
| <input type="radio"/>            | Base + Effective Address |
| <input type="radio"/>            | Index + Offset           |
| <input checked="" type="radio"/> | Base + Index + Offset    |

Question # 1 of 10 ( Start time: 10:59:50 AM, 31 December 2021 )

Total Marks

All mathematical and logical operations are performed on the .....

Select the correct option

- |                       |                       |
|-----------------------|-----------------------|
| <input type="radio"/> | Accumulator           |
| <input type="radio"/> | Program Counter       |
| <input type="radio"/> | Flags Register        |
| <input type="radio"/> | Arithmetic Logic Unit |

MC210205744: TEHREEM UJALA

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Question # 2 of 10 ( Start time: 11:01:50 AM, 51 December 2021 )

..... can also be used as a masking operation to invert selective bits.

Select the correct option

- |                       |     |
|-----------------------|-----|
| <input type="radio"/> | OR  |
| <input type="radio"/> | AND |
| <input type="radio"/> | XOR |
| <input type="radio"/> | NOT |

Question # 10 of 10 ( Start time: 10:13:25 AM, 31 December 2021 )

Total Marks: 1

Which of the following register is used to hold address of the next instruction to be executed?

Select the correct option

<input checked="" type="radio"/>	Program Counter
<input type="radio"/>	Index register
<input type="radio"/>	Accumulator register
<input type="radio"/>	Counter register



BC200412065: USAMA UMAR

Time Left 86 sec(s)

C5401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 10 of 10 ( Start time: 10:14:19 AM, 31 December 2021 )

Total Marks: 1

Which of the following is a program control instruction?

Select the correct option

- |                       |              |
|-----------------------|--------------|
| <input type="radio"/> | add bx, 0534 |
| <input type="radio"/> | mov ax, bx   |
| <input type="radio"/> | jne 0534     |
| <input type="radio"/> | cld          |

Click to Save Answer &amp; Move to Next Question



BC200412005: USAMA UMAR

Time Left 86  
sec(s)CS401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 8 of 10 ( start time: 10:10:51 AM, 31 December 2021 )

Total Marks: 1

The basic function of register is to hold \_\_\_\_\_.

Select the correct option

- |                       |           |
|-----------------------|-----------|
| <input type="radio"/> | mnemonics |
| <input type="radio"/> | operands  |
| <input type="radio"/> | operators |
| <input type="radio"/> | opcodes   |

Click to Save Answer &amp; Move to Next Question



BC200412005: USAMA UMAR

Time Left 86  
sec(s)CS401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 8 of 10 ( start time: 10:10:51 AM, 31 December 2021 )

Total Marks: 1

The basic function of register is to hold \_\_\_\_\_.

Select the correct option

- |                       |           |
|-----------------------|-----------|
| <input type="radio"/> | mnemonics |
| <input type="radio"/> | operands  |
| <input type="radio"/> | operators |
| <input type="radio"/> | opcodes   |

Click to Save Answer &amp; Move to Next Question



BC200412065: USAMA UMAR

Time Left 88 sec(s)

C5401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 9 of 10 ( Start time: 10:12:28 AM, 31 December 2021 )

Total Marks: 1

A 16-bit processor has an accumulator of \_\_\_\_\_

Select the correct option

Reload Math Equations

- 8 bit
- 64 bit
- 16 bit
- 32 bit

Click to Save Answer & Move to next Question



BC200412005: USAMA UMAR

Time Left 87 sec(s)

C5401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 7 of 10 ( Start time: 10:09:15 AM, 31 December 2021 )

Total Marks: 1

REPE and REPNE prefixes are only meaningful with .....

Select the correct option

- MOV
- CMPS
- STOS
- LODS

Click to Save Answer & Move to Next Question





BC200412005: USAMA UMAR

Time Left 87  
sec(s)C5401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 5 of 10 ( start time: 10:08:23 AM, 31 December 2021)

Total Marks: 1

Stack is a data structure that behaves in \_\_\_\_\_ manner.

Select the correct option

- |                       |                    |
|-----------------------|--------------------|
| <input type="radio"/> | FIFO               |
| <input type="radio"/> | Both LIFO and FIFO |
| <input type="radio"/> | LILO               |
| <input type="radio"/> | LIFO               |

[Click to Save Answer & Move to Next Question](#)



BC200412005: USAMA UMAR

Time Left 87  
sec(s)CS401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 4 of 10 ( start time: 10:04:52 AM, 31 December 2021 )

Total Marks: 1

Which of the following bit is dropped into the carry flag after the execution of "Shift Logical Right" operation?

Select the correct option

- |                       |                |
|-----------------------|----------------|
| <input type="radio"/> | 0 bit          |
| <input type="radio"/> | Right most bit |
| <input type="radio"/> | Left most bit  |
| <input type="radio"/> | 1 bit          |

Click to Save Answer &amp; Move to Next Question



BC200412065: USAMA UMAR

Time Left 87  
sec(s)C5401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 6 of 10 ( Start time: 10:07:46 AM, 31 December 2021 )

Total Marks: 1

How logical errors are different from syntax errors?

Select the correct option

Reload Math Equations

- |                       |  |
|-----------------------|--|
| <input type="radio"/> | Identifying syntax errors is the responsibility of programmer unlike logical errors                  |
| <input type="radio"/> | Identifying both types of errors is the responsibility of assembler                                  |
| <input type="radio"/> | Identifying syntax and logical errors is the responsibility of assembler and programmer respectively |
| <input type="radio"/> | Syntax errors are more important than the logical errors   |

Click to Save Answer &amp; Move to next Question



BC200412065: USAMA UMAR

Time Left 87  
sec(s)CS401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 2 of 10 (Start time: 10:01:46 AM, 31 December 2021)

Total Marks: 1

In \_\_\_\_\_ operation, the output is 1 only if both inputs are 1.

Select the correct option

<input type="radio"/>	OR
<input type="radio"/>	NOT
<input type="radio"/>	AND
<input type="radio"/>	XOR

[Click to Save Answer & Move to Next Question](#)



BC200412005: USAMA UMAR

Time Left 87  
sec(s)CS401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 5 of 10 ( start time: 10:08:23 AM, 31 December 2021)

Total Marks: 1

Stack is a data structure that behaves in \_\_\_\_\_ manner.

Select the correct option

<input type="radio"/>	FIFO
<input type="radio"/>	Both LIFO and FIFO
<input type="radio"/>	LILO
<input type="radio"/>	LIFO

[Click to Save Answer & Move to Next Question](#)



BC200412005: USAMA UMAR

Time Left 86 sec(s)

C5401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 3 of 10 ( Start time: 10:03:36 AM, 31 December 2021)

Total Marks: 1

..... decrements SP (the stack pointer) by two, and then transfers a word from the source operand to the top of the stack.

Select the correct option

Reload Math Equations

- PUSH
- CALL
- RET
- POP

Click to Save Answer & Move to next Question





BC200417715: WALEED ARSHAD VIRK

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:43 AM, 31 December 2021

Question # 9 of 10 ( start time: 09:52:08 AM, 31 December 2021 )

Total Marks: 1

In Shift logical Left (SHL), the \_\_\_\_\_ bit is dropped into the carry flag.

Select the correct option

Reload Math Equations

- second least significant
- third least significant
- most significant
- least significant

Click to Save Answer & Move to Next Question





BC200412065: USAMA UMAR

Time Left: 87  
sec(s)CS401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 09:59 AM, 31 December 2021

Question # 2 of 10 ( Start time: 10:01:46 AM, 31 December 2021 )

Total Marks: 1

In \_\_\_\_\_ operation, the output is 1 only if both inputs are 1.

Select the correct option

<input type="radio"/>	OR
<input type="radio"/>	NOT
<input type="radio"/>	AND
<input type="radio"/>	XOR

Click to Save Answer &amp; Move to Next Question

Question # 7 of 10 ( start time: 09:49:59 AM, 31 December 2021 )

Total Marks: 1

In STOS, the implied source operand always resides in \_\_\_\_\_.

Select the correct option

<input type="radio"/>	CL or CX
<input type="radio"/>	DL or DX
<input type="radio"/>	BL or BX
<input type="radio"/>	AL or AX

[Click to Save Answer & Move to Next Question](#)

Question # 6 of 10 ( Start time: 09:49:30 AM, 31 December 2021 )

Total Marks: 1

Which of following is an illegal assembly instruction?

Select the correct option

- |                       |                    |
|-----------------------|--------------------|
| <input type="radio"/> | mov [num1], al     |
| <input type="radio"/> | mov [num1], [num2] |
| <input type="radio"/> | mov ax, bx         |
| <input type="radio"/> | mov al, bl         |

Click to Save Answer & Move to Next Question

BC200417715: WALEED ARSHAD VIRK

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 09:43 AM, 31 December 2021

Question # 4 of 10 ( start time: 09:47:04 AM, 31 December 2021 )

Total Marks: 1

sub sp, 2

The above instruction is used to perform a \_\_\_\_\_ operation on stack.

Select the correct option

 Reload Math Equations

- POP
- PUSH
- RET n
- RET



BC200417715: WALEED ARSHAD VIRK

Time Left 88 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:43 AM, 31 December 2021

Question # 5 of 10 ( start time: 09:48:00 AM, 31 December 2021 )

Total Marks: 1

The maximum amount of memory accessible using 8085 processor is -----

Select the correct option

- 1 MB
- 2 MB
- 64 KB
- 85 KB

Click to Save Answer & Move to Next Question



Question # 3 of 10 ( Start time: 09:45:46 AM, 31 December 2021 )

Total Marks: 1

In direct addressing, the memory address given in the instruction is \_\_\_\_\_.

Select the correct option

[Reload Math Equations](#)

- |                       |          |
|-----------------------|----------|
| <input type="radio"/> | variable |
| <input type="radio"/> | empty    |
| <input type="radio"/> | register |
| <input type="radio"/> | fixed    |

[Click to Save Answer & Move to Next Question](#)

Question # 2 of 10 ( Start time: 09:44:46 AM, 31 December 2021 )

Total Marks: 1

What makes CMP different from SUB?

Select the correct option

 Reload Math Equations

- |                       |   |
|-----------------------|---|
| <input type="radio"/> | Both CMP and SUB are destructive subtraction.   |
| <input type="radio"/> | SUB is a destructive subtraction while CMP is a non-destructive subtraction. Both affect the flags.           |
| <input type="radio"/> | CMP is a non-destructive subtraction that does not affect any flag, however SUB is a destructive subtraction. |
| <input type="radio"/> | SUB is a non-destructive subtraction while CMP is a destructive one.  |

Click to Save Answer &amp; Move to Next Question

Question # 1 of 10 ( start time: 09:43:29 AM, 31 December 2021 )

Total Marks: 1

Which of the following are the basic bitwise logical operations?

Select the correct option

- |                       |                        |
|-----------------------|------------------------|
| <input type="radio"/> | AND, OR, XOR, and NOT  |
| <input type="radio"/> | NOT, OR, AND, and NNOT |
| <input type="radio"/> | AND, NOR, OR and NOT   |
| <input type="radio"/> | AND, OR, XOR, and NAND |

Click to Save Answer & Move to Next Question

Question # 1 of 10 ( start time: 09:43:29 AM, 31 December 2021 )

Total Marks: 1

Which of the following are the basic bitwise logical operations?

Select the correct option

- |                       |                        |
|-----------------------|------------------------|
| <input type="radio"/> | AND, OR, XOR, and NOT  |
| <input type="radio"/> | NOT, OR, AND, and NNOT |
| <input type="radio"/> | AND, NOR, OR and NOT   |
| <input type="radio"/> | AND, OR, XOR, and NAND |

Click to Save Answer & Move to Next Question



BC200414332: YASIR KHAN

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 9 of 10 ( Start time: 09:34:47 AM, 31 December 2021 )

Total Marks: 1

To convert the case of a character, we add or subtract \_\_\_\_\_ from its ASCII code.

Select the correct option

- 0x41
- 0x20
- 0x10
- 0x30

Click to Save Answer & Move to Next Question





BC200414332: YASIR KHAN

Time Left 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 7 of 10 ( Start time: 09:30:22 AM, 31 December 2021 )

Total Marks: 1

sub sp, 2

The above instruction is used to perform a\_\_\_\_\_ operation on stack.

Select the correct option

Reload Math Equations

- POP
- RET
- RET n
- PUSH

Click to Save Answer & Move to Next Question





BC200414332: YASIR KHAN

Time Left 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 7 of 10 ( Start time: 09:30:22 AM, 31 December 2021 )

Total Marks: 1

sub sp, 2

The above instruction is used to perform a \_\_\_\_\_ operation on stack.

Select the correct option

Reload Math Equations

- POP
- RET
- RET n
- PUSH

Click to Save Answer & Move to Next Question





BC200414332: YASIR KHAN

Time Left 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 6 of 10 ( Start time: 09:28:29 AM, 31 December 2021 )

Total Marks: 1

When the RET instruction is executed, it recovers the value of the \_\_\_\_\_ from the stack.

Select the correct option

Reload Math Equations

- accumulator register
- instruction pointer
- previous instruction
- current instruction

Click to Save Answer & Move to Next Question





BC200414332: YASIR KHAN

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 5 of 10 ( Start time: 09:26:34 AM, 31 December 2021 )

Total Marks: 1

In Shift logical Left (SHL), the \_\_\_\_\_ bit is dropped into the carry flag.

Select the correct option

Reload Math Equations

- third least significant
- least significant
- most significant
- second least significant

Click to Save Answer & Move to Next Question





BC200414332: YASIR KHAN

Time Left 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 6 of 10 ( Start time: 09:28:29 AM, 31 December 2021 )

Total Marks: 1

When the RET instruction is executed, it recovers the value of the \_\_\_\_\_ from the stack.

Select the correct option

Reload Math Equations

- accumulator register
- instruction pointer
- previous instruction
- current instruction

Click to Save Answer & Move to Next Question





BC200414332: YASIR KHAN

Time Left 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 6 of 10 ( Start time: 09:28:29 AM, 31 December 2021 )

Total Marks: 1

When the RET instruction is executed, it recovers the value of the \_\_\_\_\_ from the stack.

Select the correct option

Reload Math Equations

- accumulator register
- instruction pointer
- previous instruction
- current instruction

Click to Save Answer & Move to Next Question





BC200414332: YASIR KHAN

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 4 of 10 ( Start time: 09:24:44 AM, 31 December 2021 )

Total Marks: 1

When the relative address stored with the instruction is in 16-bits, the jump is called a \_\_\_\_\_ jump.

Select the correct option

- Far
- Short
- Near
- Long

Click to Save Answer & Move to Next Question





BC200414332: YASIR KHAN

Time Left 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 1 of 10 ( Start time: 09:19:21 AM, 31 December 2021 )

Total Marks: 1

Registers are storage cells \_\_\_\_\_.

Select the correct option

- both inside and outside the processor
- outside the processor
- inside the memory
- inside the processor

Click to Save Answer & Move to Next Question




Question # 1 of 10 (start time: 09:01:45 PM, 30 December 2021)

Total Marks: 1

The \_\_\_\_\_ flag has a special role in debugging.

Select the correct option

 Reload Math Equations

- sign
- trap
- zero
- parity

Click to Save Answer & Move to Next Question



Question # 2 of 10 ( start time: 09:03:21 PM, 30 December 2021 )

Total Marks: 1

IAPX88 stands for \_\_\_\_\_

Select the correct option

- Intel Advanced Processor External 88
- Intel Active Processor Extensions 88
- Intel Advanced Process Extensions 88
- Intel Advanced Processor Extensions 88

Click to Save Answer & Move to Next Question





BC200414332: YASIR KHAN

Time Left 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:19 AM, 31 December 2021

Question # 1 of 10 ( Start time: 09:19:21 AM, 31 December 2021 )

Total Marks: 1

Registers are storage cells \_\_\_\_\_.

Select the correct option

- both inside and outside the processor
- outside the processor
- inside the memory
- inside the processor

Click to Save Answer & Move to Next Question




Question # 1 of 10 ( start time: 09:01:45 PM, 30 December 2021 )

Total Marks: 1

The \_\_\_\_\_ flag has a special role in debugging.

Select the correct option

 Reload Math Equations

- sign
- trap
- zero
- parity

Click to Save Answer & Move to Next Question



Question # 2 of 10 ( start time: 09:03:21 PM, 30 December 2021 )

Total Marks: 1

IAPX88 stands for \_\_\_\_\_

Select the correct option

- Intel Advanced Processor External 88
- Intel Active Processor Extensions 88
- Intel Advanced Process Extensions 88
- Intel Advanced Processor Extensions 88

Click to Save Answer & Move to Next Question



Question # 3 of 10 ( Start time: 11:29:38 PM, 30 December 2021 )

Total Marks: 1

Star topology based network cannot detect\_\_\_\_\_

Select the correct option

- Collision
- Packets
- Deadlock
- Transmission

Question # 3 of 10 ( Start time: 11:29:38 PM, 30 December 2021 )

Total Marks: 1

Star topology based network cannot detect\_\_\_\_\_

Select the correct option

- Collision
- Packets
- Deadlock
- Transmission

----- subtracts one from the operand.

Select the correct option

<input type="radio"/>	Modulus
<input type="radio"/>	DEC
<input type="radio"/>	Shift
<input type="radio"/>	MEC

Click to Save Answer & Move to

Stack is a data structure that behaves in \_\_\_\_\_ manner.

**Select the correct option**

<input type="radio"/>	LILO
<input type="radio"/>	Both LIFO and FIFO
<input type="radio"/>	LIFO
<input type="radio"/>	FIFO

Click to S

STI stands for?

Select the correct option

- |                                  |                          |
|----------------------------------|--------------------------|
| <input type="radio"/>            | Set Interrupt            |
| <input checked="" type="radio"/> | Set the Interrupt flag   |
| <input type="radio"/>            | Standard Timer interrupt |
| <input type="radio"/>            | Set Instruction          |

Click

RET Instruction takes \_\_\_\_\_ argument(s).

**Select the correct option**

<input type="radio"/>	1
<input type="radio"/>	3
<input type="radio"/>	2
<input type="radio"/>	0

A symbol associated to a point in the program is called a \_\_\_\_\_.

**Select the correct option**

<input type="radio"/>	JUMP
<input type="radio"/>	Stack
<input type="radio"/>	Subroutines
<input type="radio"/>	Label

How logical errors are different from syntax errors?

**Select the correct option**

- |                       |  |
|-----------------------|--|
| <input type="radio"/> | Identifying syntax errors is the responsibility of programmer unlike logical errors                  |
| <input type="radio"/> | Identifying syntax and logical errors is the responsibility of assembler and programmer respectively |
| <input type="radio"/> | Identifying both types of errors is the responsibility of assembler                                  |
| <input type="radio"/> | Syntax errors are more important than the logical errors   |

Click to Save Ans

STI stands for?

**Select the correct option**

<input type="radio"/>	Set Interrupt
<input type="radio"/>	Set the Interrupt flag
<input type="radio"/>	Standard Timer Interrupt
<input type="radio"/>	Set Instruction

Click

Which of the following is a program control instruction?

**Select the correct option**

<input type="radio"/>	<code>mov ax, bx</code>
<input type="radio"/>	<code>jne 0534</code>
<input type="radio"/>	<code>add bx, 0534</code>
<input type="radio"/>	<code>cli</code>

Click

BC200416752: TAHREEM ZAFAR

Time Left 90  
sec(s)

C5401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 10:08 PM, 30 December 2021

Question # 10 of 10 ( start time: 10:19:38 PM, 30 December 2021 )

Total Marks: 1

In 8088, there is a \_\_\_\_\_ stack.

Select the correct option

semi-automatic

incrementing

stagnant

decrementing

Click to Save Answer & Move to Next Question

CX register is mostly used as a \_\_\_\_\_ register.

**Select the correct option**

<input type="radio"/>	base
<input type="radio"/>	flag
<input type="radio"/>	counter
<input type="radio"/>	destination

CX register is mostly used as a \_\_\_\_\_ register.

**Select the correct option**

<input type="radio"/>	base
<input type="radio"/>	flag
<input type="radio"/>	counter
<input type="radio"/>	destination



BC200416752: TAHREEM ZAFAR

Time Left 89  
sec(s)

CS401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)


Quiz Start Time: 10:08 PM, 30 December 2021

Question # 8 of 10 ( start time: 10:16:56 PM, 30 December 2021 )

Total Marks: 1

There are \_\_\_\_\_ byte(s) for each character on the screen.

Select the correct option

 Reload Math Equations

6

4

2

1

BC200416752: TAHREEM ZAFAR

Time Left 88  
sec(s)

C5401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 10:08 PM, 30 December 2021

Question # 5 of 10 ( start time: 10:14:55 PM, 30 December 2021 )

Total Marks: 1

Whenever an instruction needs a memory source, \_\_\_\_\_ holds the pointer to it.

Select the correct option

DS:SP

DS:SI

ES:DI

ES:DI

Click to Save Answer & Move to Next Question

BC200416752: TAHREEM ZAFAR

Time Left 88  
sec(s)

C5401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 10:08 PM, 30 December 2021

Question # 5 of 10 ( start time: 10:14:55 PM, 30 December 2021 )

Total Marks: 1

Whenever an instruction needs a memory source, \_\_\_\_\_ holds the pointer to it.

Select the correct option

DS:SP

DS:SI

ES:DI

ES:DI

Click to Save Answer & Move to Next Question

Question # 7 of 10 ( Start time: 10:16:02 PM, 30 December 2021 )

Total Marks: 1

Keeping in view the downward compatibility between the two systems, the codes written for the intel 8088 are ..... on the intel 386 processor.

Select the correct option

[Reload Math Equations](#)

<input type="radio"/>	invalid
<input type="radio"/>	control
<input type="radio"/>	altered
<input type="radio"/>	valid

[Click to Save Answer & Move to Next Question](#)



BC200416752: TAHREEM ZAFAR

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 10:08 PM, 30 December 2021

Question # 7 of 10 (Start time: 10:16:02 PM, 30 December 2021)

Total Marks: 1

Keeping in view the downward compatability between the two systems, the codes written for the Intel 8088 are \_\_\_\_\_ on the Intel 386 processor.

Select the correct option

Reload Math Equations

- invalid
- control
- altered
- valid

Click to Save Answer & Move to Next Question

Question # 6 of 10 ( start time: 10:14:55 PM, 30 December 2021 )

Total Marks: 1

Whenever an instruction needs a memory source, \_\_\_\_\_ holds the pointer to it.

Select the correct option

<input type="radio"/>	DS:SP
<input type="radio"/>	DS:SI
<input type="radio"/>	ES:DX
<input type="radio"/>	ES:DI

[Click to Save Answer & Move to Next Question](#)

Question # 4 of 10 ( start time: 10:11:53 PM, 30 December 2021)

Total Marks: 1

..... decrements SP (the stack pointer) by two, and then transfers a word from the source operand to the top of the stack.

Select the correct option

[Reload Math Equations](#)

<input type="radio"/>	BET
<input type="radio"/>	POP
<input type="radio"/>	PUSH
<input type="radio"/>	CALL

[Click to Save Answer & Move to Next Question](#)



BC200416752: TAHREEM ZAFAR

Time Left 88 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 10:08 PM, 30 December 2021

Question # 4 of 10 ( start time: 10:11:53 PM, 30 December 2021 )

Total Marks: 1

----- decrements SP (the stack pointer) by two, and then transfers a word from the source operand to the top of the stack.

Select the correct option

Reload Math Equations

- |                       |      |
|-----------------------|------|
| <input type="radio"/> | RET  |
| <input type="radio"/> | POP  |
| <input type="radio"/> | PUSH |
| <input type="radio"/> | CALL |

Click to Save Answer & Move to Next Question



BC200416752: TAHREEM ZAFAR

Time Left 88 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 10:08 PM, 30 December 2021

Question # 4 of 10 ( start time: 10:11:53 PM, 30 December 2021 )

Total Marks: 1

----- decrements SP (the stack pointer) by two, and then transfers a word from the source operand to the top of the stack.

Select the correct option

Reload Math Equations

- |                       |      |
|-----------------------|------|
| <input type="radio"/> | RET  |
| <input type="radio"/> | POP  |
| <input type="radio"/> | PUSH |
| <input type="radio"/> | CALL |

Click to Save Answer & Move to Next Question

BC200416752: TAHREEM ZAFAR

Time Left 87  
sec(s)

C5401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 10:08 PM, 30 December 2021

Question # 2 of 10 ( start time: 10:09:31 PM, 30 December 2021 )

Total Marks: 1

In multiplication algorithm, we take the first digit of the multiplier and multiply it with the \_\_\_\_\_.

Select the correct option



Reminder



Divider



Result



Multiplicand

Click to Save Answer & Move to Next Question

BC200416752: TAHREEM ZAFAR

Time Left 87  
sec(s)

C5401 - Computer Architecture and Assembly Language  
Programming (Quiz No. 1)

Quiz Start Time: 10:08 PM, 30 December 2021

Question # 2 of 10 ( start time: 10:09:31 PM, 30 December 2021 )

Total Marks: 1

In multiplication algorithm, we take the first digit of the multiplier and multiply it with the \_\_\_\_\_.

Select the correct option



Reminder



Divider



Result



Multiplicand

Click to Save Answer & Move to Next Question

Select the correct option

<input type="radio"/>	Stack Pointer
<input type="radio"/>	Flags
<input type="radio"/>	Program Counter
<input type="radio"/>	Index

Click to S

The ordering of a program's instructions is ensured by the \_\_\_\_\_ register.

**Select the correct option**

<input type="radio"/>	Stack Pointer
<input type="radio"/>	Flags
<input type="radio"/>	Program Counter
<input type="radio"/>	Index

Click to S



🔒 vulms.vu.edu.pk/Quiz/Q

1



**Time Left** 43 sec(s)

age

Quiz Start Time: 09:44 PM, 30 December 2021

er 2021)

Total Marks: |

mented by \_\_\_\_\_

▶ Saving...





When an element is pushed on the stack, SP is decremented by \_\_\_\_\_.

**Select the correct option**

- four
- five
- two
- three





In iAPX88, \_\_\_\_\_ flag is specially related to the string instructions.

Select the correct option

- direction
- sign
- carry
- parity





🔒 vulms.vu.edu.pk/Quiz/Q

1



**Time Left** 43 sec(s)

age

Quiz Start Time: 09:44 PM, 30 December 2021

er 2021)

Total Marks: |

mented by -----

▶ Saving...





When an element is pushed on the stack, SP is decremented by \_\_\_\_\_.

**Select the correct option**

- four
- five
- two
- three



When an element is pushed on the stack, SP is decremented by \_\_\_\_\_.

Select the correct option

- four
- five
- two
- three





Which of following conditional jump is not dependant on any flag?

**elect the correct option**

- |                                  |      |
|----------------------------------|------|
| <input type="radio"/>            | JNG  |
| <input checked="" type="radio"/> | JCXZ |
| <input type="radio"/>            | JP   |
| <input type="radio"/>            | JPE  |



Which of following conditional jump is not dependant on any flag?

**elect the correct option**

- |                       |      |
|-----------------------|------|
| <input type="radio"/> | JNG  |
| <input type="radio"/> | JCXZ |
| <input type="radio"/> | JP   |
| <input type="radio"/> | JPE  |



When an element is pushed on the stack, SP is decremented by \_\_\_\_\_.

**Select the correct option**

- four
- five
- two
- three





Which of following conditional jump is not dependant on any flag?

**elect the correct option**

- |                       |      |
|-----------------------|------|
| <input type="radio"/> | JNG  |
| <input type="radio"/> | JCXZ |
| <input type="radio"/> | JP   |
| <input type="radio"/> | JPE  |



Question # 5 of 10 (Start time: 09:52:37 PM, 30 December 2021)

Total

In \_\_\_\_\_ operation, a zero is inserted from the left and every bit moves one position to the right. The right most bit is discarded into the carry flag.

Select the correct option

Reload Math Editor

- Shift Arithmetic Left (SAL)
- Shift Arithmetic Right (SAR)
- Shift logical Right (SHR)
- Shift logical Left (SHL)

Click to Save Answer & Move to Next Question





Question # 5 of 10 (Start time: 09:52:37 PM, 30 December 2021)

Total

In \_\_\_\_\_ operation, a zero is inserted from the left and every bit moves one position to the right. The right most bit is d into the carry flag.

Select the correct option

Reload Math E

- Shift Arithmetic Left (SAL)
- Shift Arithmetic Right (SAR)
- Shift logical Right (SHR)
- Shift logical Left (SHL)

Click to Save Answer & Move to Next Qu

}





In case of DIV BX instruction, the quotient is stored in \_\_\_\_\_ register.

Select the correct option

- AL
- AX
- DX
- BL





Question # 5 of 10 (Start time: 09:52:37 PM, 30 December 2021)

Total

In \_\_\_\_\_ operation, a zero is inserted from the left and every bit moves one position to the right. The right most bit is discarded into the carry flag.

Select the correct option

Reload Math Editor

- Shift Arithmetic Left (SAL)
- Shift Arithmetic Right (SAR)
- Shift logical Right (SHR)
- Shift logical Left (SHL)

Click to Save Answer & Move to Next Question





Which of the following flags sets when a larger number is subtracted from a smaller number?

Select the correct option

- ZF
- CF
- SF
- OF

Click





Question # 20110 ( Start time: 09:40:33 PM, 09 December 2021 )

Which of following is an illegal assembly instruction?

Select the correct option

- `mov ax, bx`
- `mov [num1], al`
- `mov al, bl`
- `mov [num1], [num2]`





Question # 20110 ( Start time: 09:40:55 PM, 09 December 2021 )

Which of following is an illegal assembly instruction?

Select the correct option

- |                                  |                    |
|----------------------------------|--------------------|
| <input checked="" type="radio"/> | mov ax, bx         |
| <input type="radio"/>            | mov [num1], al     |
| <input type="radio"/>            | mov al, bl         |
| <input type="radio"/>            | mov [num1], [num2] |





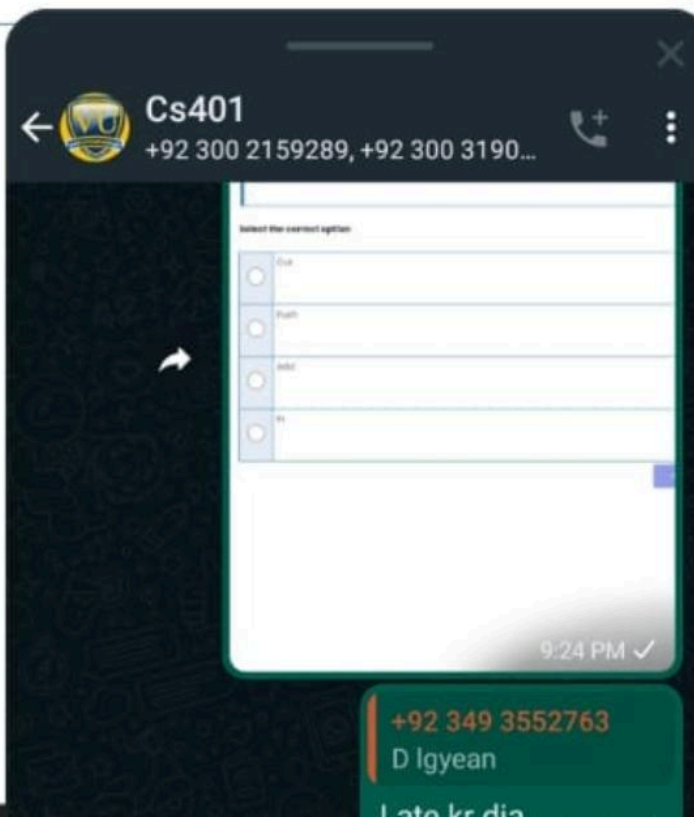
Cs401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Question # 4 of 10 ( Start time: 09:25:18 PM, 30 December 2021 )

Which of the following is the most illegal instruction?

Select the correct option

- Mov ax, al
- Mov al, [num1]
- Mov ax, [num1]
- Mov al, ax





Question # 10 of 10 ( Start time: 09:29:36 PM, 30 December 202

In ADC instruction, there are \_\_\_\_\_.

Select the correct option

- |                       |                                       |
|-----------------------|---------------------------------------|
| <input type="radio"/> | two operands and the CF               |
| <input type="radio"/> | two registers only                    |
| <input type="radio"/> | two operands and the ZF               |
| <input type="radio"/> | two registers and one memory location |



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Call instruction takes a \_\_\_\_\_ as an argument.

Select the correct option

- |                       |                      |
|-----------------------|----------------------|
| <input type="radio"/> | memory address       |
| <input type="radio"/> | destination register |
| <input type="radio"/> | source register      |
| <input type="radio"/> | label                |

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9:29 PM

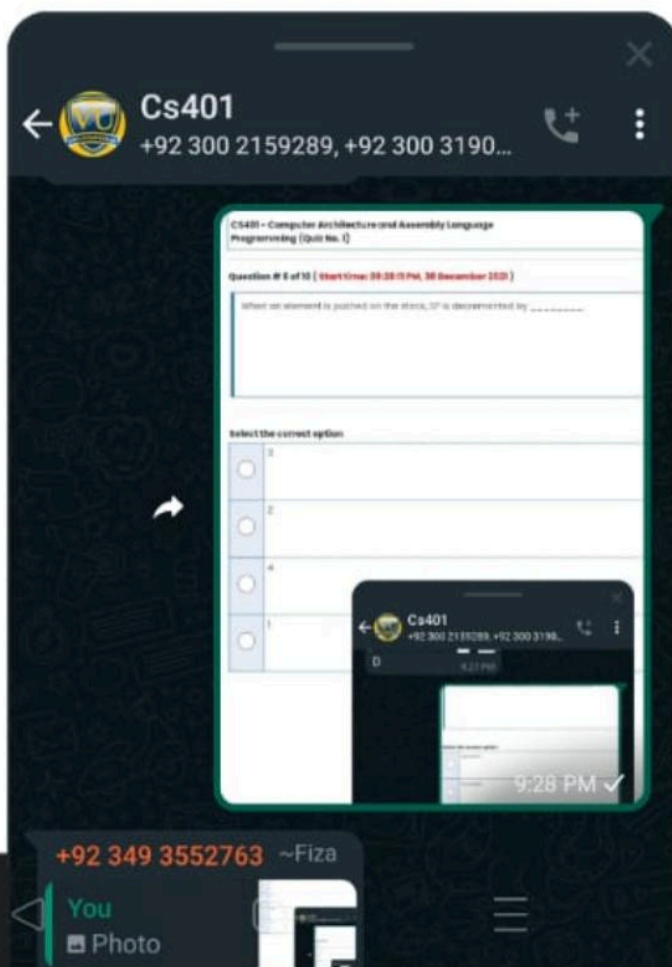


Question # 9 of 10 ( Start time: 09:28:53 PM, 30 December 2021 )

Call Instruction takes a \_\_\_\_\_ as an argument.

Select the correct option

- memory address
- destination register
- source register
- label





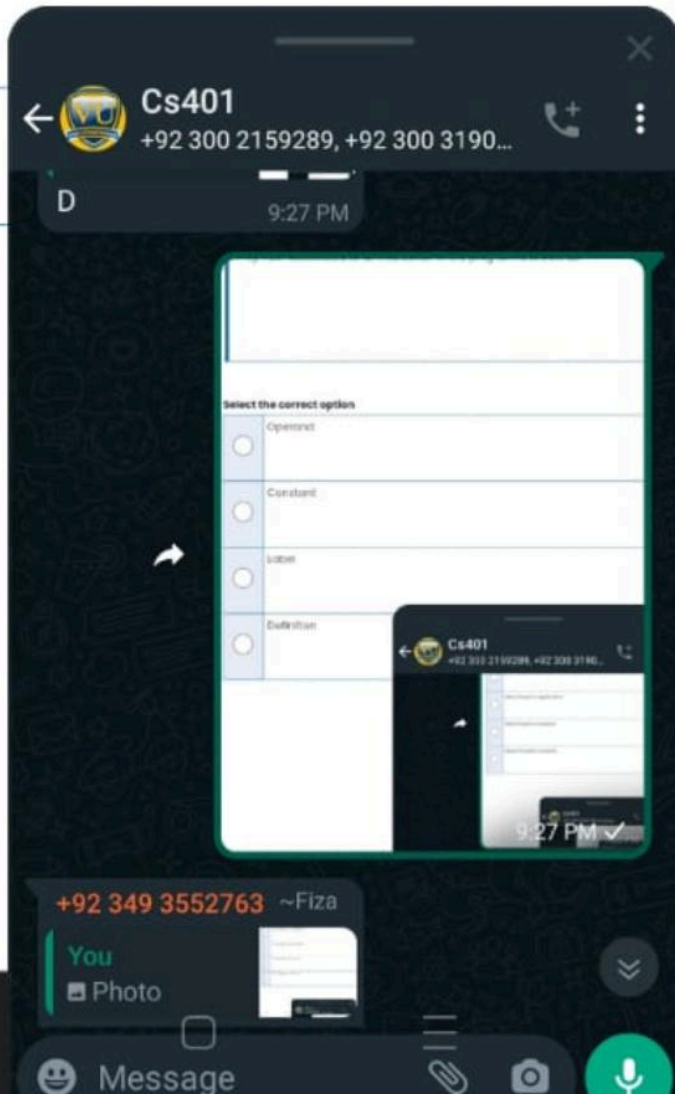
CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Question # 8 of 10 ( Start time: 09:28:11 PM, 30 December 2021 )

When an element is pushed on the stack, SP is decremented by \_\_\_\_\_.

Select the correct option

- 3
- 2
- 4
- 1

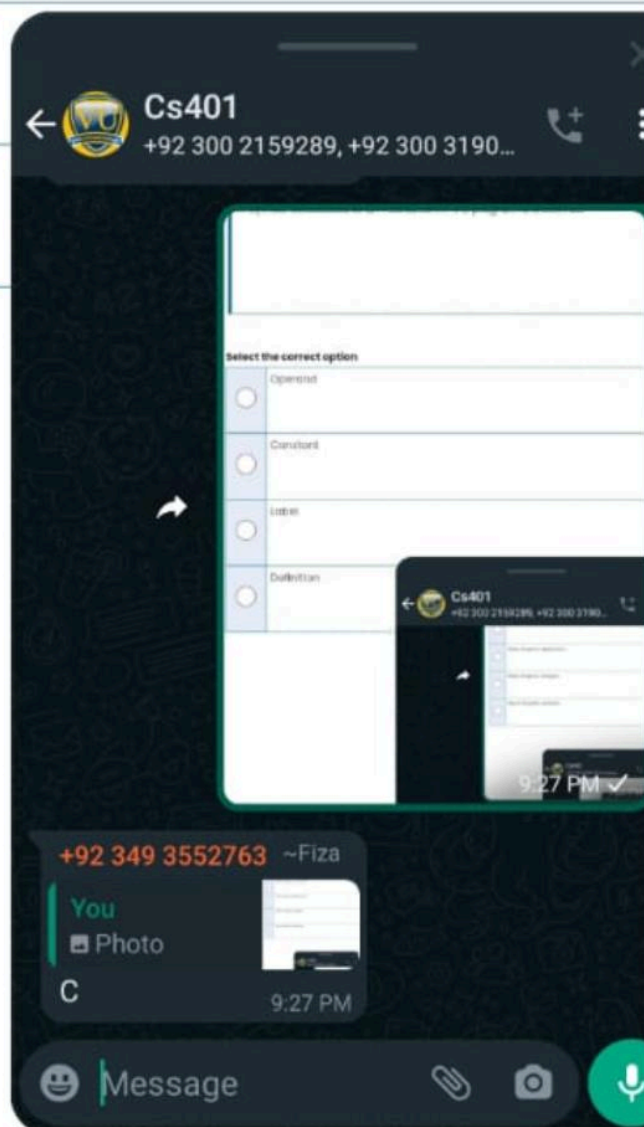




Which of the following jump does not depend on flags register?

Select the correct option

- JO
- JP
- JCXZ
- JNE

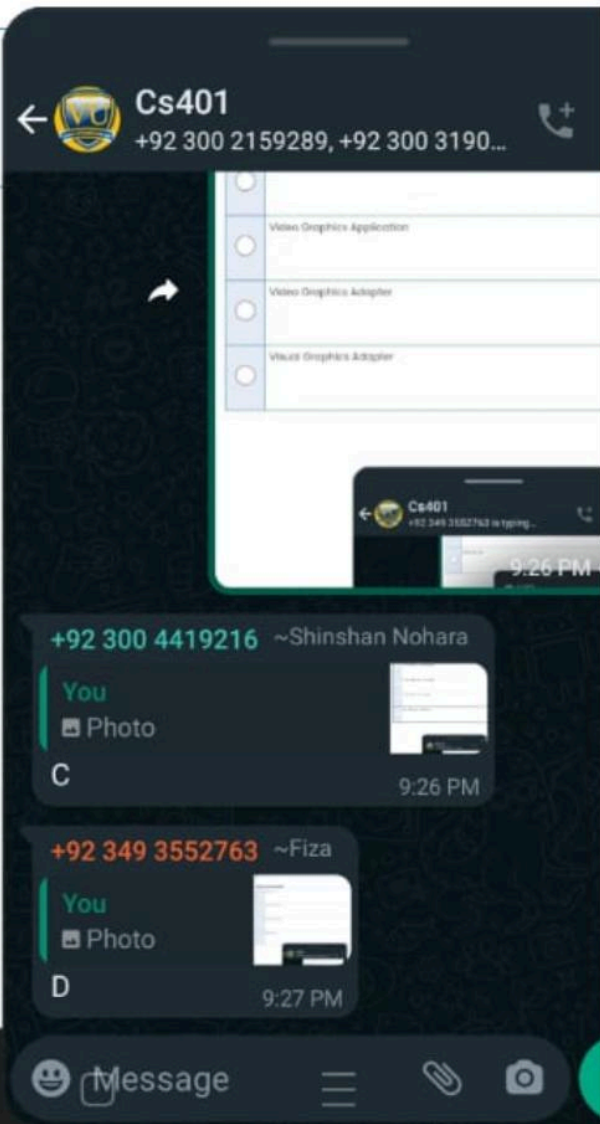


Question # 6 of 10 ( Start time: 09:27:04 PM, 30 December 2021 )

A symbol associated to an instruction in the program is known as:

Select the correct option

- Operand
- Constant
- Label
- Definition



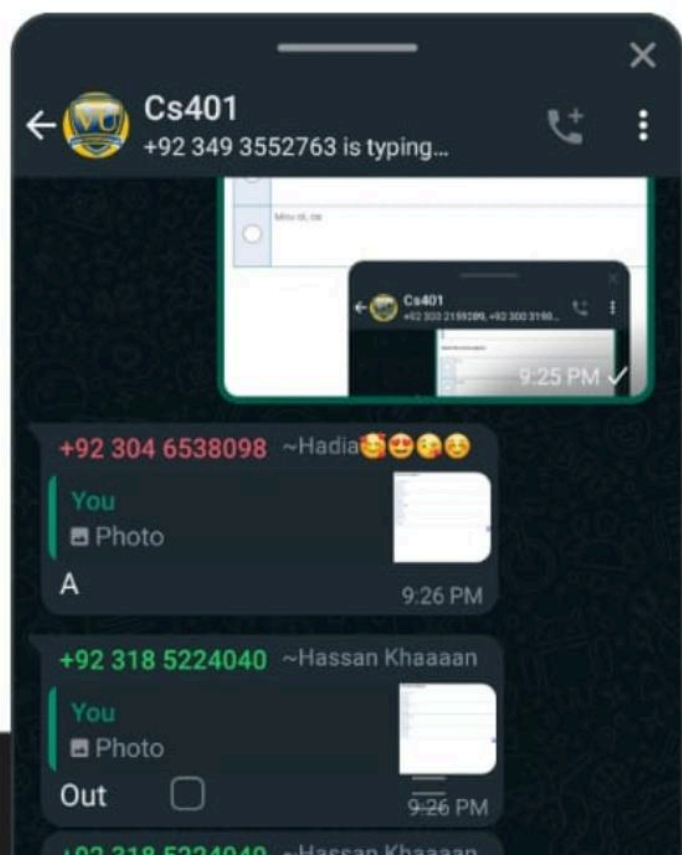
Programming (Quiz No. 1)

Question # 5 of 10 ( Start time: 09:26:23 PM, 30 December 2021 )

VGA stands for \_\_\_\_\_

Select the correct option

- Video Graphic Accumulator
- Video Graphics Application
- Video Graphics Adapter
- Visual Graphics Adapter



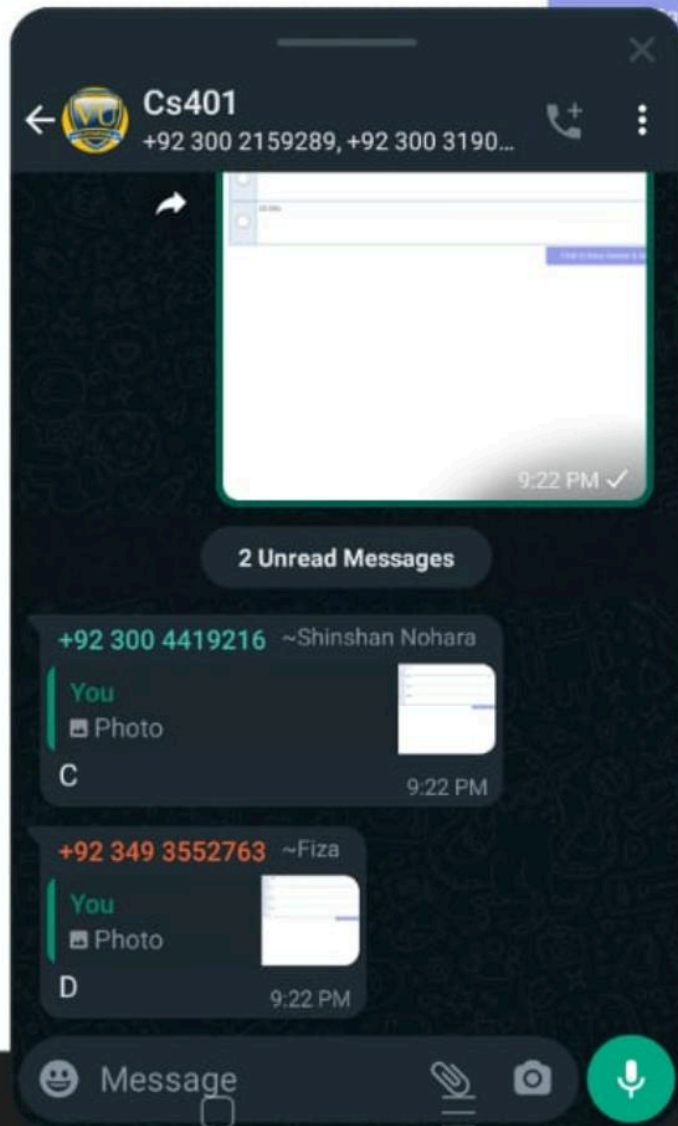


Question # 2 of 10 ( Start time: 09:23:05 PM, 30 December 2021 )

When the control is transferred to a subroutine?

Select the correct option

- When the instruction pointer is not available
- When the return address is recovered
- After the arguments are pushed on the stack
- After the arguments are popped out from the stack





## Cs401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Question # 4 of 10 ( Start time: 09:25:18 PM, 30 December 2021 )

Which of the following is the most illegal instruction?

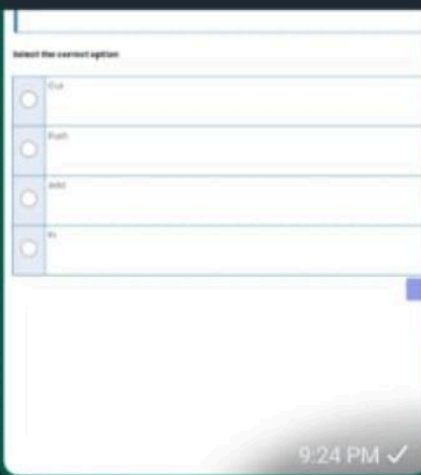
Select the correct option

- |                       |                |
|-----------------------|----------------|
| <input type="radio"/> | Mov ax, al     |
| <input type="radio"/> | Mov al, [num1] |
| <input type="radio"/> | Mov ax, [num1] |
| <input type="radio"/> | Mov al, ax     |



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9:24 PM ✓

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+92 300 4419216 ~Shinshan Nohara



BC190203430: ARSLAN YOUSAF

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Question # 3 of 10 ( Start time: 09:24:23 PM, 30 December 2021 )

Stack is a data structure that behaves in a First In Last \_\_\_\_\_ manner.

Select the correct option

- Out
- Push
- Add
- In

WhatsApp chat interface for 'Cs401' (+92 349 3552763). The chat shows a photo being sent at 9:22 PM. The contact name is 'Cs401' with a VU logo. The status is '+92 349 3552763 is typing...'. The chat background is dark blue with a pattern.

Question # 10 of 10 ( start time: 09:17:06 PM, 30 December 2021 )

Total Marks: 1

Which of the following is a program control instruction?

Select the correct option

- cll
- mov ax, bx
- add bx, 0534
- jne 0534



Click to Save Answer & Move to Next Question



Question # 10 of 10 ( start time: 09:17:06 PM, 30 December 2021 )

Total Marks: 1

Which of the following is a program control instruction?

Select the correct option

- cll
- mov ax, bx
- add bx, 0534
- jne 0534



Click to Save Answer & Move to Next Question





MC210201973: SHANZA GHAFAR

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 09:01 PM, 30 December 2021

Question # 10 of 10 ( Start time: 09:17:06 PM, 30 December 2021)

Total Marks: 1

Which of the following is a program control instruction?

Select the correct option

- cli
- mov ax, bx
- add bx, 0534
- jne 0534

Click to Save Answer & Move to Next Question



Question # 10 of 10 ( start time: 09:17:06 PM, 30 December 2021 )

Total Marks: 1

Which of the following is a program control instruction?

Select the correct option

- cfi
- mov ax, bx
- add bx, 0534
- jne 0534

Click to Save Answer & Move to Next Question



Select the correct option

- 4
- 32
- 8
- 16



[Click to Save Answer & Move to Next Question](#)

Question # 9 of 10 ( Start time: 09:15:07 PM, 30 December 2021 )

Total Marks: 1

CS and IP are both \_\_\_\_\_ bit registers.

Select the correct option

<input type="radio"/>	4
<input type="radio"/>	32
<input type="radio"/>	8
<input type="radio"/>	16

Click to Save Answer & Move to Next Question



Question # 8 of 10 ( start time: 09:13:54 PM, 30 December 2021 )

Total Marks: 1

A decremting stack moves from \_\_\_\_\_ to \_\_\_\_\_ addresses as elements are added in it.

Select the correct option

 Reload Math Equations

- higher, lower
- negative, positive
- positive, negative
- lower, higher



Click to Save Answer & Move to Next Question



Question # 7 of 10 ( start time: 09:12:30 PM, 30 December 2021)

Total Marks: 1

The most convenient place to store local variables is \_\_\_\_\_.

Select the correct option

- cache memory
- stack
- registers
- main memory



Click to Save Answer & Move to Next Question



Question # 7 of 10 ( start time: 09:12:30 PM, 30 December 2021)

Total Marks: 1

The most convenient place to store local variables is \_\_\_\_\_.

Select the correct option

- cache memory
- stack
- registers
- main memory



Click to Save Answer & Move to Next Question




Question # 8 of 10 ( Start time: 09:13:54 PM, 30 December 2021 )

Total Marks: 1

A decremting stack moves from \_\_\_\_\_ to \_\_\_\_\_ addresses as elements are added in it.

Select the correct option

 Reload Math Equations

- higher, lower
- negative, positive
- positive, negative
- lower, higher

Click to Save Answer & Move to Next Question



Question # 6 of 10 ( Start time: 09:10:53 PM, 30 December 2021 )

Total Marks: 1

\_\_\_\_\_ can be used to check whether particular bits of a number are set or not.

Select the correct option

<input type="radio"/>	XOR
<input type="radio"/>	NOT
<input type="radio"/>	OR
<input type="radio"/>	AND



Click to Save Answer & Move to Next Question



Question # 6 of 10 ( Start time: 09:10:53 PM, 30 December 2021)

Total Marks: 1

\_\_\_\_\_ can be used to check whether particular bits of a number are set or not.

Select the correct option

<input type="radio"/>	XOR
<input type="radio"/>	NOT
<input type="radio"/>	OR
<input type="radio"/>	AND

Click to Save Answer & Move to Next Question



Question # 7 of 10 ( start time: 09:12:30 PM, 30 December 2021)

Total Marks: 1

The most convenient place to store local variables is \_\_\_\_\_.

Select the correct option

- cache memory
- stack
- registers
- main memory

Click to Save Answer & Move to Next Question



Question # 6 of 10 ( start time: 09:10:53 PM, 30 December 2021)

Total Marks: 1

\_\_\_\_\_ can be used to check whether particular bits of a number are set or not.

Select the correct option

<input type="radio"/>	XOR
<input type="radio"/>	NOT
<input type="radio"/>	OR
<input type="radio"/>	AND

Click to Save Answer & Move to Next Question



Question # 5 of 10 ( Start time: 09:09:04 PM, 30 December 2021 )

Total Marks: 1

Which of the following operations is used to set any specific bit in a binary number?

Select the correct option

 Reload Math Equations

- |                       |     |
|-----------------------|-----|
| <input type="radio"/> | AND |
| <input type="radio"/> | XOR |
| <input type="radio"/> | OR  |
| <input type="radio"/> | NOT |

Click to Save Answer & Move to Next Question



Question # 3 of 10 ( start time: 09:04:57 PM, 30 December 2021 )

Total Marks: 1

Which bit of attribute byte represents the blue component of foreground color?

Select the correct option

- 3
- 0
- 2
- 1

Click to Save Answer & Move to Next Question




Question # 5 of 10 ( Start time: 09:09:04 PM, 30 December 2021 )

Total Marks: 1

Which of the following operations is used to set any specific bit in a binary number?

Select the correct option

 Reload Math Equations

- |                       |     |
|-----------------------|-----|
| <input type="radio"/> | AND |
| <input type="radio"/> | XOR |
| <input type="radio"/> | OR  |
| <input type="radio"/> | NOT |

Click to Save Answer & Move to Next Question



Question # 4 of 10 ( Start time: 09:07:36 PM, 30 December 2021 )

Total Marks: 1

The segment:offset pair is called a/an \_\_\_\_\_ address.

Select the correct option

- physical
- logical
- effective
- virtual

[Click to Save Answer & Move to Next Question](#)




Question # 1 of 10 ( Start time: 09:01:45 PM, 30 December 2021 )

Total Marks: 1

The \_\_\_\_\_ flag has a special role in debugging.

Select the correct option

 Reload Math Equations

- sign
- trap
- zero
- parity

Click to Save Answer & Move to Next Question



BC190407419: UMBAR MANZOOR

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question #10 of 10 (Start time: 08:57:57 PM, 30 December 2021)

Total Marks: 1

The extra bit produced as a result of an arithmetic operation that does not fit in the target register is stored in:

Select the correct option

Reveal Math Equations

- Carry Flag
- Zero Flag
- Interrupt Flag
- Overflow Flag

Click to Save Answer & Move to Next Question

BC190407419: UMBAR MANZOOR Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1) Quiz Start Time: 08:41 PM, 30 December 2021

Question #10 of 10 (Start time: 08:57:57 PM, 30 December 2021) Total Marks: 1

The extra bit produced as a result of an arithmetic operation that does not fit in the target register is stored in:

Select the correct option

Reveal Math Equations

- Carry Flag
- Zero Flag
- Interrupt Flag
- Overflow Flag

Click to Save Answer & Move to Next Question

BC190407419: UMBAR MANZOOR

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question # 9 of 10 (Start time: 08:56:04 PM, 30 December 2021)

Total Marks: 1

Cell width refers to the total number of bits in a memory cell while the total number of cells is called the\_\_\_\_\_.

Select the correct option

- Cell Length
- Depth
- Cell Height
- Cell width

Click to Save Answer & Move to Next Question

BC190407419: UMBAR MANZOOR

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question #10 of 10 ( Start time: 08:57:57 PM, 30 December 2021 )

Total Marks: 1

The extra bit produced as a result of an arithmetic operation that does not fit in the target register is stored in:

Select the correct option

Reload Math Equations

- Carry Flag
- Zero Flag
- Interrupt Flag
- Overflow Flag

Click to Save Answer & Move to Next Question



# Quiz

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BC190407419: UMBAR MANZOOR

**Time Left** 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question # 8 of 10 (Start time: 08:54:59 PM, 30 December 2021)

Total Marks: 1


Intel 8085 can access up to \_\_\_\_\_ of memory, whereas Intel 8088 can access up to \_\_\_\_\_ of memory.

Select the correct option

- 16 bits, 32 bits
- 64 KB, 1 MB
- 1 MB, 64 KB
- 64 KB, 1 GB

Click to Save Answer & Move to Next Question



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1 new message

BC

CS

Programming (Semester 1)

2021

Question # 6 of 10 ( Start time: 08:52:36 PM, 30 December 2021 )

Total Marks: 1

Registers are normally part of \_\_\_\_\_.

Select the correct option

 Reload Math Equations

- Hard disk
- RAM
- CPU
- CPU and Cache

Click to Save Answer & Move to Next Question





# Quiz

<https://vulms.vu.edu.pk>



BC190407419: UMBAR MANZOOR

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question # 9 of 10 (Start time: 08:56:04 PM, 30 December 2021)

Total Marks: 1

Cell width refers to the total number of bits in a memory cell while the total number of cells is called the\_\_\_\_\_.

Select the correct option

- Cell Length
- Depth
- Cell Height
- Cell width

Click to Save Answer & Move to Next Question



BC190407419: UMBAR MANZOOR

Time Left 71 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question # 3 of 10 (Start time: 08:45:05 PM, 30 December 2021)

Total Marks: 1

The base pointer accesses local variables using \_\_\_\_\_ offsets.

Select the correct option

- incremental
- positive
- fixed
- negative

Click to Save Answer & Move to Next Question



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1 new message

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CS

Programming (Semester 1)

2021

Question # 6 of 10 ( Start time: 08:52:36 PM, 30 December 2021 )

Total Marks: 1

Registers are normally part of \_\_\_\_\_

Select the correct option

Reload Math Equations

- Hard disk
- RAM
- CPU
- CPU and Cache

Click to Save Answer & Move to Next Question



BC190407419: UMBAR MANZOOR

Time Left 89 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question # 3 of 10 ( Start time: 08:45:05 PM, 30 December 2021 )

Total Marks: 1

The base pointer accesses local variables using \_\_\_\_\_ offsets.

Select the correct option

- fixed
- negative
- positive
- incremental

Click to Save Answer & Move to Next Question



# Quiz

<https://vulms.vu.edu.pk>



BC190407419: UMBAR MANZOOR

**Time Left** 81 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question # 4 of 10 (Start time: 08:48:07 PM, 30 December 2021)

Total Marks: 1

How logical errors are different from syntax errors?

Select the correct option

Reload Math Equations

- Identifying syntax and logical errors is the responsibility of assembler and programmer respectively
- Syntax errors are more important than the logical errors
- Identifying syntax errors is the responsibility of programmer unlike logical errors
- Identifying both types of errors is the responsibility of assembler

Click to Save Answer & Move to Next Question





# Quiz

<https://vulms.vu.edu.pk>



BC190407419: UMBAR MANZOOR

**Time Left** 81 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question # 4 of 10 (Start time: 08:48:07 PM, 30 December 2021)

Total Marks: 1

How logical errors are different from syntax errors?

Select the correct option

Reload Math Equations

- Identifying syntax and logical errors is the responsibility of assembler and programmer respectively
- Syntax errors are more important than the logical errors
- Identifying syntax errors is the responsibility of programmer unlike logical errors
- Identifying both types of errors is the responsibility of assembler

Click to Save Answer & Move to Next Question



BC190407419: UMBAR MANZOOR

Time Left 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question #1 of 10 ( Start time: 08:41:56 PM, 30 December 2021 )

Total Marks: 1

ASCII table is the contiguous arrangement of the uppercase alphabets (41-5A), the lowercase alphabets (61-7A), and the numbers -----

Select the correct option

- 30-39
- 31-40
- 29-39
- 31-41

Click to Save Answer & Move to Next Question

BC190407419: UMBAR MANZOOR

Time Left 90 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question #1 of 10 ( Start time: 08:41:56 PM, 30 December 2021 )

Total Marks: 1

ASCII table is the contiguous arrangement of the uppercase alphabets (41-5A), the lowercase alphabets (61-7A), and the numbers -----

Select the correct option

- 30-39
- 31-40
- 29-39
- 31-41

Click to Save Answer & Move to Next Question



## Quiz

<https://vulms.vu.edu.pk>

BC190407419: UMBAR MANZOOR

Time Left 89  
sec(s)CS401 - Computer Architecture and Assembly Language  
Programming (Quiz No.1)

Quiz Start Time: 08:41 PM, 30 December 2021

Question # 2 of 10 ( Start time: 08:42:59 PM, 30 December 2021 )

Total Marks: 1

If AX=00FF, then which of the following instruction can be used to change the value of AX to FF00?

Select the correct option

- |                       |              |
|-----------------------|--------------|
| <input type="radio"/> | AND AX, FF00 |
| <input type="radio"/> | OR AX, AX    |
| <input type="radio"/> | XOR AX, AX   |
| <input type="radio"/> | NOT AX       |

Click to Save Answer &amp; Move to Next Question



Question # 6 of 10 ( Start time: 03:41:46 PM, 30 December 2021 )

Which of the following string instructions is generally used in a loop instead of REP prefix?

Select the correct option

 Reload Math E

- |                                  |      |
|----------------------------------|------|
| <input checked="" type="radio"/> | LODS |
| <input type="radio"/>            | CMPS |
| <input type="radio"/>            | SCAS |
| <input type="radio"/>            | STOS |


Click to Save Answer & Move to Next Q

Question # 5 of 10 ( start time: 03:40:19 PM, 30 December 2021 )

Total Marks: 10

The correct instruction to subtract with borrow is \_\_\_\_\_.

Select the correct option

 Reload Math Equation

- |                                  |      |
|----------------------------------|------|
| <input checked="" type="radio"/> | SBB  |
| <input type="radio"/>            | SJBB |
| <input type="radio"/>            | SB   |
| <input type="radio"/>            | SWB  |

Click to Save Answer & Move to Next Question

Question # 3 of 10 ( Start time: 03:35:48 PM, 30 December 2021 )

mov [1234], ax is an example of \_\_\_\_\_ addressing.

Select the correct option

- direct
- register indirect
- base + index
- base register indirect

Click to Save Answer & Move to Next

Not  
confuse

Question # 4 of 10 ( Start time: 03:37:46 PM, 30 December 2021 )

Total

What the following piece of code does?

Shl word [multiplicand], 1

Rcl word [multiplicand+2], 1

Select the correct option

[Reload Math Equ](#)

Extended Shift left

Multiply multiplicand and multiplicand+2

Extended Shift right

Rotate multiplicand and multiplicand+2

[Click to Save Answer & Move to Next Que](#)

Not a  
confusion

Question # 7 of 10 ( Start time: 03:43:14 PM, 30 December 2021 )

T

With the execution of CALL instruction, the value of \_\_\_\_\_ is decremented by 2.

Select the correct option

- |                                  |    |
|----------------------------------|----|
| <input checked="" type="radio"/> | SP |
| <input type="radio"/>            | DS |
| <input type="radio"/>            | BX |
| <input type="radio"/>            | CX |

Click to Save Answer & Move to Next Q

**Question # 8 of 10 ( Start time: 03:44:44 PM, 30 December 2021 )**

IAPX88 stands for \_\_\_\_\_ .

**Select the correct option**

- Intel Active Processor Extensions 88
- Intel Advanced Processor Extensions 88
- Intel Advanced Process Extensions 88
- Intel Advanced Processor External 88

[Click to Save Answer & M](#)

Question # 8 of 10 ( Start time: 03:44:44 PM, 30 December 2021 )

IAPX88 stands for \_\_\_\_\_ .

Select the correct option

- |                                  |  |
|----------------------------------|--|
| <input type="radio"/>            | Intel Active Processor Extensions 88   |
| <input checked="" type="radio"/> | Intel Advanced Processor Extensions 88 |
| <input type="radio"/>            | Intel Advanced Process Extensions 88   |
| <input type="radio"/>            | Intel Advanced Processor External 88   |

Click to Save Answer & M

Question # 1 of 10 ( start time: 03:32:52 PM, 30 December 2021 )

Tot

Which of the following bus is used to inform the memory that whether processor wants to read data or write data?

Select the correct option

<input type="radio"/>	Function Bus
<input checked="" type="radio"/>	Control Bus
<input type="radio"/>	Address Bus
<input type="radio"/>	Data Bus

Question # 9 of 10 ( start time: 03:45:50 PM, 30 December 2021 )

Total Marks: 1

In far jump, \_\_\_\_\_

Select the correct option

no need to mention any segment or offset

both offset and segment are given

only segment address is given

only offset is given

Click to Save Answer & Move to Next Question

Question # 10 of 10 ( Start time: 03:47:00 PM, 30 December 2021 )

A value 0500 is stored in memory. If we transfer this value to a general-purpose register, then it will be shown as:

Select the correct option

0005

5000

0500

0050



Click to Save Answer & Move to Next Q

Question # 2 of 10 ( Start time: 03:34:27 PM, 30 December 2021 )

Total

In which of the following addressing, both registers are not constant?

Select the correct option

<input checked="" type="radio"/>	Base + offset + Index
<input type="radio"/>	Index + offset
<input type="radio"/>	Base + Index
<input type="radio"/>	Base + offset

Click to Save Answer & Move to Next Question

Question # 10 of 10 ( Start time: 03:47:00 PM, 30 December 2021 )

A value 0500 is stored in memory. If we transfer this value to a general-purpose register, then it will be shown as:

Select the correct option

0005

5000

0500

0050

Click to Save Answer & Move to Next Question

Question # 9 of 10 ( start time: 03:45:50 PM, 30 December 2021 )

In far jump, -----

Select the correct option

no need to mention any segment or offset

both offset and segment are given

only segment address is given

only offset is given

Click to Save Answer & Move to Next



Question # 8 of 10 ( Start time: 03:44:44 PM, 30 December 2021 )

IAPX88 stands for \_\_\_\_\_ .

Select the correct option



Intel Active Processor Extensions 88



Intel Advanced Processor Extensions 88



Intel Advanced Process Extensions 88



Intel Advanced Processor External 88

Click to Save Answer & M

Question # 6 of 10 ( Start time: 03:41:46 PM, 30 December 2021 )

Which of the following string instructions is generally used in a loop instead of REP prefix?

Select the correct option

 Reload Math

- |                       |      |
|-----------------------|------|
| <input type="radio"/> | LODS |
| <input type="radio"/> | CMPS |
| <input type="radio"/> | SCAS |
| <input type="radio"/> | STOS |

Question # 5 of 10 ( Start time: 03:40:19 PM, 30 December 2021 )

Total Marks: 10

The correct instruction to subtract with borrow is \_\_\_\_\_.

Select the correct option

 Reload Math Equation

- |                       |      |
|-----------------------|------|
| <input type="radio"/> | SBB  |
| <input type="radio"/> | SUBB |
| <input type="radio"/> | SB   |
| <input type="radio"/> | SWB  |

Click to Save Answer & Move to Next Question

## Question # 7 of 10 ( start time: 03:43:14 PM, 30 December 2021 )

With the execution of CALL instruction, the value of \_\_\_\_\_ is decremented by 2.

## Select the correct option

 SP DS BX CX[Click to Save Answer & Move to Next Q](#)

Question # 4 of 10 ( start time: 03:37:46 PM, 30 December 2021 )

Total M

What the following piece of code does?

```
Shl word [multiplicand], 1
```

```
Rcl word [multiplicand+2], 1
```

select the correct option

 Reload Math Equations

Extended Shift left

Multiply multiplicand and multiplicand+2

Extended Shift right

Rotate multiplicand and multiplicand+2

Click to Save Answer & Move to Next Question

Question # 2 of 10 ( start time: 03:34:27 PM, 30 December 2021 )

Total

In which of the following addressing, both registers are not constant?

Select the correct option

Base + offset + Index

Index + offset

Base + Index

Base + offset

Click to Save Answer & Move to Next Question



Question # 3 of 10 ( Start time: 03:35:48 PM, 30 December 2021 )

mov [1234], ax is an example of \_\_\_\_\_ addressing.

Select the correct option

direct

register indirect

base + index

base register indirect

Click to Save Answer & Move to N

Question # 1 of 10 ( Start time: 03:32:52 PM, 30 December 2021 )

Tot

Which of the following bus is used to inform the memory that whether processor wants to read data or write data?

Select the correct option

- |                       |              |
|-----------------------|--------------|
| <input type="radio"/> | Function Bus |
| <input type="radio"/> | Control Bus  |
| <input type="radio"/> | Address Bus  |
| <input type="radio"/> | Data Bus     |

Question # 10 of 10 ( Start time: 10:32:15 AM, 30 December 2021 )

Total Marks: 1

The number of \_\_\_\_\_ in a cell is called the cell width.

Select the correct option

Bits

Nibbles

Words

Bytes

Click to Save Answer & Move to Next Question

**Question # 1 of 10 ( Start time: 10:39:42 AM, 30 December 2021 )** **Total Marks: 1**

In extended multiplication, we store the multiplicand in \_\_\_\_\_ bits, and the result is stored in \_\_\_\_\_ bits.

**Select the correct option** 🔄 Reload Math Equations

- 32, 64
- 16, 32
- 64, 64
- 32, 32

Click to Save Answer & Move to Next Question

Synchronization between the processor and the memory for read and write operations is done by the \_\_\_\_\_.

Select the correct option

 Reload Math Equations

- Data bus
- Address bus
- Control bus
- Cache bus

Click to Save Answer & Move to Next Question

Question # 9 of 10 ( Start time: 10:30:26 AM, 30 December 2021 )

Total Marks: 1

iAPX88 Architecture consists of

Select the correct option

- 16 Registers
- 14 Registers
- 10 Registers
- 32 Registers

Click to Save Answer & Move to Next Question

Question # 6 of 10 ( Start time: 10:25:36 AM, 30 December 2021 )

Total Marks: 1

Intel 8085 can access up to \_\_\_\_\_ of memory, whereas Intel 8088 can access up to \_\_\_\_\_ of memory.

Select the correct option

64 KB, 1 GB

1 MB, 64 KB

64 KB, 1 MB

16 bits, 32 bits



Click to Save Answer & Move to Next Question

Question # 5 of 10 ( Start time: 10:23:19 AM, 30 December 2021 )

Total Marks: 1

Shifting the integer 5 left by 1 bit results in \_\_\_\_\_.

Select the correct option

25

5

50

10

Click to Save Answer & Move to Next Question

Question # 4 of 10 ( Start time: 10:21:38 AM, 30 December 2021 )

Total Marks: 1

We can convert a digit into its ASCII representation by adding \_\_\_\_\_ to it.

Select the correct option

- |                                  |      |
|----------------------------------|------|
| <input checked="" type="radio"/> | 0x30 |
| <input type="radio"/>            | 0x10 |
| <input type="radio"/>            | 0x20 |
| <input type="radio"/>            | 0x05 |

Click to Save Answer & Move to Next Question

Question #1 of 10 ( Start time: 10:16:42 AM, 30 December 2021 )

Total Marks: 1

Shift logical Left (SHL) moves all the bits one position to the \_\_\_\_\_ and inserts a zero from the \_\_\_\_\_.

Select the correct option

 Reload Math Equations

right, right

right, left

left, left

left, right

Click to Save Answer & Move to Next Question

Question # 2 of 10 ( Start time: 10:18:10 AM, 30 December 2021 )

Total Marks: 1

In MUL instruction, if the source operand is a byte, then it is multiplied with \_\_\_\_\_ register.

Select the correct option

AH

BL


AL

BH

Click to Save Answer & Move to Next Question

When the operand of DIV instruction is in 16 bits, then the implied dividend will be in \_\_\_\_\_.

Select the correct option

 Reload Math Equations

DX:AX



ES:AX

AX

DS:BX

Click to Save Answer & Move to Next Question

Question # 7 of 10 ( Start time: 10:27:32 AM, 30 December 2021 )

Total Marks: 1

What does the PUSH operation do?

Select the correct option

 Reload Math Equations

It copies the operand on the stack



It increments the stack pointer

It increments the stack segment

It deletes the operand from the stack

Click to Save Answer & Move to Next Question

BC210427836: UMER ILYAS

Time Left 54 sec(s)

CS401 - Computer Architecture and Assembly Language Programming (Quiz No. 1)

Quiz Start Time: 10:39 AM, 30 December 2021

Question # 2 of 10 ( Start time: 10:41:08 AM, 30 December 2021 )

Total Marks: 1

----- transfers the word at the top of the stack (pointed to by SP) to the destination operand, and increments SP by two.

Select the correct option

Reload Math Equations

- RET n
- POP
- PUSH
- CALL

**Programming (Quiz No. 1)**

**Question # 4 of 10 ( Start time: 10:44:03 AM, 30 December 2021 )** **Total Marks: 1**

Suppose,  $BX=0x0120$ ,  $CS=0x1000$ , and the memory under consideration is  $[CS:BX+0x0880]$ .

Which of the following will be the physical address?

[Reload Math Equations](#)

Select the correct option

- 109A0
- 11A00
- 11100
- 10834

[Click to Save Answer & Move to Next Question](#)

Suppose AX=5, BX=5, DX=0, CF=1, ZF=1 and AF=1. What will be the final value in AX register after the execution of ADC AX, BX?


Select the correct option

- 10
- 11
- 5
- 6

Click to Save Answer & Move to Next Question

----- transfers the word at the top of the stack (pointed to by SP) to the destination operand, and increments SP by two.

Select the correct option

 Reload Math Equations


- |                       |       |
|-----------------------|-------|
| <input type="radio"/> | RET n |
| <input type="radio"/> | POP   |
| <input type="radio"/> | PUSH  |
| <input type="radio"/> | CALL  |

**Question # 1 of 10 ( Start time: 10:39:42 AM, 30 December 2021 )**

**Total Marks: 1**

In extended multiplication, we store the multiplicand in \_\_\_\_\_ bits, and the result is stored in \_\_\_\_\_ bits.

**Select the correct option**



- 32, 64
- 16, 32
- 64, 64
- 32, 32

[Click to Save Answer & Move to Next Question](#)

Question # 8 of 10 ( start time: 10:28:48 AM, 30 December 2021 )

Total Marks: 1

Synchronization between the processor and the memory for read and write operations is done by the \_\_\_\_\_.

Select the correct option

 Reload Math Equations

<input type="radio"/>	Data bus
<input type="radio"/>	Address bus
<input type="radio"/>	Control bus
<input type="radio"/>	Cache bus

Question # 6 of 10 ( Start time: 10:25:36 AM, 30 December 2021 )

Total Marks: 1

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Select the correct option

64 KB, 1 GB

1 MB, 64 KB

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16 bits, 32 bits

Click to Save Answer & Move to Next Question

What does the PUSH operation do?

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 Reload Math Equations

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It increments the stack segment

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When the operand of DIV instruction is in 16 bits, then the implied dividend will be in \_\_\_\_\_.

Select the correct option

 Reload Math Equations

DX:AX

ES:AX

AX

DS:BX

Click to Save Answer & Move to Next Question

Question # 4 of 10 ( Start time: 10:21:38 AM, 30 December 2021 )

Total Marks: 1

We can convert a digit into its ASCII representation by adding \_\_\_\_\_ to it.

Select the correct option

0x30

0x10

0x20

0x05

Click to Save Answer & Move to Next Question

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Total Marks: 1

Shifting the integer 5 left by 1 bit results in \_\_\_\_\_.

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50

10

Question #1 of 10 ( Start time: 10:16:42 AM, 30 December 2021 )

Total Marks: 1

Shift logical Left (SHL) moves all the bits one position to the \_\_\_\_\_ and inserts a zero from the \_\_\_\_\_

Select the correct option

 Reload Math Equations

right, right

right, left

left, left

left, right

Click to Save Answer & Move to Next Question