

Arsl sHaNi

Question # 9 of 10 (Start time: 10:00:49 AM, 25 August 2020)

Which of the following IRQs is a cascading interrupt?

Select the correct option

IRQ 0

IRQ 1

IRQ 2

IRQ 3



Which of the following IRQs is derived by a timer device?

Select the correct option

- IRQ 0
- IRQ 1
- IRQ 2
- IRQ3

Click on the Answer & Move to next question





The parallel port connector is called ?

Select the correct option

- DB-25 
- BD-25
- DB-24
- BD-24





An End of Interrupt (EOI) signal is sent by

Select the correct option

- Handler
- Processor
- IRQ
- PIC

Click on the Answer & Move to next question



BC190206835: ABDULLAH NASEER

Time Left 89 sec(s)

CS401:Quiz No. 1

Quiz Start Time: 09:40 AM, 25 August 2020

Question # 4 of 10 (Start time: 09:43:54 AM, 25 August 2020)

Total Marks: 1

Which of the following IRQs is derived by a key board?

Select the correct option

- IRQ 0
- IRQ 1
- IRQ 2
- IRQ 3



Which of the following is the ACK pin in DB-25 connector?

Select the correct option

- 10
- 11
- 12
- 13

Click on the Answer & Move to next question



Question # 6 of 10 (Start time: 09:45:18 PM, 24 August 2020)

Total Marks: 1

Each thread can have their own

Select the correct option



execution area



stack



memory



array

Which of the following IRQs is used for Floppy disk drive?

Select the correct option



IRQ 4



IRQ 5



IRQ 6



IRQ 7

Which of the following IRQs is used for sound card or network card?

Select the correct option

IRQ 4

IRQ 5

IRQ 6

IRQ 7

Question # 2 of 10 (Start time: 09:37:40 PM, 24 August 2020)

Total Marks: 1

In multitasking, each PCB requires _____ bytes to store the values of registers.

Select the correct option



14



28



32



16

Question # 1 of 10 (Start time: 09:36:32 PM, 24 August 2020)

Total Marks: 1

Programmable interrupt controller has _____ ports.

Select the correct option



1



2



3



4

Question # 9 of 10 (Start time: 09:50:16 PM, 24 August 2020)

Total Marks: 1

IRET returns on the basis of _____ and _____.

Select the correct option

CS, IP



DS, IP

CS, SS

IP, SP

Which of the following bits of parallel control port enables the IRQ 7 triggering by the ACK pin?

Select the correct option



Bit 1



Bit 2



Bit 3



Bit 4



The input frequency of the programmable interval timer (PIT) is

Select the correct option



Fixed



Depends on processor clock



Variable



Depends on hardware attached

Which of the following flags can be used for mathematical operations ?

Select the correct option



Direction Flag



Carry Flag



Trap Flag



Interrupt Flag

Question # 2 of 10 (Start time: 10:18:47 PM, 24 August 2020)

Total Marks: 1

For the external hardware to generate an interrupt there are how many pins out side the processor?

Select the correct option



1



2



3



4

Which of the following IRQs is derived by a key board?

Select the correct option

IRQ 0

IRQ 1

IRQ 2

IRQ 3

DOS has a single entry point to access all its services through interrupt _____.

Select the correct option



INT 21



INT 16



INT 2



INT 10

Which of the following interrupts is used for maintaining the system time?

Select the correct option



INT 0



INT 1



INT 8



INT 10

Which of the following pins of a parallel port connector are grounded ?

Select the correct option



10-18



18-25



25-32



32-39

Question # 1 of 10 (Start time: 09:03:23 PM, 24 August 2020)

Total Marks: 1

The offset address of an interrupt n will be at

Select the correct option

- | | | |
|----------------------------------|-----|---|
| <input type="radio"/> | n | / |
| <input type="radio"/> | nx2 | / |
| <input type="radio"/> | nx3 | / |
| <input checked="" type="radio"/> | nx4 | / |

[Click to Save Answer & Move to Next Question](#)

Programmable interrupt controller has _____ ports.

Select the correct option:

1

2

3

4

Question # 4 of 10 (Start time: 09:06:44 PM, 24 August 2020)

Total Marks: 1

Which of the following is the ACK pin in DB-25 connector?

Select the correct option

- 10
- 11
- 12
- 13

Click to Save Answer & Move to Next Question

Question # 2 of 10 (Start time: 09:04:52 PM, 24 August 2020)

Total Marks: 1

In multitasking environment, which of the following structure is used to maintain the ordering of active PCBs ?

Select the correct option

- Array
- Register
- Linked List
- Stack

Click to Save Answer & Move to Next Question

Question # 8 of 10 (Start time: 10:26:44 PM, 24 August 2020)

Total Marks: 1

All registers of a task are stored in a space called

Select the correct option

 Process Storage Block Process Control Block  Process Terminal Swapping Space

Question # 7 of 10 (Start time: 09:10:26 PM, 24 August 2020)

Total Marks: 1

The programmable interval timer (PIT) has input frequency of

Select the correct option

- | | | |
|-----------------------|----------|---|
| <input type="radio"/> | 1.193MHZ | ✓ |
| <input type="radio"/> | 2.193MHZ | ✗ |
| <input type="radio"/> | 3.193MHZ | ✗ |
| <input type="radio"/> | 4.193MHZ | ✗ |

[Click to Save Answer & Move to Next Question](#)

Question # 3 of 10 (Start time: 09:05:55 PM, 24 August 2020)

Total Marks: 1

Which of the following is the destination register in IN instruction?

Select the correct option

- | | | |
|-----------------------|----------|----|
| <input type="radio"/> | AL or AX | // |
| <input type="radio"/> | BL or BX | // |
| <input type="radio"/> | CL or CX | // |
| <input type="radio"/> | DL or DX | // |

Click to Save Answer & Move to Next Question

Question # 9 of 10 (Start time: 09:13:04 PM, 24 August 2020)

Total Marks: 1

In programmable interrupt controller which of the following ports is referred as a control port?

Select the correct option

- | | | |
|----------------------------------|----|---|
| <input type="radio"/> | 19 | / |
| <input checked="" type="radio"/> | 20 | / |
| <input type="radio"/> | 21 | / |
| <input type="radio"/> | 22 | / |

[Click to Save Answer & Move to Next Question](#)

Question # 5 of 10 (Start time: 09:07:58 PM, 24 August 2020)

Total Marks: 1

In PIC, which of the following ports is used for selectively enabling or disabling interrupts?

Select the correct option

- 19
- 20
- 21
- 22

Click to Save Answer & Move to Next Question

BC190206835: ABDULLAH NASEER

Time Left 89 sec(s)

CS401:Quiz No. 1

Quiz Start Time: 09:40 AM, 25 August 2020

Question # 7 of 10 (Start time: 09:48:39 AM, 25 August 2020)

Total Marks: 1

Programmable interrupt controller has _____ ports.

Select the correct option

- 1
- 2
- 3
- 4

Question # 8 of 10 (Start time: 09:11:34 PM, 24 August 2020)

Total Marks: 1

Which of the following interrupts is used for saving and restoring the registers

Select the correct option

- | | | |
|----------------------------------|------|----|
| <input type="radio"/> | INT6 | // |
| <input type="radio"/> | INT7 | // |
| <input checked="" type="radio"/> | INT8 | // |
| <input type="radio"/> | INT0 | // |

Click to Save Answer & Move to Next Question

Question # 10 of 10 (Start time: 09:13:57 PM, 24 August 2020)

Total Marks: 1

The time interval between two timer ticks is ?

Select the correct option

- 40ms
- 45ms
- 50ms
- 55ms

Click to Save Answer & Move to Next Question

BC190206835: ABDULLAH NASEER

Time Left 89 sec(s)

CS401:Quiz No. 1

Quiz Start Time: 09:40 AM, 25 August 2020

Question # 8 of 10 (Start time: 09:49:12 AM, 25 August 2020)

Total Marks: 1

Which of the following is the destination register in IN instruction?

Select the correct option

- AL or AX
- BL or BX
- CL or CX
- DL or DX

Question # 1 of 10 (Start time: 08:22:47 PM, 24 August 2020)

In multitasking environment, which of the following structure is used to maintain the ordering of active PCBs ?

Select the correct option

- Array
- Register
- Linked List
- Stack

MC180405145: ZAINAB BIBI

CS401: Quiz No. 1

Question # 9 of 10 (Start time: 08:58:20 PM, 24 August 2020)

When two devices in the system want to use the same IRQ line, is referred as:

Select the correct option

- IRQ Collision
- IRQ Conflict
- IRQ Crash
- IRQ Blockage

BC180401499: MUHAMMAD ZEESHAN

CS401: Quiz No. 1

Question # 3 of 10 (Start time: 08:12:55 PM, 24 August 2020)

In older computers, the port numbers designed for parallel port LPT2 are _____.

Select the correct option

- 078-07A
- 178-17A
- 278-27A
- 378-37A

MC180405145: ZAINAB BIBI

CS401: Quiz No. 1

Question # 10 of 10 (Start time: 08:58:50 PM, 24 August 2020)

The input frequency of the programmable interval timer (PIT) is

Select the correct option

- Fixed
- Depends on processor clock
- Variable
- Depends on hardware attached



In 8088 processor, there can be total _____ possible entries in an Interrupt Vector Table.

Select the correct option

- 256
- 64
- 128
- 512

MC180405145: ZAINAB BIBI

CS401:Quiz No. 1

Question # 1 of 10 (Start time: 08:51:31 PM, 24 August 2020)

For the external hardware to generate an interrupt there are how many pins out side the processor?

Select the correct option

- 1
- 2
- 3
- 4

Question # 6 of 10 (Start time: 08:55:05 PM, 24 August 2020)

Which of the following is the most commonly used port with a printer?

Select the correct option

- Serial port
- USB port
- Parallel port
- DVI port



MC190204455: YUSRA FAYYAZ

Time Left 87 sec(s)

CS401:Quiz No. 1

Quiz Start Time: 08:38 PM, 24 August 2020

Question # 6 of 10 (Start time: 08:45:20 PM, 24 August 2020)

Total Marks: 1

Which of the following IRQs is connected to serial port COM 2?

Select the correct option

- IRQ 0
- IRQ 1
- IRQ2
- IRQ3

Click to Save Answer & Move to Next Question

Question # 3 of 10 (Start time: 08:53:00 PM, 24 August 2020)

Which of the following interrupts is used in debugging with the trap flag?

Select the correct option

- | | |
|-----------------------|-------|
| <input type="radio"/> | INT 0 |
| <input type="radio"/> | INT 1 |
| <input type="radio"/> | INT 2 |
| <input type="radio"/> | INT 3 |



Type here to search





MC190204455: YUSRA FAYYAZ

Time Left 90 sec(s)

CS401:Quiz No. 1

Quiz Start Time: 08:38 PM, 24 August 2020

Question # 8 of 10 (Start time: 08:47:26 PM, 24 August 2020)

Total Marks: 1

All registers of a task are stored in a space called

_____.

Select the correct option

- Process Storage Block
- Process Control Block
- Process Terminal
- Swapping Space

Click to Save Answer & Move to Next Question



MC190204455: YUSRA FAYYAZ

Time Left 87 sec(s)

CS401:Quiz No. 1

Quiz Start Time: 08:38 PM, 24 August 2020

Question # 7 of 10 (Start time: 08:46:38 PM, 24 August 2020)

Total Marks: 1

Which of the following IRQs is used by the parallel port?

Select the correct option

- IRQ 4
- IRQ 5
- IRQ 6
- IRQ 7

Click to Save Answer & Move to Next Question



MC190204455: YUSRA FAYYAZ

Time Left 90 sec(s)

CS401:Quiz No. 1

Quiz Start Time: 08:38 PM, 24 August 2020

Question # 1 of 10 (Start time: 08:38:15 PM, 24 August 2020)

Total Marks: 1

Which of the following IRQs is derived by a key board?

Select the correct option

- IRQ 0
- IRQ 1
- IRQ 2
- IRQ 3

Click to Save Answer & Move to Next Question

Question # 2 of 10 (**Start time: 08:24:13 PM, 24 August 2020**)

Which of the following interrupt is of highest priority interrupt?

Select the correct option

- Key board interrupt
- Timer interrupt
- INT 2
- INT 3



Type here to search



IRET returns on the basis of _____ and _____.

Select the correct option

- CS, IP
- DS, IP
- CS, SS
- IP, SP



Type here to search





For the external hardware to generate an interrupt there are how many pins out side the processor?

Select the correct option

- 1
- 2
- 3
- 4



MC190204455: YUSRA FAYYAZ

Time Left 90 sec(s)

CS401:Quiz No. 1

Quiz Start Time: 08:38 PM, 24 August 2020

Question # 2 of 10 (Start time: 08:39:40 PM, 24 August 2020)

Total Marks: 1

The programmable interval timer (PIT) has input frequency of

Select the correct option

- 1.193MHZ
- 2.193MHZ
- 3.193MHZ
- 4.193MHZ

Click to Save Answer & Move to Next Question



MC190204455: YUSRA FAYYAZ

Time Left 89 sec(s)

CS401:Quiz No. 1

Quiz Start Time: 08:38 PM, 24 August 2020

Question # 4 of 10 (Start time: 08:41:55 PM, 24 August 2020)

Total Marks: 1

In programmable interrupt controller which of the following ports is referred as a interrupt mask register?

Select the correct option

- 19
- 20
- 21
- 22

Click to Save Answer & Move to Next Question

Question # 8 of 10 (Start time: 08:32:23 PM, 24 August 2020)

The interrupt mask register which can be used for selectively enabling or disabling interrupts is associated with

Select the correct option

- | | |
|-----------------------|---------|
| <input type="radio"/> | Port 19 |
| <input type="radio"/> | Port 20 |
| <input type="radio"/> | Port 21 |
| <input type="radio"/> | Port 22 |

Question # 10 of 10 (Start time: 08:34:30 PM, 24 August 2020)

IBM AT has how many PICs (Programmable interrupt controller)

Select the correct option

- | | |
|-----------------------|---|
| <input type="radio"/> | 1 |
| <input type="radio"/> | 2 |
| <input type="radio"/> | 3 |
| <input type="radio"/> | 4 |

Type here to search



Question # 2 of 10 (Start time: 08:24:13 PM, 24 August 2020)

Which of the following interrupt is of highest priority interrupt?

Select the correct option

- | | |
|-----------------------|---------------------|
| <input type="radio"/> | Key board interrupt |
| <input type="radio"/> | Timer interrupt |
| <input type="radio"/> | INT 2 |
| <input type="radio"/> | INT 3 |



Type here to search



BC180401499: MUHAMMAD ZEESHAN

CS401: Quiz No. 1

Question # 6 of 10 (Start time: 08:16:24 PM. 24 August 2020)

The first parallel port LPT1 has port number designated from _____.

Select the correct option

- 178 to 17A
- 278 to 27A
- 378 to 37A
- 478 to 47A

CS401: Quiz No. 1

Question # 10 of 10 (**Start time: 08:21:23 PM, 24 August 2020**)

When two devices in the system want to use the same IRQ line, is referred as:

Select the correct option

- IRQ Collision
- IRQ Conflict
- IRQ Crash
- IRQ Blockage



Type here to search



BC180401499: MUHAMMAD ZEESHAN

CS401: Quiz No. 1

Question # 7 of 10 (Start time: 08:17:38 PM, 24 August 2020)

Which of the following is used for exporting keyboard services?

Select the correct option

- INT 16
- INT 11
- INT 12
- INT 8

BC180401499: MUHAMMAD ZEESHAN

CS401: Quiz No. 1

Question # 9 of 10 (**Start time: 08:20:13 PM, 24 August 2020**)

TSR stands for:

Select the correct option

- Terminate and Store Routines
- Transmits and Save Resources
- Terminate and Stay Resident
- Truncate and Start Recursively



Type here to search



Question # 3 of 10 (Start time: 08:25:39 PM, 24 August 2020)

All registers of a task are stored in a space called _____

Select the correct option

- Process Storage Block
- Process Control Block
- Process Terminal
- Swapping Space

Type here to search

Question # 4 of 10 (Start time: 08:27:03 PM, 24 August 2020)

Programmable interrupt controller has two ports (20 and 21). Port 20 is the control port while port 21 is _____

Select the correct option

- | | |
|-----------------------|-----------------------------|
| <input type="radio"/> | the interrupt mask register |
| <input type="radio"/> | interrupt port |
| <input type="radio"/> | output port |
| <input type="radio"/> | input port |