

CS401 Computer Architecture and Assembly Language Programming

BC080400877 (MUHAMMAD ABRAR RAFIQ)

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

The operand of PUSH is called

Answer (Please select your correct option)

Source operand

Destination operand

Stack operand

Data operand

Start Time: 9:58 AM

120:00

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Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

The operand of PUSH is called

Answer (Please select your correct option)

Source operand

Destination operand

Stack operand

Data operand

Start Time: 9:58 AM

120:00

Time Left



1



MADE BY WAQAR SIDDHU

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

The clear screen operation initializes whole block of video memory to

Answer (Please select your correct option)

- 0417
- 0741
- 0714
- 0174

Start Time: 9:58 AM
119:00
Time Left

2



MADE BY WAQAR SIDDHU

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 2?

Answer (Please select your correct option)

IRQ 0

IRQ 1

IRQ 2

IRQ 3

Start Time: 9:58 AM

119:00

Time Left



3



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Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

- IRQ 4
- IRQ 5
- IRQ 6
- IRQ 7

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Time Left

4



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Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is used for sound card or network card?

Answer (Please select your correct option)

IRQ 4

IRQ 5

IRQ 6

IRQ 7

Start Time: 9:58 AM

119:00

Time Left



5



MADE BY WAQAR SIDDHU

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

- 1. 193MHZ
- 2. 193MHZ
- 3. 193MHZ
- 4. 193MHZ

Start Time: 9:58 AM
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Time Left

6



MADE BY WAQAR SIDDHU

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer (Please select your correct option)

Shl AX , 1

Shr AX , 1

NOT AX

AND AX, 0xFFFF

Start Time: 9:58 AM
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Time Left

7



MADE BY WAQAR SIDDHU

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 processor divides interrupts into ____ classes.

Answer (Please select your correct option)

- 2
- 3
- 4
- 5

Start Time: 9:58 AM
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8



MADE BY WAQAR SIDDHU

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

In A4FB:4872 Segment:offset pair the physical address is (both segment and offset are in hexadecimal):

Answer (Please select your correct option)

A4FB0

04872

A9822

A4872

Start Time: 9:58 AM

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Time Left



9



MADE BY WAQAR SIDDHU

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

When the instruction "CMP AX,BX" executes and after execution ZF is set, it indicates that

Answer (Please select your correct option)

AX is greater

BX is greater

Both AX and BX are equal

ZF has no relation with CMP

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Time Left



10



MADE BY WAQAR SIDDHU

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

- Code Segment register
- Data Segment register
- Accumulator register
- Stack segment register

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Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

Stack is a data structure which behaves as

Answer (Please select your correct option)

First in first out manner

Last in first out manner

All in no out manner

Last in last out manner

Start Time: 9:58 AM

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Time Left

12

MADE BY WAQAR SIDDHU

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

Segment wraparound occurs when during the effective address calculation _____ is generated.

Answer (Please select your correct option)

- carry
- error
- zero
- parity

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13



MADE BY WAQAR SIDDHU

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

The bits of _____ register work independently. If combined all these bits then they are meaningless.

Answer (Please select your correct option)

Accumulator (AX)

Instruction Pointer (IP)

Flag

Base

Start Time: 9:58 AM

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14



MADE BY WAQAR SIDDHU

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

IBM PC has separate memory address space and peripheral address space. Memory address space is selected when _____ instruction is given to the processor.

Answer (Please select your correct option)

- MOV
- DEC
- IN
- ADD

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Time Left

15



MADE BY WAQAR SIDDHU

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 10 is used for _____ services.

Answer (Please select your correct option)

- BIOS video
- DOS video
- RAM
- Disk

Start Time: 9:58 AM
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16



MADE BY WAQAR SIDDHU

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ are not used in mathematical operations.

Answer (Please select your correct option)

- Carry, Interrupt and Trap flag
- Direction, Interrupt and Trap flag
- Direction, Overflow and Trap flag
- Direction, Interrupt and Sign flag

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17



MADE BY WAQAR SIDDHU

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

- Variable Electronics Standards Association
- Video Electronics Standards Association
- Video Energy Standards Association
- Vega Energy Standards Association

Start Time: 9:58 AM
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Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

- Programming mode
- Real time mode
- Visualization mode
- Memory mode

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19



MADE BY WAQAR SIDDHU

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

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20



MADE BY WAQAR SIDDHU

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, RISC stands for _____

Answer (Please select your correct option)

Reversed Instruction Set Computer

Reversed Instruction System Computer

Reduced Instruction Set Computer

Reduced Instruction System Computer

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21



MADE BY WAQAR SIDDHU

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, CISC stands for _____

Answer (Please select your correct option)

Complex Instruction Set Computer

Complex Instruction System Computer

Correct Instruction Set Computer

Correct Instruction System Computer

Start Time: 9:58 AM

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Time Left



22



MADE BY WAQAR SIDDHU

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

First Byte

Second Byte

Third Byte

Last Byte

Start Time: 9:58 AM

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Time Left

23

MADE BY WAQAR SIDDHU

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

INT 10

INT 09

INT 08

INT 07

Start Time: 9:58 AM

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Time Left



24



MADE BY WAQAR SIDDHU

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

The thread registration code initializes the PCB and adds it to the linked list, so that which will give it a turn?

Answer (Please select your correct option)

- scheduler
- linker
- debugger
- assembler

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25



MADE BY WAQAR SIDDHU

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking programming, all threads are

Answer (Please select your correct option)

- Independent to each other
- Dependent on each other
- Not executed
- Not scheduled

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MADE BY WAQAR SIDDHU

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

"INT 10 Video - Scroll Up Window" is a type of video services used in which mode?

Answer (Please select your correct option)

- Graphics
- Text
- Audio
- Numeric

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27



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Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

BIOS sees the disks as

Answer (Please select your correct option)

logical storage device

raw storage device

in the form of sectors only

in the form of tracks only

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28



MADE BY WAQAR SIDDHU

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

- DOS
- RAM
- ROM
- BIOS

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29



MADE BY WAQAR SIDDHU

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

In device attribute word, which of the following bit decides whether it is a character device or a block device ?

Answer (Please select your correct option)

Bit 12

Bit 13

Bit 14

Bit 15

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30



MADE BY WAQAR SIDDHU

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

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31



MADE BY WAQAR SIDDHU

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

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Time Left

32



MADE BY WAQAR SIDDHU

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____

Answer (Please select your correct option)

- 7
- 8
- 9
- 6

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33



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Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

- 16 bit processor
- 32 bit processor
- 64 bit processor
- 128 bit processor

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34



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Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

- 1
- 0
- 1
- 2

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35



MADE BY WAQAR SIDDHU

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

- 8 bit
- 16 bit
- 32 bit
- 64 bit

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36



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Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a 32 bit that holds the address of currently executing instruction.

Answer (Please select your correct option)

- Program counter
- Stack pointer
- Register
- Stack

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37



MADE BY WAQAR SIDDHU

Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

- 2
- 4
- 8
- 16

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38



MADE BY WAQAR SIDDHU

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupts is Non Maskable Interrupt (NMI).

Answer (Please select your correct option)

- INT 2
- INT 3
- INT 0
- INT 1

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Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ number of pin(s) of a processor is/are used by the external hardware to generate an interrupt.

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

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40



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Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

What is the purpose of multithreading?

Answer (Please [click here](#) to Add Answer)

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41

Navigation icons: Information, Previous, Home, Next, Previous, Next, Print

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Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

In Pascal language, what naming convention is used to call a function ?

Answer (Please [click here](#) to Add Answer)

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Navigation icons: Information, Previous, Home, Next, Print

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Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

Write the interrupt number which is used for BIOS disk services.

Answer ([Please click here to Add Answer](#))

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43



MADE BY WAQAR SIDDHU

Question No : 44 of 52

Marks: 2 (Budgeted Time 4 Min)

If num1 is 0005 and the address of num1 is 0117 then after execution of instruction "mov ax, [num1]", what will be contents of AX register ?

Answer ([Please click here to Add Answer](#))

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44



MADE BY WAQAR SIDDHU

Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

Following are three common video services for INT 10 used in text mode. Identify the use of each video service which is listed below.

- 0. AH =00h
- 1. AH =01h
- 2. Ah =02h

Answer (Please [click here](#) to Add Answer)

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45

Navigation icons: Information, Previous, Home, Next, Print

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Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

What is the format of input buffer of DOS?

Answer ([Please click here to Add Answer](#))

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46

Navigation controls including an information icon, back, forward, and search buttons.

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Question No : 47 of 52

Marks: 3 (Budgeted Time 6 Min)

In assembly language programming, which information is represented by descriptor's first 16-bits?

Answer ([Please click here to Add Answer](#))

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47

Navigation icons: Information, Previous, Home, Next, Print

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Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

Which two instructions are used to check whether particular bits are set or not? How these two instructions differ from each other?

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

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48



MADE BY WAQAR SIDDHU

Question No : 49 of 52

Marks: 5 (Budgeted Time 10 Min)

In case of branching techniques used in Intel's architecture, which instruction is used to provide temporary diversion and how?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for undo, redo, bold, italic, underline, list, link, unlink, and zoom (100%).

Rich text editor toolbar with dropdown menus for font face (Normal, Arial) and font size (12), and buttons for bold, italic, underline, bulleted list, numbered list, link, and unlink.

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49

Navigation icons: information, back, forward, next, and print.

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Question No : 50 of 52 Marks: 5 (Budgeted Time 10 Min)

Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

CLR (A3)
EXG D0, D2
MOVE.B (A1), (A2)
MOVEA (2222).L, A4

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

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50 ← → ⏪ ⏩ ⏴ ⏵ 🖨

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Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

What are attributes of chargen video service for getting information about font of the text?

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:58 AM
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51



MADE BY WAQAR SIDDHU

Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

In BIOS video services, which specific services in text mode are invoked by the following values of AH register?

- AH = 01
- AH = 02
- AH = 06
- AH = 07
- AH = 09

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

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52



MADE BY WAQAR SIDDHU

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

The operand of PUSH is called

Answer (Please select your correct option)

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Source operand

Destination operand

Stack operand

Data operand

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Answer (Please select your correct option)

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Data operand

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The clear screen operation initializes whole block of video memory to

Answer (Please select your correct option)

0417

0741

0714

0174

VuAnswers.com

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Which of the following IRQs is connected to serial port COM 2?

Answer (Please select your correct option)

IRQ 0



IRQ 1



IRQ 2



IRQ 3



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Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

IRQ 4

IRQ 5

IRQ 6

IRQ 7

VuAnswers.com

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Which of the following IRQs is used for sound card or network card?

Answer (Please select your correct option)

IRQ 4



IRQ 5



IRQ 6



IRQ 7



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The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

1.193MHZ

2.193MHZ

3.193MHZ

4.193MHZ

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By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer (Please select your correct option)

Shl AX , 1

Shr AX, 1

NOT AX

AND AX, 0xFFFF

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8088 processor divides interrupts into ____ classes.

Answer (Please select your correct option)

2

3

4

5

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In A4FB:4872 Segment:offset pair the physical address is (both segment and offset are in hexadecimal):

Answer (Please select your correct option)

A4FB0



04872



A9822



A4872



VuAnswers.com

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When the instruction "CMP AX,BX" executes and after execution ZF is set, it indicates that

Answer (Please select your correct option)

- AX is greater
- BX is greater
- Both AX and BX are equal
- ZF has no relation with CMP

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Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

Code Segment register

Data Segment register

Accumulator register

Stack segment register

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Stack is a data structure which behaves as

first in last out manner 59

Answer (Please select your correct option)

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First in first out manner

Last in first out manner

All in no out manner

Last in last out manner

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Segment wraparound occurs when during the effective address calculation _____ is generated.

Answer (Please select your correct option)

carry

error

zero

parity

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The bits of _____ register work independently. If combined all these bits then they are meaningless.

Answer (Please select your correct option)

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Accumulator (AX)

Instruction Pointer (IP)

Flag

Base

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IBM PC has separate memory address space and peripheral address space. Memory address space is selected when _____ instruction is given to the processor.

Answer (Please select your correct option)

MOV



DEC



IN



ADD



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INT 10 is used for _____ services.

Answer (Please select your correct option)

BIOS video

DOS video

RAM

Disk

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_____ are not used in mathematical operations.

Answer (Please select your correct option)

Carry, Interrupt and Trap flag

Direction, Interrupt and Trap flag

Direction, Overflow and Trap flag

Direction, Interrupt and Sign flag

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VESA stands for _____

Answer (Please select your correct option)

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

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In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

Programming mode

Real time mode

not sure

Visualization mode

Memory mode

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In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

1

2

3

4

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In Computer Architecture, RISC stands for _____

Answer (Please select your correct option)

Reversed Instruction Set Computer

Reversed Instruction System Computer

Reduced Instruction Set Computer

Reduced Instruction System Computer

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In Computer Architecture, CISC stands for _____

Answer (Please select your correct option)

Complex Instruction Set Computer

Complex Instruction System Computer

Correct Instruction Set Computer

Correct Instruction System Computer

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Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

First Byte



Second Byte



Third Byte



Last Byte



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In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

INT 10



INT 09



INT 08



INT 07



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The thread registration code initializes the PCB and adds it to the linked list. so that which will give it a turn?

Answer (Please select your correct option)

scheduler

linker

debugger

assembler

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In multi-tasking programming, all threads are

Answer (Please select your correct option)

Independent to each other

Dependent on each other

Not executed

Not scheduled

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"INT 10 Video - Scroll Up Window" is a type of video services used in which mode?

Answer (Please select your correct option)

Graphics

Text

Audio

Numeric

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BIOS sees the disks as

Answer (Please select your correct option)

logical storage device

raw storage device

in the form of sectors only

in the form of tracks only

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Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

DOS

RAM

ROM

BIOS

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In device attribute word, which of the following bit decides whether it is a character device or a block device ?

Answer (Please select your correct option)

Bit 12



Bit 13



Bit 14



Bit 15



VuAnswers.com

Made by: Waqar Siddhu

The serial port connection is a ----- connector

Answer (Please select your correct option)

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

VuAnswers.com

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer (Please select your correct option)

1

2

3

4

VuAnswers.com

Made by: Waqar Siddhu

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____.

Answer (Please select your correct option)

7

8

9

6

VuAnswers.com

Made by: Waqar Siddhu

8088 is a

Answer (Please select your correct option)

16 bit processor

32 bit processor

64 bit processor

128 bit processor

VuAnswers.com

Made by: Waqar Siddhu

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

1

0

-1

-2

VuAnswers.com

Made by: Waqar Siddhu

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

8 bit

16 bit

32 bit

64 bit

VuAnswers.com

Made by: Waqar Siddhu

In 68K processors there is a 32 bit that holds the address of currently executing instruction.

Answer (Please select your correct option)

Program counter

Stack pointer

Register

Stack

VuAnswers.com

Made by: Waqar Siddhu

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

2



4



8



16



VuAnswers.com

Made by: Waqar Siddhu

Which of the following interrupts is Non Maskable Interrupt (NMI).

Answer (Please select your correct option)

INT 2

INT 3

INT 0

INT 1

VuAnswers.com

Made by: Waqar Siddhu

_____ number of pin(s) of a processor is/are used by the external hardware to generate an interrupt.

Answer (Please select your correct option)

1

VuAnswers.com

2

3

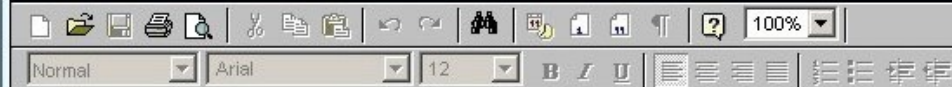
4

Made by: Waqar Siddhu

If num1 is 0005 and the address of num1 is 0117 then after execution of instruction "mov ax, [num1]", what will be contents of AX register ?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



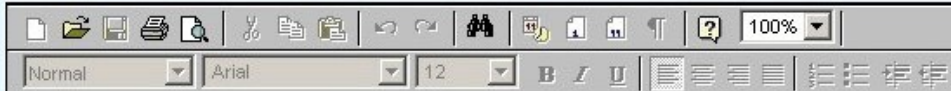
Made by: Waqar Siddhu

Following are three common video services for INT 10 used in text mode. Identify the use of each video service which is listed below.

0. AH =00h
1. AH =01h
2. Ah =02h

Answer ([Please click here to Add Answer](#))

VuAnswers.com

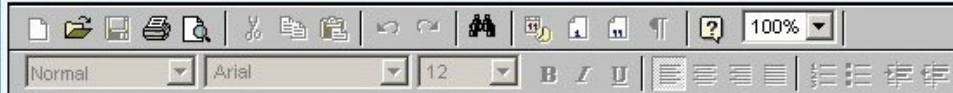


Made by: Waqar Siddhu

What is the format of input buffer of DOS?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

In assembly language programming, which information is represented by descriptor's first 16-bits?

Answer ([Please click here to Add Answer](#))

VuAnswers.com

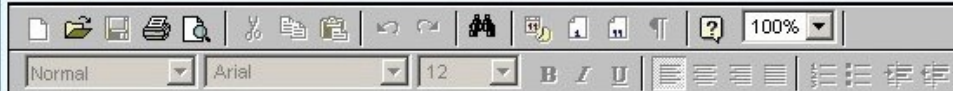


Made by: Waqar Siddhu

Which two instructions are used to check whether particular bits are set or not? How these two instructions differ from each other?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

In case of branching techniques used in Intel's architecture, which instruction is used to provide temporary diversion and how?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

CLR (A3)
EXG D0, D2
MOVE.B (A1), (A2)
MOVEA (2222).L, A4

Answer ([Please click here to Add Answer](#))

VuAnswers.com

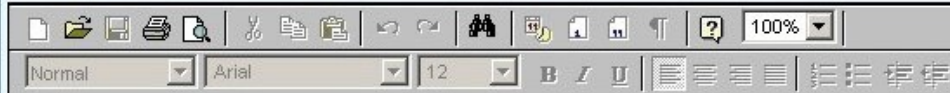


Made by: Waqar Siddhu

What are attributes of charged video service for getting information about font of the text?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

In BIOS video services, which specific services in text mode are invoked by the following values of AH register?

AH = 01

AH = 02

AH = 06

AH = 07

AH = 09

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of "PUSH AX" statement

Answer (Please select your correct option)

VuAnswers.com

- AX register will reside on the stack
- A copy of AX will go on the stack
- The value of AX disappear after moving on stack
- Stack will send an acceptance message

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

An End of Interrupt (EOI) signal is sent by

Answer (Please select your correct option)

VuAnswers.com

Handler

Processor

IRQ

PIC

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

VuAnswers.com

1.193MHZ

2.193MHZ

3.193MHZ

4.193MHZ

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

INT 2

INT 3

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

The actual physical address represented by the segment-offset pair "AB01:FFFF" will be:

Answer (Please select your correct option)

VuAnswers.com

1AB00



001AB



BB00F



F00BB



Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:
SHL DH, CL.

Answer (Please select your correct option)

VuAnswers.com

- 01010000
- 10001010
- 00010100
- 01010111

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

When two devices in the system want to use the same IRQ line then what will happen?

Answer (Please select your correct option)

VuAnswers.com

An IRQ Collision

An IRQ Conflict

An IRQ Crash

An IRQ Blockage

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of STOSW, the CX will be

Answer (Please select your correct option)

VuAnswers.com

Decrement by 1

Decrement by 2

Incremented by 1

Incremented by 2

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following is true ?

Answer (Please select your correct option)

VuAnswers.com

In general the memory cell cannot be wider than data bus

Control bus is unidirectional

Memory can use the address bus to refer to certain register in processor

There is no relation between memory and data bus

Made by: Waqar Siddhu

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

VuAnswers.com

Code Segment register

Data Segment register

Accumulator register

Stack segment register

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

Relating to the stack in 8088 architecture which of the following statement is true ?

Answer (Please select your correct option)

VuAnswers.com

- Single word can not be pushed on the stack
- Single word can not be popped from the stack
- Single byte can not be popped from the stack
- Single quad can be popped from the stack

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

The second byte of the word designated for one screen location holds

Answer (Please select your correct option)

VuAnswers.com

- The dimensions of the screen
- Character position on the screen
- Character color on the screen
- ASCII code of the character

Made by: Waqar Siddhu

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 10 is used for _____ services.

Answer (Please select your correct option)

VuAnswers.com

BIOS video

DOS video

RAM

Disk

Made by: Waqar Siddhu

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

VuAnswers.com

- line
- dot
- circle
- rectangle

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

The value of AH register in the write Graphics pixel service is _____

Answer (Please select your correct option)

VuAnswers.com

0Ch

0Bh

1Ch

2Ch

Made by: Waqar Siddhu

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

VuAnswers.com

control block

Process control block

stack

hard disk

Made by: Waqar Siddhu

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

Answer (Please select your correct option)

VuAnswers.com

a)

b)

c)

d)

Made by: Waqar Siddhu

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA VBE 2.0 is a standard for _____

Answer (Please select your correct option)

VuAnswers.com

Ultra resolution mode

High resolution mode

Medium resolution mode

Low resolution mode

Made by: Waqar Siddhu

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

VuAnswers.com

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

Made by: Waqar Siddhu

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

VuAnswers.com

Video BIOS Extensions

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

Made by: Waqar Siddhu

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

IDTR is a _____ register.

Answer (Please select your correct option)

VuAnswers.com

16 bit

20 bit

32 bit

48 bit

Made by: Waqar Siddhu

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

VuAnswers.com

Programming mode

Real time mode

Visualization mode

Memory mode

Made by: Waqar Siddhu

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

VuAnswers.com

First Byte

Second Byte

Third Byte

Last Byte

Made by: Waqar Siddhu

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

What is the size of a Sector of a standard 1.44MB Floppy disk having 80 tracks and 18 Sectors/track?

Answer (Please select your correct option)

VuAnswers.com

128 Bytes

256 Bytes

512 Bytes

1024 Bytes

Made by: Waqar Siddhu

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

VuAnswers.com

INT 10

INT 09

INT 08

INT 07

Made by: Waqar Siddhu

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

VuAnswers.com

global

local

legal

illegal

Made by: Waqar Siddhu

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

Technically IAPX88 stands for “_____ Advanced Processor Extensions 88.”

Answer (Please select your correct option)

VuAnswers.com

Intel

Integrated

Intra

Internal

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

In Left-Shift-Operation, the left most bit _____

Answer (Please select your correct option)

VuAnswers.com

- will be dropped
- will go into CF
- will be moved to the right-most position
- will always be 1

Made by: Waqar Siddhu

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

VuAnswers.com

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

Made by: Waqar Siddhu

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

VuAnswers.com

6

7

8

9

Made by: Waqar Siddhu

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CD (Carrier Detect) ?

Answer (Please select your correct option)

VuAnswers.com

1

2

3

4

Made by: Waqar Siddhu

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following BIOS INT provides serial port services

Answer (Please select your correct option)

VuAnswers.com

INT14

INT11

INT12

INT13

Made by: Waqar Siddhu

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

VuAnswers.com

16 bit processor

32 bit processor

64 bit processor

128 bit processor

Made by: Waqar Siddhu

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

A 32 bit address register can access up to of memory, so memory access has increased a lot.

Answer (Please select your correct option)

VuAnswers.com

2GB



4GB



6GB



8GB



Made by: Waqar Siddhu

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

VuAnswers.com

1

0

-1

-2

Made by: Waqar Siddhu

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

VuAnswers.com

8 bit

16 bit

32 bit

64 bit

Made by: Waqar Siddhu

In 68K processors there is a 32 bit that holds the address of currently executing instruction.

VuAnswers.com

Answer (Please select your correct option)

Program counter

Stack pointer

Register

Stack

Made by: Waqar Siddhu

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Programmable Interrupt Controller (PIC) has

Answer (Please select your correct option)

VuAnswers.com

- One input signals and eight output signals
- One input signal and one output signal
- Eight input signals and one output signals
- Eight input signals and eight output signals

Made by: Waqar Siddhu

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

VuAnswers.com

INT 0



INT 1



INT 2



INT 3



Made by: Waqar Siddhu

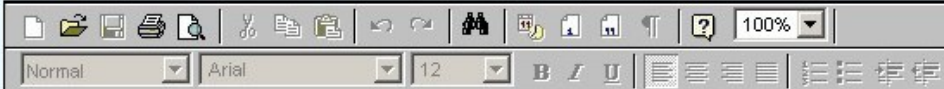
Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

For which purpose INT 1 is used and describe the functionality of this interrupt?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

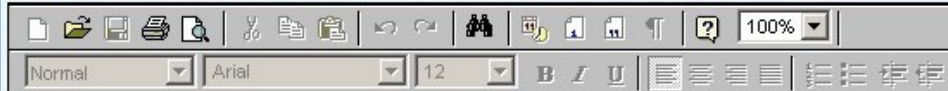
Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

What are different register settings for file closing using INT 21 service?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

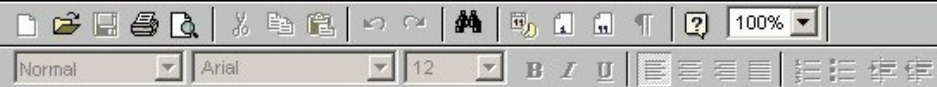
Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

In C language, what naming convention is used to call a function ?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



c prepends an underscore to
every one

Made by: Waqar Siddhu

Question No : 44 of 52

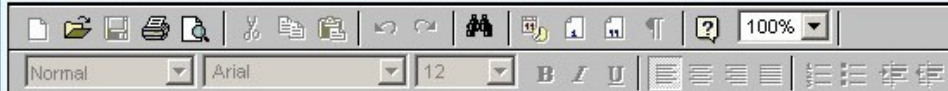
Marks: 2 (Budgeted Time 4 Min)

Write a single instruction that can be used as an alternative of the following two instructions?

```
DEC CX  
JNZ NextChar
```

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

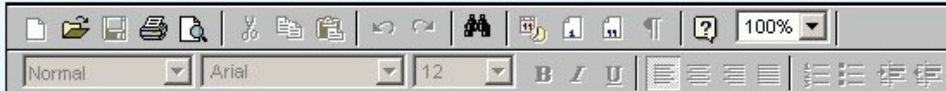
Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

Suppose we want to execute multiple processes at same time. For this purpose we need to store and restore states of processes running in a multitasking operating system. Which process is used to store and restore states of processes in a multitasking operating system.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

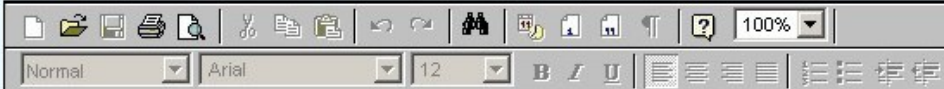
Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

Consider the function "**int subtract (int x, int y)**" declared in C, Write the code to call this function from Assembly language.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

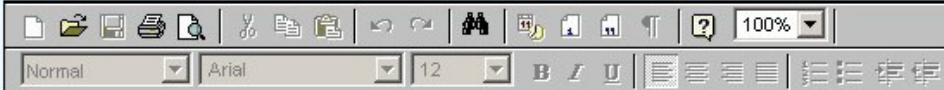
Question No : 47 of 52

Marks: 3 (Budgeted Time 6 Min)

For what purpose Global Descriptor Table (GDT) is used?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 48 of 52

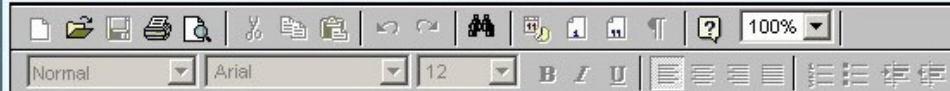
Marks: 3 (Budgeted Time 6 Min)

Check these instructions, whether they are valid or not, if not why? Make corrections and also mention how many bits have been moved in each instruction?

```
mov ax, [num1]
mov al, [num1]
mov ax, al
mov [num1], 5
```

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

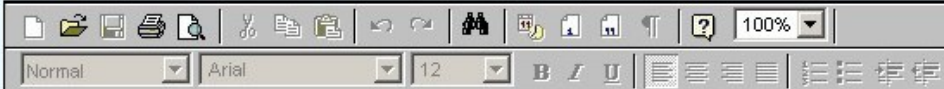
Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

```
mov ax, [num1]
mov al, [num1]
mov ax, al
mov [num1], 5
mov byte[num1], 5
```

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 49 of 52

Marks: 5 (Budgeted Time 10 Min)

Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

CLR (A3)
EXG D0, D2
MOVE.B (A1), (A2)
MOVEA (2222).L, A4

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

What is the effective address generated by each of the following instruction?

Initially BX=0x0100, label=0x0234, [label]=0x0010, and SI=0x00E1

- a. `mov ax, [bx+40]`
- b. `mov ax, [bx+label]`

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

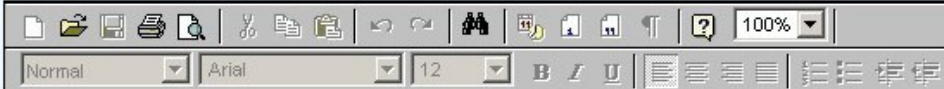
Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

In INT 10 of video services while writing graphics pixel, what do the values of registers BH, AL, CX and DX correspond to?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

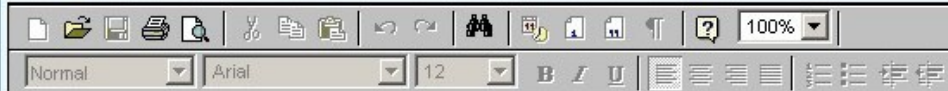
Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

What are different register settings for memory allocation using INT 21 Service?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

CS401 Computer Architecture and Assembly Language Programming

BC100402988 (MALIK MUHAMMAD AYAZ)

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSW instruction, when DF is clear, SI is

Answer (Please select your correct option)

Incremented by 1

Incremented by 2

Decrementd by 1

Decrementd by 2

Start Time: 9:36 AM

120:00

MADE BY WAQAR SIDDHU

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSW instruction, when DF is clear, SI is

Answer (Please select your correct option)

Incremented by 1

Incremented by 2

Decrementd by 1

Decrementd by 2

Start Time: 9:36 AM

120:00

Time Left



1



MADE BY WAQAR SIDDHU

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

- IRQ 4
- IRQ 5
- IRQ 6
- IRQ 7

Start Time: 9:36 AM
119:00
Time Left

2



MADE BY WAQAR SIDDHU

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is used by the parallel port?

Answer (Please select your correct option)

- IRQ 4
- IRQ 5
- IRQ 6
- IRQ 7

Start Time: 9:36 AM
119:00
Time Left

3



MADE BY WAQAR SIDDHU

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

- 1. 193MHZ
- 2. 193MHZ
- 3. 193MHZ
- 4. 193MHZ

Start Time: 9:36 AM
119:00
Time Left

4



MADE BY WAQAR SIDDHU

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

Key board interrupt

Timer interrupt

INT 2

INT 3

Start Time: 9:36 AM

119:00

Time Left



5



MADE BY WAQAR SIDDHU

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer (Please select your correct option)

- Shl AX , 1
- Shr AX, 1
- NOT AX
- AND AX, 0xFFFF

Start Time: 9:36 AM

119:00

Time Left



6

MADE BY WAQAR SIDDHU

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

In the rotate right operation every bit moves one position to the right and the bit dropped from the right is inserted at the left and:

Answer (Please select your correct option)

- The dropped bit is also copied in CF
- The dropped bit is also copied in AF
- The dropped bit is also copied in ZF
- The dropped bit is not copied in any flag

Start Time: 9:36 AM
119:00
Time Left

7



MADE BY WAQAR SIDDHU

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

In string instructions, CX is always

Answer (Please select your correct option)

- Decrement by 1
- Decrement by 2
- Increment by 1
- Increment by 2

Start Time: 9:36 AM

119:00
Time Left



8



MADE BY WAQAR SIDDHU

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

The word size of the processor is defined by the size of _____

Answer (Please select your correct option)

- Its accumulator register
- Address bus attached
- Memory attached
- Control Bus

Start Time: 9:36 AM
119:00
Time Left

9



MADE BY WAQAR SIDDHU

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ cannot predict which element the _____ needs, at a particular instant of time.

Answer (Please select your correct option)

- Control bus, Memory
- Processor, Control bus
- Processor, Memory
- Memory, Processor
- Control bus, Memory

Start Time: 9:36 AM

119:00
Time Left

10



MADE BY WAQAR SIDDHU

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

The time interval between two timer ticks is ____

Answer (Please select your correct option)

40ms

45ms

50ms

55ms

Start Time: 9:36 AM

119:00
Time Left

11



MADE BY WAQAR SIDDHU

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

Answer (Please select your correct option)

- third
- fourth
- first
- second

Start Time: 9:36 AM
119:00
Time Left

12



MADE BY WAQAR SIDDHU

Question No : 13 of 52 Marks: 1 (Budgeted Time 1 Min)

A thread can not be created _____

Answer (Please select your correct option)

- in static way
- in dynamic way
- in static and dynamic way at the same time
- manually

Start Time: 9:36 AM
119:00
Time Left



Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

- control block
- Process control block
- stack
- hard disk

Start Time: 9:36 AM
119:00
Time Left

14



MADE BY WAQAR SIDDHU

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

- Variable Electronics Standards Association
- Video Electronics Standards Association
- Video Energy Standards Association
- Vega Energy Standards Association

Start Time: 9:36 AM

119:00
Time Left

15



MADE BY WAQAR SIDDHU

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

- Video BIOS Extensions
- Video BIOS Emergence
- Video Binary Extensions
- Video Blocked Extensions

Start Time: 9:36 AM
119:00
Time Left

16



MADE BY WAQAR SIDDHU

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

Programming mode

Real time mode

Visualization mode

Memory mode

Start Time: 9:36 AM

119:00

Time Left



17



MADE BY WAQAR SIDDHU

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

Start Time: 9:36 AM
119:00
Time Left



MADE BY WAQAR SIDDHU

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

In Motorola 68K processors, _____ registers can hold addresses in indirect memory accesses.

Answer (Please select your correct option)

- A0-A7
- B0-B7
- C0-C7
- D0-D7

Start Time: 9:36 AM
118:00
Time Left



MADE BY WAQAR SIDDHU

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

Motorola 68K processors have _____ 32 bit general purpose registers.

Answer (Please select your correct option)

- 32
- 64
- 8
- 16

Start Time: 9:36 AM
118:00
Time Left

20



MADE BY WAQAR SIDDHU

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

- First Byte
- Second Byte
- Third Byte
- Last Byte

Start Time: 9:36 AM
118:00
Time Left

21



MADE BY WAQAR SIDDHU

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

- global
- local
- legal
- illegal

Start Time: 9:36 AM
118:00
Time Left



Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking programming, all threads are

Answer (Please select your correct option)

- Independent to each other
- Dependent on each other
- Not executed
- Not scheduled

Start Time: 9:36 AM

118:00

Time Left



23



MADE BY WAQAR SIDDHU

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Video services are exported via

Answer (Please select your correct option)

INT 7

INT 10

INT 9

INT 8

Start Time: 9:36 AM

118:00
Time Left

24



MADE BY WAQAR SIDDHU

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

The maximum memory, LAPX88 can access is _____.

Answer (Please select your correct option)

- 1 KB
- 64 KB
- 1 MB
- 64 MB

Start Time: 9:36 AM

118:00
Time Left

25



MADE BY WAQAR SIDDHU

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In direct addressing the memory address is _____ and is given in the instruction.

Answer (Please select your correct option)

- Fixed
- Empty
- A variable
- A register

Start Time: 9:36 AM
118:00
Time Left

26



MADE BY WAQAR SIDDHU

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Instruction "mov ax, bf" has a _____ problem.

Answer (Please select your correct option)

size mismatch

value mismatch

operand mismatch

memory mismatch

Start Time: 9:36 AM

118:00

Time Left



27



MADE BY WAQAR SIDDHU

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

The jump "JC" is taken when

Answer (Please select your correct option)

- CF=1
- CF=0
- ZF=0
- ZF=1

Start Time: 9:36 AM
118:00
Time Left



MADE BY WAQAR SIDDHU

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

- DOS
- RAM
- ROM
- BIOS

Start Time: 9:36 AM
118:00
Time Left

29



MADE BY WAQAR SIDDHU

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

- 9 pin DB-9
- 8 pin DB-9
- 3 pin DB-9
- 9 pin DB-5

Start Time: 9:36 AM
118:00
Time Left

30



MADE BY WAQAR SIDDHU

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer (Please select your correct option)

- 5
- 6
- 7
- 8

Start Time: 9:36 AM
118:00
Time Left

31



MADE BY WAQAR SIDDHU

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

- 6
- 7
- 8
- 9

Start Time: 9:36 AM
118:00
Time Left



Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____

Answer (Please select your correct option)

- 6
- 7
- 8
- 9

Start Time: 9:36 AM
118:00
Time Left

33



MADE BY WAQAR SIDDHU

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

- 16 bit processor
- 32 bit processor
- 64 bit processor
- 128 bit processor

Start Time: 9:36 AM
118:00
Time Left

34



MADE BY WAQAR SIDDHU

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

AX register in extended 32 bit register is renamed as _____

Answer (Please select your correct option)

- AXE
- EAX
- AAX
- XAX

Start Time: 9:36 AM
118:00
Time Left

35



MADE BY WAQAR SIDDHU

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

- 1
- 0
- 1
- 2

Start Time: 9:36 AM
118:00
Time Left

36



MADE BY WAQAR SIDDHU

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

- 8 bit
- 16 bit
- 32 bit
- 64 bit

Start Time: 9:36 AM
118:00
Time Left

37



MADE BY WAQAR SIDDHU

Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

- 2
- 4
- 8
- 16

Start Time: 9:36 AM
118:00
Time Left

38



MADE BY WAQAR SIDDHU

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

- INT 0
- INT 1
- INT 2
- INT 3

Start Time: 9:36 AM
118:00
Time Left



MADE BY WAQAR SIDDHU

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

- 19
- 20
- 21
- 22

Start Time: 9:36 AM
118:00
Time Left

40



MADE BY WAQAR SIDDHU

Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

Device drivers are operating system extensions which extend its services to new devices. Describe the structure of "Device Driver File" which is used for device management.

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

41

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

With reference to branching technique used in assembly language programming, write any two control instructions.

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

42

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

How local thread variables are created and accessed ?

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

43



MADE BY WAQAR SIDDHU

Question No : 44 of 52

Marks: 2 (Budgeted Time 4 Min)

What are the following statements represent?

gdt: dd 0x00000000, 0x00000000

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

44



MADE BY WAQAR SIDDHU

Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

What are the important "command codes" in request header of device driver?

Answer (Please [click here](#) to Add Answer)

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Start Time: 9:36 AM
118:00
Time Left

45



MADE BY WAQAR SIDDHU

Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

In order to access Video memory in high resolution (more than a megabyte MB), what standard we have to use?

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

46

Navigation controls including an information icon, back, forward, and refresh buttons.

MADE BY WAQAR SIDDHU

Question No : 47 of 52 Marks: 3 (Budgeted Time 6 Min)

Describe "Indexed Register Indirect + Offset" addressing mode with an example.

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

47

Navigation controls including an information icon, back, forward, and search buttons.

MADE BY WAQAR SIDDHU

Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

With reference to the multitasking program "TSR caller". Describe what task will be performed by each of these instructions?

- `mov al, [chars+bx]`
- `mov [es:40], al`
- `inc bx`

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

48

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 49 of 52 Marks: 5 (Budgeted Time 10 Min)

How many types of interrupt are used for writing debugger and also elaborate functionality of these interrupts with proper explanation?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

Question No : 50 of 52

Marks: 5 (Budgeted Time 10 Min)

What are the contents at the starting position of the file designated as device driver and mention important attributes of this driver file?

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for undo, redo, bold, italic, underline, bulleted list, numbered list, link, unlink, insert image, insert table, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

50

Navigation icons: information, back, forward, and print.

MADE BY WAQAR SIDDHU

Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

At which location interrupt gate descriptor table is stored and describe the structure of this table with an example?

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for undo, redo, bold, italic, underline, list, link, unlink, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

51



MADE BY WAQAR SIDDHU

Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

Give the new values of AX and DX for each of the following instructions, or write if the overflow occurs.

- . DIV BX, if DX contains 0000h, AX contains 0007h, and BX contains 0002h
- a. DIV BX, if DX contains 0000h, AX contains FFFFh, and BX contains 0010h

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:36 AM
118:00
Time Left

52

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSW instruction, when DF is clear, SI is

Answer (Please select your correct option)

VuAnswers.com

Incremented by 1

Incremented by 2

Decrement by 1

Decrement by 2

Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSW instruction, when DF is clear, SI is

Answer (Please select your correct option)

VuAnswers.com

Incremented by 1

Incremented by 2

Decrement by 1

Decrement by 2

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is used by the parallel port?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PTT) has input frequency of

Answer (Please select your correct option)

VuAnswers.com

1. 193MHZ

2. 193MHZ

3. 193MHZ

4. 193MHZ

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

INT 2

INT 3

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer (Please select your correct option)

VuAnswers.com

Shl AX, 1

Shr AX, 1

NOT AX

AND AX, 0xFFFF

Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

In the rotate right operation every bit moves one position to the right and the bit dropped from the right is inserted at the left and:

Answer (Please select your correct option)

VuAnswers.com

- The dropped bit is also copied in CF
- The dropped bit is also copied in AF
- The dropped bit is also copied in ZF
- The dropped bit is not copied in any flag

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

In string instructions, CX is always

Answer (Please select your correct option)

VuAnswers.com

Decrement by 1

Decrement by 2

Incremented by 1

Incremented by 2

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

The word size of the processor is defined by the size of _____

Answer (Please select your correct option)

VuAnswers.com

Its accumulator register

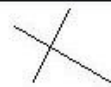
correct

Address bus attached

Memory attached

Control Bus

not sure



Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ cannot predict which element the _____ needs, at a particular instant of time.

Answer (Please select your correct option)

VuAnswers.com

Control bus, Memory

Processor, Control bus
 Processor, Memory

Memory, Processor

Control bus, Memory

Made by: Waqar Siddhu

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

The time interval between two timer ticks is ____

Answer (Please select your correct option)

VuAnswers.com

40ms

45ms

50ms

55ms

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

Answer (Please select your correct option)

VuAnswers.com

third

fourth

first

second

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

A thread can not be created _____

Answer (Please select your correct option)

VuAnswers.com

in static way

in dynamic way

in static and dynamic way at the same time

manually

Made by: Waqar Siddhu

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Select your correct option)

VuAnswers.com

control block

Process control block

stack

hard disk

Made by: Waqar Siddhu

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

VuAnswers.com

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

VuAnswers.com

Video BIOS Extensions

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

Made by: Waqar Siddhu

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

VuAnswers.com

Programming mode

Real time mode

Visualization mode

Memory mode

Made by: Waqar Siddhu

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

VuAnswers.com

1

2

3

4

Made by: Waqar Siddhu

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

In Motorola 68K processors, _____ registers can hold addresses in indirect memory accesses.

Answer (Please select your correct option)

VuAnswers.com

A0-A7

B0-B7

C0-C7

D0-D7

Made by: Waqar Siddhu

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

Motorola 68K processors have _____ 32 bit general purpose registers.

Answer (Please select your correct option)

VuAnswers.com

32

64

8

16

Made by: Waqar Siddhu

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

VuAnswers.com

First Byte

Second Byte

Third Byte

Last Byte

Made by: Waqar Siddhu

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

VuAnswers.com

global

local

legal

illegal

Made by: Waqar Siddhu

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking programming, all threads are

Answer (Please select your correct option)

VuAnswers.com

Independent to each other

Dependent on each other

Not executed

Not scheduled

Made by: Waqar Siddhu

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Video services are exported via

Answer (Please select your correct option)

VuAnswers.com

INT 7

INT 10

INT 9

INT 8

Made by: Waqar Siddhu

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

The maximum memory, IAPX88 can access is _____.

Answer (Please select your correct option)

VuAnswers.com

1 KB

64 KB

1 MB

64 MB

Made by: Waqar Siddhu

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In direct addressing the memory address is _____ and is given in the instruction.

Answer (Please select your correct option)

VuAnswers.com

Fixed

Empty

A variable

A register

Made by: Waqar Siddhu

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Instruction "mov ax, bl" has a _____ problem.

Answer (Please select your correct option)

VuAnswers.com

size mismatch

value mismatch

operand mismatch

memory mismatch

Made by: Waqar Siddhu

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

The jump "JC" is taken when

Answer (Please select your correct option)

VuAnswers.com

CF=1

CF=0

ZF=0

ZF=1

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

VuAnswers.com

DOS

RAM

ROM

BIOS

Made by: Waqar Siddhu

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

VuAnswers.com

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

Made by: Waqar Siddhu

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer (Please select your correct option)

VuAnswers.com

5

6

7

8

Made by: Waqar Siddhu

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

VuAnswers.com

6

7

8

9

Made by: Waqar Siddhu

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____

Answer (Please select your correct option)

VuAnswers.com

6



7



8



9



Made by: Waqar Siddhu

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

VuAnswers.com

16 bit processor

32 bit processor

64 bit processor

128 bit processor

Made by: Waqar Siddhu

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

AX register in extended 32 bit register is renamed as _____

Answer (Please select your correct option)

VuAnswers.com

AXE

EAX

AAX

XAX

Made by: Waqar Siddhu

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

VuAnswers.com

1

0

-1

-2

Made by: Waqar Siddhu

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a _____ program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

VuAnswers.com

8 bit

16 bit

32 bit

64 bit

Made by: Waqar Siddhu

Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

VuAnswers.com

2



4



8



16



Made by: Waqar Siddhu

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

VuAnswers.com

INT 0

INT 1

INT 2

INT 3

Made by: Waqar Siddhu

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

VuAnswers.com

19



20



21



22



Made by: Waqar Siddhu

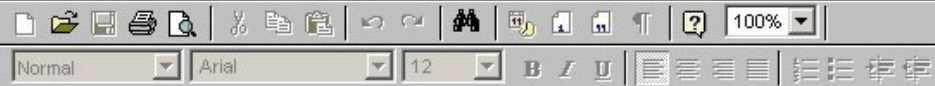
Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

Device drivers are operating system extensions which extend its services to new devices. Describe the structure of "Device Driver File" which is used for device management.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

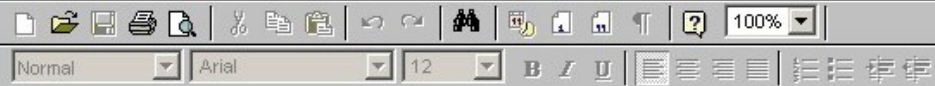
Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

With reference to branching technique used in assembly language programming, write any two control instructions.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

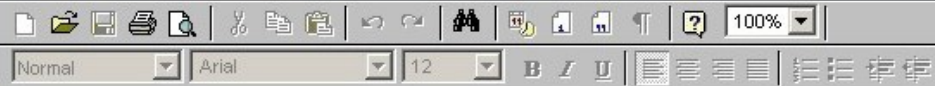
Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

How local thread variables are created and accessed ?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 44 of 52

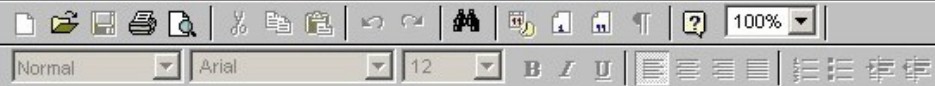
Marks: 2 (Budgeted Time 4 Min)

What are the following statements represent?

gdt: dd 0x00000000, 0x00000000

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

What are the important "command codes" in request header of device driver?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

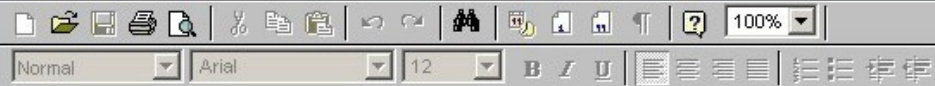
Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

In order to access Video memory in high resolution (more than a megabyte MB), what standard we have to use?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

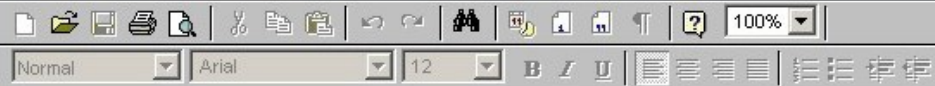
Question No : 47 of 52

Marks: 3 (Budgeted Time 6 Min)

Describe "Indexed Register Indirect + Offset" addressing mode with an example.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 48 of 52

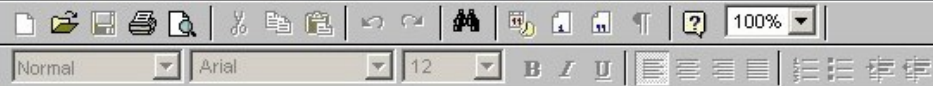
Marks: 3 (Budgeted Time 6 Min)

With reference to the multitasking program "TSR caller". Describe what task will be performed by each of these instructions?

- `mov al, [chars+bx]`
- `mov [es:40], al`
- `inc bx`

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

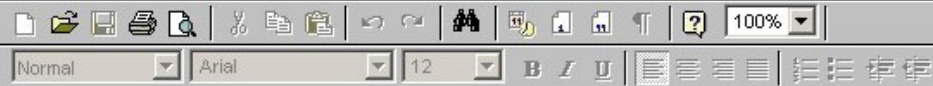
Question No : 49 of 52

Marks: 5 (Budgeted Time 10 Min)

How many types of interrupt are used for writing debugger and also elaborate functionality of these interrupts with proper explanation?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

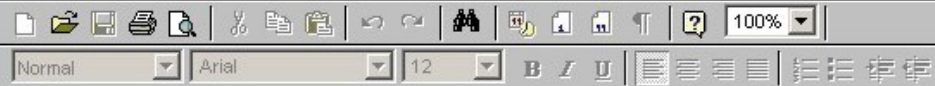
Question No : 50 of 52

Marks: 5 (Budgeted Time 10 Min)

What are the contents at the starting position of the file designated as device driver and mention important attributes of this driver file?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

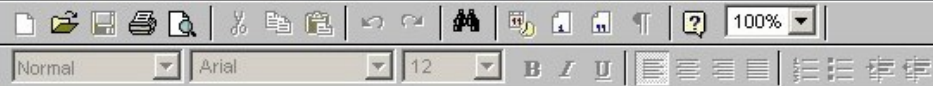
Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

At which location interrupt gate descriptor table is stored and describe the structure of this table with an example?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Give the new values of AX and DX for each of the following instructions, or write if the overflow occurs.

- b. DIV BX, if DX contains 0000h, AX contains 0007h, and BX contains 0002h
- a. DIV BX, if DX contains 0000h, AX contains FFFFh, and BX contains 0010h

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

CS401 Computer Architecture and Assembly Language Programming

BC110201815 (MUHAMMAD FAHAD ALI)

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In an indirect addressing mode the address of memory cell

Answer (Please select your correct option)

is placed in a register

is directly accessed by instruction

is constant

is variable

Start Time: 9:42 AM

120:00

MADE BY WAQAR SIDDHU

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In an indirect addressing mode the address of memory cell

Answer (Please select your correct option)

is placed in a register

is directly accessed by instruction

is constant

is variable

Start Time: 9:42 AM

119:00

Time Left



1

MADE BY WAQAR SIDDHU



Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSB instruction, when DF is clear, SI is

Answer (Please select your correct option)

Incremented by 1

Incremented by 2

Decrementd by 1

Decrementd by 2

Start Time: 9:42 AM

119:00

Time Left

MADE BY WAQAR SIDDHU

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

IBM AT has how many PICs (Programmable interrupt controller)

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

Start Time: 9:42 AM
119:00
Time Left

3



MADE BY WAQAR SIDDHU

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

Processor informs the memory that it wants to read data, using :

Answer (Please select your correct option)

- Address bus
- Data bus
- Control bus
- It informs without using any bus

Start Time: 9:42 AM
119:00
Time Left

4



MADE BY WAQAR SIDDHU

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Memory cell width refers to _____

Answer (Please select your correct option)

- Number of bits in a cell
- The maximum value that can be stored in a cell including sign-bit
- The maximum value that can be stored in a cell excluding sign-bit
- Address of that cell

Start Time: 9:42 AM
119:00
Time Left

5



MADE BY WAQAR SIDDHU

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

In MUL instruction if the source operand is a byte then it is multiplied by register

Answer (Please select your correct option)

- AX
- AL
- CX
- DL

Start Time: 9:42 AM
119:00
Time Left

6



MADE BY WAQAR SIDDHU

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

In the word designated for one screen location, the higher address contains

Answer (Please select your correct option)

- The character code
- The attribute byte
- The parameters
- The dimensions

Start Time: 9:42 AM
119:00
Time Left

7



MADE BY WAQAR SIDDHU

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

"mov [bx], ax" moves the two bytes contents of the AX register to the address contained in BX register in the current

Answer (Please select your correct option)

- Stack segment
- Code segment
- Data segment
- Extra segment

Start Time: 9:42 AM

119:00

Time Left



8



MADE BY WAQAR SIDDHU

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

CALL instruction transfer the control

Answer (Please select your correct option)

- Permanently
- Temporarily
- Permanently when CALL is NEAR
- Permanently when CALL is FAR

Start Time: 9:42 AM

119:00

Time Left



9



MADE BY WAQAR SIDDHU

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

A 32 bit processor has an accumulator of _____

Answer (Please select your correct option)

- 8 bits
- 16 bits
- 32 bits
- 64 bits

Start Time: 9:42 AM
119:00
Time Left

10



MADE BY WAQAR SIDDHU

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

The parallel port connector is called _____

Answer (Please select your correct option)

DB-25

BD-25

DB-24

BD-24

Start Time: 9:42 AM

119:00

Time Left



11



MADE BY WAQAR SIDDHU

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

The Single step interrupt is _____

Answer (Please select your correct option)

Hardware interrupt

Like divide by zero interrupt

Like divide by 1 interrupt

Like divide by 2 interrupt

Start Time: 9:42 AM

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Time Left

12



MADE BY WAQAR SIDDHU

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ are not used in mathematical operations.

Answer (Please select your correct option)

- Carry, Interrupt and Trap flag
- Direction, Interrupt and Trap flag
- Direction, Overflow and Trap flag
- Direction, Interrupt and Sign flag

Start Time: 9:42 AM

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Time Left

13



MADE BY WAQAR SIDDHU

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

- line
- dot
- circle
- rectangle

Start Time: 9:42 AM
119:00
Time Left

14



MADE BY WAQAR SIDDHU

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

Answer (Please select your correct option)

- third
- fourth
- first
- second

Start Time: 9:42 AM

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Time Left

15



MADE BY WAQAR SIDDHU

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA VBE 2.0 is a standard for _____

Answer (Please select your correct option)

Ultra resolution mode

High resolution mode

Medium resolution mode

Low resolution mode

Start Time: 9:42 AM

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Time Left



16



MADE BY WAQAR SIDDHU

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

- Video BIOS Extensions
- Video BIOS Emergence
- Video Binary Extensions
- Video Blocked Extensions

Start Time: 9:42 AM
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Time Left

17



MADE BY WAQAR SIDDHU

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

IDTR is a _____ register.

Answer (Please select your correct option)

- 16 bit
- 20 bit
- 32 bit
- 48 bit

Start Time: 9:42 AM
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Time Left



MADE BY WAQAR SIDDHU

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

Programming mode

Real time mode

Visualization mode

Memory mode

Start Time: 9:42 AM

119:00

Time Left



19



MADE BY WAQAR SIDDHU

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

Start Time: 9:42 AM
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Time Left

20



MADE BY WAQAR SIDDHU

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

In NASM, an imported symbol is declared with the _____ while and exported symbol is declared with the _____

Answer (Please select your correct option)

Global directive, External directive

External directive, Global directive

Home Directive, Foreign Directive

Foreign Directive, Home Directive

Start Time: 9:42 AM

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Time Left



21



MADE BY WAQAR SIDDHU

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, RISC stands for _____

Answer (Please select your correct option)

Reversed Instruction Set Computer

Reversed Instruction System Computer

Reduced Instruction Set Computer

Reduced Instruction System Computer

Start Time: 9:42 AM

119:00

Time Left



22



MADE BY WAQAR SIDDHU

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, CISC stands for _____

Answer (Please select your correct option)

- Complex Instruction Set Computer
- Complex Instruction System Computer
- Correct Instruction Set Computer
- Correct Instruction System Computer

Start Time: 9:42 AM
119:00
Time Left

23



MADE BY WAQAR SIDDHU

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

- global
- local
- legal
- illegal

Start Time: 9:42 AM
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Time Left

24



MADE BY WAQAR SIDDHU

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

"INT 10 Video - Scroll Up Window" is a type of video services used in which mode?

Answer (Please select your correct option)

Numeric

Graphics

Text

Audio

Start Time: 9:42 AM

118:00
Time Left

25



MADE BY WAQAR SIDDHU

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

The first possible segment base value is _____.

Answer (Please select your correct option)

0000

0001

0010

0100

Start Time: 9:42 AM

118:00

Time Left



26



MADE BY WAQAR SIDDHU

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

BIOS sees the disks as

Answer (Please select your correct option)

logical storage device

raw storage device

in the form of sectors only

in the form of tracks only

Start Time: 9:42 AM

118:00

Time Left



27



MADE BY WAQAR SIDDHU

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

In device attribute word, which of the following bit decides whether it is a character device or a block device ?

Answer (Please select your correct option)

- Bit 12
- Bit 13
- Bit 14
- Bit 15

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Time Left



MADE BY WAQAR SIDDHU

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to DSR (Data Set Ready) ?

Answer (Please select your correct option)

- 4
- 5
- 6
- 7

Start Time: 9:42 AM
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Time Left

29



MADE BY WAQAR SIDDHU

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer (Please select your correct option)

- 5
- 6
- 7
- 8

Start Time: 9:42 AM
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Time Left

30



MADE BY WAQAR SIDDHU

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to RD (Received Data) ?

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

Start Time: 9:42 AM
118:00
Time Left

31



MADE BY WAQAR SIDDHU

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

Start Time: 9:42 AM
118:00
Time Left

32



MADE BY WAQAR SIDDHU

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following BIOS INT provides serial port services

Answer (Please select your correct option)

- INT14
- INT11
- INT12
- INT13

Start Time: 9:42 AM
118:00
Time Left

33



MADE BY WAQAR SIDDHU

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

A 32 bit address register can access up to of memory, so memory access has increased a lot.

Answer (Please select your correct option)

- 2GB
- 4GB
- 6GB
- 8GB

Start Time: 9:42 AM
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Time Left

34



MADE BY WAQAR SIDDHU

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

The segment address of interrupt n will be at

Answer (Please select your correct option)

nx4

nx4+1

nx4+2

nx4+3

Start Time: 9:42 AM

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Time Left



35



MADE BY WAQAR SIDDHU

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

- 2
- 4
- 8
- 16

Start Time: 9:42 AM
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Time Left

36



MADE BY WAQAR SIDDHU

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupts is Non Maskable Interrupt (NMI).

Answer (Please select your correct option)

INT 2

INT 3

INT 0

INT 1

Start Time: 9:42 AM

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Time Left

37



MADE BY WAQAR SIDDHU

Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following is a special type of interrupt that returns to the same instruction instead of the next instruction?

Answer (Please select your correct option)

- Divide overflow interrupt
- Debug interrupt
- Arithmetic overflow interrupt
- Change of sign interrupt

Start Time: 9:42 AM
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Time Left

38



MADE BY WAQAR SIDDHU

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

- 19
- 20
- 21
- 22

Start Time: 9:42 AM
118:00
Time Left

39



MADE BY WAQAR SIDDHU

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

Which one of the following interrupts is used for arithmetic overflow?

Answer (Please select your correct option)

- INT 4
- INT 1
- INT 2
- INT 3

Start Time: 9:42 AM
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Time Left

40



MADE BY WAQAR SIDDHU

Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

For which purpose INT 1 is used and describe the functionality of this interrupt?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:42 AM
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41

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

What is programmer's view of processor?

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for undo, redo, bold, italic, underline, bulleted list, numbered list, link, unlink, insert image, link, unlink, and a 100% zoom level. The text area below is empty.

Start Time: 9:42 AM
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Time Left

42



MADE BY WAQAR SIDDHU

Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

We have 16 bit (word) information for device attribute. For what specific purpose the fifteenth bit is reserved?

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:42 AM
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Time Left

43



MADE BY WAQAR SIDDHU

Question No : 44 of 52

Marks: 2 (Budgeted Time 4 Min)

Write the complete command to assemble ABC.asm using NASM.

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:42 AM
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44

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

What is the range of addressable memory in protected mode and which register is used to reference the descriptor for the Stack segment?

Answer (Please [click here](#) to Add Answer)

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Start Time: 9:42 AM
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Time Left

45



MADE BY WAQAR SIDDHU

Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

How many routines are required to be defined by a device driver and what are their names?

Answer (Please [click here](#) to Add Answer)

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Start Time: 9:42 AM
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Time Left

46



MADE BY WAQAR SIDDHU

Question No : 47 of 52

Marks: 3 (Budgeted Time 6 Min)

Suggest a modification in the design of 8088 Processor Architecture such that it can support 16MB RAM?

Hint: It will require increase or decrease in Address bus lines.

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:42 AM
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Time Left

47

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

What are two different modes of video services of BIOS? And differentiate between both of modes.

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:42 AM
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Time Left

48



MADE BY WAQAR SIDDHU

Question No : 49 of 52

Marks: 5 (Budgeted Time 10 Min)

How many pins are used for serial port communication and which types of communication occur through serial port pins?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:42 AM
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Time Left

49



MADE BY WAQAR SIDDHU

Question No : 50 of 52 Marks: 5 (Budgeted Time 10 Min)

Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

- CLR (A3)
- EXG D0, D2
- MOVE.B (A1), (A2)
- MOVEA (2222).L, A4

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 9:42 AM
118:00
Time Left

50 ← → ⏪ ⏩ ⏴ ⏵ 📄

MADE BY WAQAR SIDDHU

Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

Write a subroutine to calculate the string length.

Answer (Please [click here](#) to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

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Time Left

51



MADE BY WAQAR SIDDHU

Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

Explain the purpose of Assembly language instructions from 1 to 4 lines as given below.

1. mov si, l2
2. mov al, [cs:si]
3. mov [opcode], al
4. mov byte [cs:si], 0xCC

Answer (Please [click here](#) to Add Answer)

Normal Arial 12 B I U

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118:00
Time Left

52



MADE BY WAQAR SIDDHU

Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

- 5. 12: inc ax
- 6. add bx, 2
- 7. dec cx
- 8. sub dx, 2
- 9. jmp 12

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for file operations, text formatting (bold, italic, underline), alignment, and zoom (100%).

Start Time: 9:42 AM
118:00
Time Left

52



MADE BY WAQAR SIDDHU

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In an indirect addressing mode the address of memory cell

Answer (Please select your correct option)

VuAnswers.com

is placed in a register

is directly accessed by instruction

is constant

is variable

Made by: Waqar Siddhu

In an indirect addressing mode the address of memory cell

Answer (Please select your correct option)

is placed in a register



is directly accessed by instruction



is constant



is variable



VuAnswers.com

Made by: Waqar Siddhu

In STOSB instruction, when DF is clear, SI is

Answer / Choose your correct option)

Incremented by 1

Incremented by 2

Decrement by 1

Decrement by 2

VuAnswers.com

Made by: Waqar Siddhu

IBM AT has how many PICs (Programmable interrupt controller)

Answer (Please select your correct option)

1

2

3

4

VuAnswers.com

Made by: Waqar Siddhu

Processor informs the memory that it wants to read data, using :

Answer (Please select your correct option)

- Address bus
- Data bus
- Control bus
- It informs without using any bus

VuAnswers.com

Made by: Waqar Siddhu

Memory cell width refers to _____

Answer (Please select your correct option)

Number of bits in a cell

The maximum value that can be stored in a cell including sign-bit

The maximum value that can be stored in a cell excluding sign-bit

Address of that cell

VuAnswers.com

Made by: Waqar Siddhu

In MUL instruction if the source operand is a byte then it is multiplied by register

Answer (Please select your correct option)

AX

AL

CX

DL

VuAnswers.com

Made by: Waqar Siddhu

In the word designated for one screen location, the higher address contains

Answer (Please select your correct option)

The character code

The attribute byte

The parameters

The dimensions

VuAnswers.com

Made by: Waqar Siddhu

"mov [bx], ax" moves the two bytes contents of the AX register to the address contained in BX register in the current

Answer (Please select your correct option)

VuAnswers.com

Stack segment

Code segment

Data segment

Extra segment

Made by: Waqar Siddhu

CALL instruction transfer the control

Answer (Please select your correct option)

- Permanently ~~X~~
- Temporarily
- Permanently when CALL is NEAR
- Permanently when CALL is FAR

VuAnswers.com

Made by: Waqar Siddhu

A 32 bit processor has an accumulator of _____

Answer (Please select your correct option)

8 bits

16 bits

32 bits

64 bits

VuAnswers.com

Made by: Waqar Siddhu

The parallel port connector is called _____.

Answer (Please select your correct option)

DB-25

BD-25

DB-24

BD-24

VuAnswers.com

Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of "PUSH AX" statement

Answer (Please select your correct option)

VuAnswers.com

- AX register will reside on the stack
- A copy of AX will go on the stack
- The value of AX disappear after moving on stack
- Stack will send an acceptance message

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

An End of Interrupt (EOI) signal is sent by

Answer (Please select your correct option)

VuAnswers.com

Handler

Processor

IRQ

PIC

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

VuAnswers.com

1.193MHZ

2.193MHZ

3.193MHZ

4.193MHZ

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

INT 2

INT 3

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

The actual physical address represented by the segment-offset pair "AB01:FFFF" will be:

Answer (Please select your correct option)

VuAnswers.com

1AB00



001AB



BB00F



F00BB



Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:
SHL DH, CL.

Answer (Please select your correct option)

VuAnswers.com

- 01010000
- 10001010
- 00010100
- 01010111

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

When two devices in the system want to use the same IRQ line then what will happen?

Answer (Please select your correct option)

VuAnswers.com

An IRQ Collision

An IRQ Conflict

An IRQ Crash

An IRQ Blockage

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of STOSW, the CX will be

Answer (Please select your correct option)

VuAnswers.com

Decrement by 1

Decrement by 2

Incremented by 1

Incremented by 2

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following is true ?

Answer (Please select your correct option)

VuAnswers.com

In general the memory cell cannot be wider than data bus

Control bus is unidirectional

Memory can use the address bus to refer to certain register in processor

There is no relation between memory and data bus

Made by: Waqar Siddhu

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

VuAnswers.com

Code Segment register

Data Segment register

Accumulator register

Stack segment register

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

Relating to the stack in 8088 architecture which of the following statement is true ?

Answer (Please select your correct option)

VuAnswers.com

- Single word can not be pushed on the stack
- Single word can not be popped from the stack
- Single byte can not be popped from the stack
- Single quad can be popped from the stack

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

The second byte of the word designated for one screen location holds

Answer (Please select your correct option)

VuAnswers.com

- The dimensions of the screen
- Character position on the screen
- Character color on the screen
- ASCII code of the character

Made by: Waqar Siddhu

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 10 is used for _____ services.

Answer (Please select your correct option)

VuAnswers.com

BIOS video

DOS video

RAM

Disk

Made by: Waqar Siddhu

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

VuAnswers.com

- line
- dot
- circle
- rectangle

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

The value of AH register in the write Graphics pixel service is _____

Answer (Please select your correct option)

VuAnswers.com

0Ch

0Bh

1Ch

2Ch

Made by: Waqar Siddhu

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

VuAnswers.com

control block

Process control block

stack

hard disk

Made by: Waqar Siddhu

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

Answer (Please select your correct option)

VuAnswers.com

a)

b)

c)

d)

Made by: Waqar Siddhu

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA VBE 2.0 is a standard for _____

Answer (Please select your correct option)

VuAnswers.com

Ultra resolution mode

High resolution mode

Medium resolution mode

Low resolution mode

Made by: Waqar Siddhu

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

VuAnswers.com

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

Made by: Waqar Siddhu

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

VuAnswers.com

Video BIOS Extensions

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

Made by: Waqar Siddhu

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

IDTR is a _____ register.

Answer (Please select your correct option)

VuAnswers.com

16 bit

20 bit

32 bit

48 bit

Made by: Waqar Siddhu

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

VuAnswers.com

Programming mode

Real time mode

Visualization mode

Memory mode

Made by: Waqar Siddhu

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

VuAnswers.com

First Byte



Second Byte



Third Byte



Last Byte



Made by: Waqar Siddhu

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

What is the size of a Sector of a standard 1.44MB Floppy disk having 80 tracks and 18 Sectors/track?

Answer (Please select your correct option)

VuAnswers.com

128 Bytes

256 Bytes

512 Bytes

1024 Bytes

Made by: Waqar Siddhu

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

VuAnswers.com

INT 10

INT 09

INT 08

INT 07

Made by: Waqar Siddhu

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

VuAnswers.com

global

local

legal

illegal

Made by: Waqar Siddhu

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

Technically IAPX88 stands for “_____ Advanced Processor Extensions 88.”

Answer (Please select your correct option)

VuAnswers.com

Intel

Integrated

Intra

Internal

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

In Left-Shift-Operation, the left most bit _____

Answer (Please select your correct option)

VuAnswers.com

- will be dropped
- will go into CF
- will be moved to the right-most position
- will always be 1

Made by: Waqar Siddhu

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

VuAnswers.com

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

Made by: Waqar Siddhu

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

VuAnswers.com

6



7



8



9



Made by: Waqar Siddhu

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CD (Carrier Detect) ?

Answer (Please select your correct option)

VuAnswers.com

1

2

3

4

Made by: Waqar Siddhu

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following BIOS INT provides serial port services

Answer (Please select your correct option)

VuAnswers.com

INT14

INT11

INT12

INT13

Made by: Waqar Siddhu

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

VuAnswers.com

16 bit processor

32 bit processor

64 bit processor

128 bit processor

Made by: Waqar Siddhu

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

A 32 bit address register can access up to of memory, so memory access has increased a lot.

Answer (Please select your correct option)

VuAnswers.com

2GB



4GB



6GB



8GB



Made by: Waqar Siddhu

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

VuAnswers.com

1

0

-1

-2

Made by: Waqar Siddhu

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

VuAnswers.com

8 bit

16 bit

32 bit

64 bit

Made by: Waqar Siddhu

In 68K processors there is a 32 bit that holds the address of currently executing instruction.

VuAnswers.com

Answer (Please select your correct option)

Program counter

Stack pointer

Register

Stack

Made by: Waqar Siddhu

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Programmable Interrupt Controller (PIC) has

Answer (Please select your correct option)

VuAnswers.com

- One input signals and eight output signals
- One input signal and one output signal
- Eight input signals and one output signals
- Eight input signals and eight output signals

Made by: Waqar Siddhu

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

VuAnswers.com

INT 0



INT 1



INT 2



INT 3



Made by: Waqar Siddhu

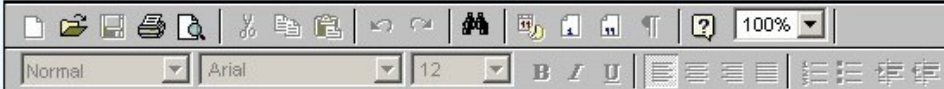
Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

For which purpose INT 1 is used and describe the functionality of this interrupt?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

What are different register settings for file closing using INT 21 service?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

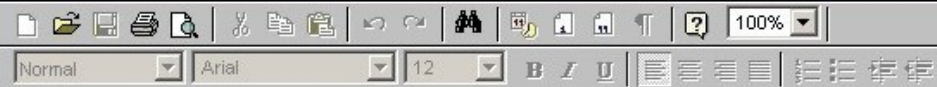
Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

In C language, what naming convention is used to call a function ?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



c prepends an underscore to
every one

Made by: Waqar Siddhu

Question No : 44 of 52

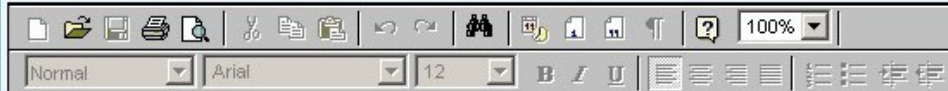
Marks: 2 (Budgeted Time 4 Min)

Write a single instruction that can be used as an alternative of the following two instructions?

```
DEC CX  
JNZ NextChar
```

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

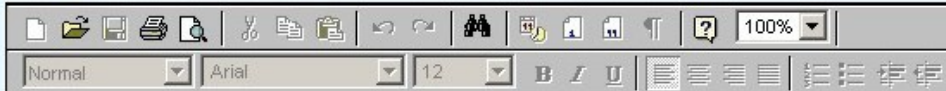
Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

Suppose we want to execute multiple processes at same time. For this purpose we need to store and restore states of processes running in a multitasking operating system. Which process is used to store and restore states of processes in a multitasking operating system.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

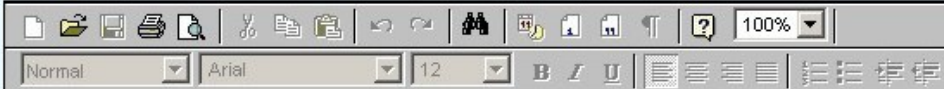
Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

Consider the function "**int subtract (int x, int y)**" declared in C, Write the code to call this function from Assembly language.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

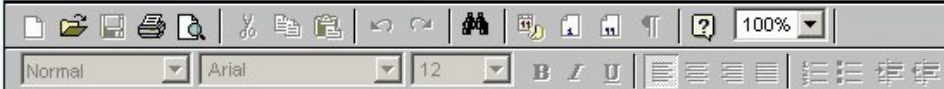
Question No : 47 of 52

Marks: 3 (Budgeted Time 6 Min)

For what purpose Global Descriptor Table (GDT) is used?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



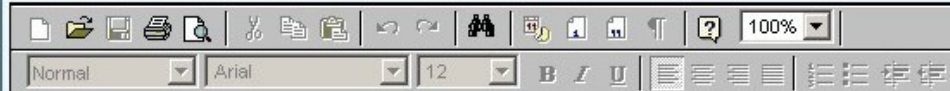
Made by: Waqar Siddhu

Check these instructions, whether they are valid or not, if not why? Make corrections and also mention how many bits have been moved in each instruction?

```
mov ax, [num1]
mov al, [num1]
mov ax, al
mov [num1], 5
```

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

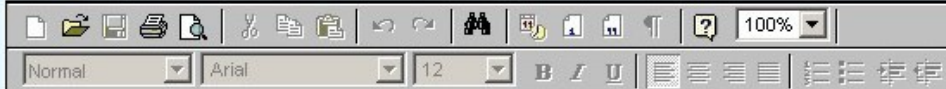
Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

```
mov ax, [num1]
mov al, [num1]
mov ax, al
mov [num1], 5
mov byte[num1], 5
```

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

CLR (A3)
EXG D0, D2
MOVE.B (A1), (A2)
MOVEA (2222).L, A4

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

What is the effective address generated by each of the following instruction?

Initially BX=0x0100, label=0x0234, [label]=0x0010, and SI=0x00E1

- a. `mov ax, [bx+40]`
- b. `mov ax, [bx+label]`

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

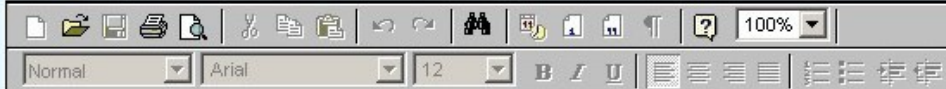
Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

In INT 10 of video services while writing graphics pixel, what do the values of registers BH, AL, CX and DX correspond to?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

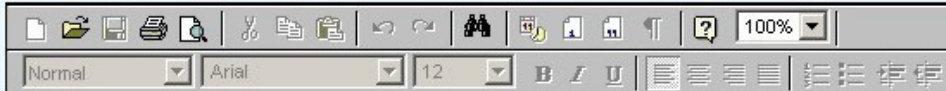
Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

What are different register settings for memory allocation using INT 21 Service?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 1 of 52 Marks: 1 (Budgeted Time 1 Min)

The process of sending signals back and forth is called

Answer (Please select your correct option)

- Activity
- Hand-shaking
- Interruption
- Time clicking

Start Time: 1:38 AM
113:00
Time Left

Question No : 2 of 52 Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

- IRQ 4
- IRQ 5
- IRQ 6
- IRQ 7

Start Time: 1:38 AM
113:00
Time Left

2



MADE BY WAQAR SIDDHU

Question No : 3 of 52 Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

- Key board interrupt
- Timer interrupt
- INT 2
- INT 3

Start Time: 1:38 AM
113:00
Time Left

3



MADE BY WAQAR SIDDHU

Question No : 4 of 52 Marks: 1 (Budgeted Time 1 Min)

Regarding assembler, which statement is true:

Answer (Please select your correct option)

- Assembler converts mnemonics to the corresponding OPCODE
- Assembler converts OPCODE to the corresponding mnemonics
- Assembler executes the assembly code all at once
- Assembler executes the assembly code step by step

Start Time: 1:38 AM
113:00
Time Left

4



MADE BY WAQAR SIDDHU

Question No : 5 of 52 Marks: 1 (Budgeted Time 1 Min)

CMPS instruction subtracts the source location to the destination location. Source location always lies in

Answer (Please select your correct option)

- ES:SI
- ES:DI
- DS:SI
- DS:DI

Start Time: 1:38 AM
113:00
Time Left

5



MADE BY WAQAR SIDDHU

Question No : 6 of 52 Marks: 1 (Budgeted Time 1 Min)

For the execution of the instruction "DIV BL", the implied dividend will be stored in

Answer (Please select your correct option)

- AX
- BX
- CX
- DX

Start Time: 1:38 AM
113:00
Time Left

6

MADE BY WAQAR SIDDHU

Question No : 7 of 52 Marks: 1 (Budgeted Time 1 Min)

When two devices in the system want to use the same IRQ line then what will happen?

Answer (Please select your correct option)

- An IRQ Collision
- An IRQ Conflict
- An IRQ Crash
- An IRQ Blockage

Start Time: 1:38 AM
113:00
Time Left

7

MADE BY WAQAR SIDDHU

Question No : 8 of 52 Marks: 1 (Budgeted Time 1 Min)

The memory address always move from _____

Answer (Please select your correct option)

- processor to memory
- memory to processor
- memory to peripheral
- peripheral to processor

Start Time: 1:38 AM
113:00
Time Left

8



MADE BY WAQAR SIDDHU

Question No : 9 of 52 Marks: 1 (Budgeted Time 1 Min)

After the execution of following two instructions, what will happen ?

```
PUSH AX  
POP DX
```

Answer (Please select your correct option)

- The value of DX will be at the top of stack
- The value of AX will be at the top of stack
- The value of DX will be copied into AX
- The value of AX will be copied into DX

Start Time: 1:38 AM
113:00
Time Left

9

MADE BY WAQAR SIDDHU

Question No : 10 of 52 Marks: 1 (Budgeted Time 1 Min)

The second byte of the word designated for one screen location holds

Answer (Please select your correct option)

- The dimensions of the screen
- Character position on the screen
- Character color on the screen
- ASCII code of the character

Start Time: 1:38 AM
113:00
Time Left

10



MADE BY WAQAR SIDDHU

Question No : 11 of 52 Marks: 1 (Budgeted Time 1 Min)

In Shift instructions the destination can be

Answer (Please select your correct option)

- An 8-bit register only
- A register only
- A memory location only
- A register or memory location

Start Time: 1:38 AM
113:00
Time Left

11



MADE BY WAQAR SIDDHU

Question No : 12 of 52 Marks: 1 (Budgeted Time 1 Min)

INT 10 is used for _____ services.

Answer (Please select your correct option)

- BIOS video
- DOS video
- RAM
- Disk

Start Time: 1:38 AM
113:00
Time Left

12



MADE BY WAQAR SIDDHU

Question No : 13 of 52 Marks: 1 (Budgeted Time 1 Min)

In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

- line
- dot
- circle
- rectangle

Start Time: 1:38 AM
113:00
Time Left

13



MADE BY WAQAR SIDDHU

Question No : 14 of 52 Marks: 1 (Budgeted Time 1 Min)

A thread can not be created _____

Answer (Please select your correct option)

- in static way
- in dynamic way
- in static and dynamic way at the same time
- manually

Start Time: 1:38 AM
113:00
Time Left

14



MADE BY WAQAR SIDDHU

Question No : 15 of 52 Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

- control block
- Process control block
- stack
- hard disk

Start Time: 1:38 AM
113:00
Time Left

15



MADE BY WAQAR SIDDHU

Question No : 16 of 52 Marks: 1 (Budgeted Time 1 Min)

VESA (an organization) organizes 16 color bits for every pixel in _____ format.

Answer (Please select your correct option)

- 5:5:5
- 5:6:5
- 5:5:6
- 5:6:6

Start Time: 1:38 AM
113:00
Time Left

16



MADE BY WAQAR SIDDHU

Question No : 17 of 52 Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

- Variable Electronics Standards Association
- Video Electronics Standards Association
- Video Energy Standards Association
- Vega Energy Standards Association

Start Time: 1:38 AM
113:00
Time Left

17



MADE BY WAQAR SIDDHU

Question No : 18 of 52 Marks: 1 (Budgeted Time 1 Min)

The Physical address of Interrupt Descriptor Table (IVT) is stored in _____

Answer (Please select your correct option)

- GDTR
- LGTD
- IDTR
- IVT

Start Time: 1:38 AM
113:00
Time Left

18



MADE BY WAQAR SIDDHU

Question No : 19 of 52 Marks: 1 (Budgeted Time 1 Min)

IDTR is a _____ register.

Answer (Please select your correct option)

- 16 bit
- 20 bit
- 32 bit
- 48 bit

Start Time: 1:38 AM
113:00
Time Left

19



MADE BY WAQAR SIDDHU

Question No : 20 of 52 Marks: 1 (Budgeted Time 1 Min)

In Motorola 68K processors, _____ registers can hold addresses in indirect memory accesses.

Answer (Please select your correct option)

- A0-A7
- B0-B7
- C0-C7
- D0-D7

Start Time: 1:38 AM
113:00
Time Left

20



MADE BY WAQAR SIDDHU

Question No : 21 of 52 Marks: 1 (Budgeted Time 1 Min)

Motorola 68K processors have _____ 32 bit general purpose registers.

Answer (Please select your correct option)

- 32
- 64
- 8
- 16

Start Time: 1:38 AM
113:00
Time Left

21



MADE BY WAQAR SIDDHU

Question No : 22 of 52 Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, RISC stands for _____

Answer (Please select your correct option)

- Reversed Instruction Set Computer
- Reversed Instruction System Computer
- Reduced Instruction Set Computer
- Reduced Instruction System Computer

Start Time: 1:38 AM
113:00
Time Left

22



MADE BY WAQAR SIDDHU

Question No : 23 of 52 Marks: 1 (Budgeted Time 1 Min)

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

- INT 10
- INT 09
- INT 08
- INT 07

Start Time: 1:38 AM
113:00
Time Left

23



MADE BY WAQAR SIDDHU

Question No : 24 of 52 Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

- global
- local
- legal
- illegal

Start Time: 1:38 AM
113:00
Time Left

24



MADE BY WAQAR SIDDHU

Question No : 25 of 52 Marks: 1 (Budgeted Time 1 Min)

In multi-tasking programming, all threads are

Answer (Please select your correct option)

- Independent to each other
- Dependent on each other
- Not executed
- Not scheduled

Start Time: 1:38 AM
113:00
Time Left

25



MADE BY WAQAR SIDDHU

Question No : 26 of 52 Marks: 1 (Budgeted Time 1 Min)

“Far” jump makes intra _____ control possible

Answer (Please select your correct option)

- Register
- Segment
- Memory
- Code

Start Time: 1:38 AM
113:00
Time Left

26



MADE BY WAQAR SIDDHU

Question No : 27 of 52 Marks: 1 (Budgeted Time 1 Min)

All possible types of Jumps in assembly language are:

Answer (Please select your correct option)

- short, far
- short, near
- short, near, far
- short, near, far, long

Start Time: 1:38 AM
113:00
Time Left

27



MADE BY WAQAR SIDDHU

Question No : 28 of 52 Marks: 1 (Budgeted Time 1 Min)

BIOS sees the disks as

Answer (Please select your correct option)

- logical storage device
- raw storage device
- in the form of sectors only
- in the form of tracks only

Start Time: 1:38 AM
113:00
Time Left

28



MADE BY WAQAR SIDDHU

Question No : 29 of 52 Marks: 1 (Budgeted Time 1 Min)

Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

- DOS
- RAM
- ROM
- BIOS

Start Time: 1:38 AM
113:00
Time Left

29



MADE BY WAQAR SIDDHU

Question No : 30 of 52 Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

Start Time: 1:38 AM
113:00
Time Left

30



MADE BY WAQAR SIDDHU

Question No : 31 of 52 Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to DTR (Data Terminal Ready) ?

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

Start Time: 1:38 AM
113:00
Time Left

31



MADE BY WAQAR SIDDHU

Question No : 32 of 52 Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____

Answer (Please select your correct option)

- 7
- 8
- 9
- 6

Start Time: 1:38 AM
113:00
Time Left

32



MADE BY WAQAR SIDDHU

Question No : 33 of 52 Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

- 16 bit processor
- 32 bit processor
- 64 bit processor
- 128 bit processor

Start Time: 1:38 AM
113:00
Time Left

33



MADE BY WAQAR SIDDHU

Question No : 34 of 52 Marks: 1 (Budgeted Time 1 Min)

A 32 bit address register can access up to of memory, so memory access has increased a lot.

Answer (Please select your correct option)

- 2GB
- 4GB
- 6GB
- 8GB

Start Time: 1:38 AM
113:00
Time Left

34



MADE BY WAQAR SIDDHU

Question No : 35 of 52 Marks: 1 (Budgeted Time 1 Min)

AX register in extended 32 bit register is renamed as _____

Answer (Please select your correct option)

- AXE
- EAX
- AAX
- XAX

Start Time: 1:38 AM
113:00
Time Left

35



MADE BY WAQAR SIDDHU

Question No : 36 of 52 Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

- 1
- 0
- 1
- 2

Start Time: 1:38 AM
113:00
Time Left

36



MADE BY WAQAR SIDDHU

Question No : 37 of 52 Marks: 1 (Budgeted Time 1 Min)

The segment address of interrupt n will be at

Answer (Please select your correct option)

- nx4
- nx4+1
- nx4+2
- nx4+3

Start Time: 1:38 AM
113:00
Time Left

37



MADE BY WAQAR SIDDHU

Question No : 38 of 52 Marks: 1 (Budgeted Time 1 Min)

Programmable Interrupt Controller (PIC) has

Answer (Please select your correct option)

- One input signals and eight output signals
- One input signal and one output signal
- Eight input signals and one output signals
- Eight input signals and eight output signals

Start Time: 1:38 AM
113:00
Time Left

38



MADE BY WAQAR SIDDHU

Question No : 39 of 52 Marks: 1 (Budgeted Time 1 Min)

_____ number of pin(s) of a processor is/are used by the external hardware to generate an interrupt.

Answer (Please select your correct option)

- 1
- 2
- 3
- 4

Start Time: 1:38 AM
112:00
Time Left

39



MADE BY WAQAR SIDDHU

Question No : 40 of 52 Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, Port _____ is referred as a Control port.

Answer (Please select your correct option)

- 19
- 20
- 21
- 22

Start Time: 1:38 AM
112:00
Time Left

40



MADE BY WAQAR SIDDHU

Question No : 41 of 52 Marks: 2 (Budgeted Time 4 Min)

How the data is transferred in serial communication?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

41

Navigation controls including an information icon, back, forward, and search buttons.

MADE BY WAQAR SIDDHU

Question No : 42 of 52 Marks: 2 (Budgeted Time 4 Min)

How to identify maximum possible speed of multitasking?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

42



MADE BY WAQAR SIDDHU

Question No : 43 of 52 Marks: 2 (Budgeted Time 4 Min)

In what order, the parameters are passed to routines in Pascal language?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

43

Navigation controls including an information icon, back, forward, and search buttons.

MADE BY WAQAR SIDDHU

Question No : 44 of 52 Marks: 2 (Budgeted Time 4 Min)

“Interrupts are asynchronous” What does it mean?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

44



MADE BY WAQAR SIDDHU

Question No : 45 of 52 Marks: 3 (Budgeted Time 6 Min)

In context of Disk media, how many bytes are reserved for each of the following parts in "32 bytes directory format"?

- Extension
- Time
- Date

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

45



MADE BY WAQAR SIDDHU

Question No : 46 of 52 Marks: 3 (Budgeted Time 6 Min)

Write the functionality of following bits in IDTR

- P
- DPL
- S

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

46 i ⏪ ⏩ ⏴ ⏵

MADE BY WAQAR SIDDHU

Question No : 47 of 52 Marks: 3 (Budgeted Time 6 Min)

Give the value of the zero flag, the carry flag, and the sign flag after each of the following instructions if AX is initialized with 0x1254 and BX is initialized with 0x0FFF.

- a. add ax, 0xEDAB
- b. add ax, bx
- c. add bx, 0xF001

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and others. The font is set to Arial, size 12.

Start Time: 1:38 AM
112:00
Time Left

47



MADE BY WAQAR SIDDHU

Question No : 48 of 52 Marks: 3 (Budgeted Time 6 Min)

How many flags that are not used in mathematical operations and also mention their names?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

48



MADE BY WAQAR SIDDHU

Question No : 49 of 52 Marks: 5 (Budgeted Time 10 Min)

How many types of interrupt are used for writing debugger and also elaborate functionality of these interrupts with proper explanation?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and others. The font is set to Arial, size 12. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

Navigation controls including a page number '49', a search icon, and navigation buttons for back, forward, and home.

MADE BY WAQAR SIDDHU

Question No : 50 of 52 Marks: 5 (Budgeted Time 10 Min)

Calculate first and last physical address accessible by the following segment values:
a. 0001
b. E000

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

Navigation controls including a page number '50', a progress slider, and buttons for Home, Previous, Next, and End.

MADE BY WAQAR SIDDHU

Question No : 51 of 52 Marks: 5 (Budgeted Time 10 Min)

Which interrupt is used to write Disk System and how can we write this system? Also mention (any four) attributes which are used in this service.

Answer (Please click here to Add Answer)

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

Start Time: 1:38 AM
112:00
Time Left

Navigation controls including a page number '51', a progress slider, and buttons for Home, Previous, Next, and End.

MADE BY WAQAR SIDDHU

Question No : 52 of 52 Marks: 5 (Budgeted Time 10 Min)

Consider the function "int multiply (int x, int y);" declared in C, Write the code to call this function from Assembly language.

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and others. The font is set to Arial, size 12. The text area is currently empty.

Start Time: 1:38 AM
112:00
Time Left

52 i ⏪ ⏩ ⏴ ⏵

MADE BY WAQAR SIDDHU

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

The process of sending signals back and forth is called

Answer (Please select your correct option)

VuAnswers.com

Activity

Hand-shaking

Interruption

Time clicking

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

INT 2

INT 3

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

Regarding assembler, which statement is true:

Answer (Please select your correct option)

VuAnswers.com

Assembler converts mnemonics to the corresponding OPCODE

Assembler converts OPCODE to the corresponding mnemonics

Assembler executes the assembly code all at once

Assembler executes the assembly code step by step

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

CMPS instruction subtracts the source location to the destination location. Source location always lies in

Answer (Please select your correct option)

VuAnswers.com

ES:SI

~~ES:DI~~

DS:SI

DS:DI

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

For the execution of the instruction "DIV BL", the implied dividend will be stored in

Answer (Please select your correct option)

VuAnswers.com

AX

BX

CX

DX

Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

When two devices in the system want to use the same IRQ line then what will happen?

Answer (Please select your correct option)

VuAnswers.com

An IRQ Collision

An IRQ Conflict

An IRQ Crash

An IRQ Blockage

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

The memory address always move from _____

Answer (Please select your correct option)

VuAnswers.com

- processor to memory
- memory to processor
- memory to peripheral
- peripheral to processor

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of following two instructions, what will happen ?

```
PUSH AX  
POP DX
```

Answer (Please select your correct option)

VuAnswers.com

- The value of DX will be at the top of stack
- The value of AX will be at the top of stack
- The value of DX will be copied into AX
- The value of AX will be copied into DX

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

The second byte of the word designated for one screen location holds

Answer (Please select your correct option)

VuAnswers.com

The dimensions of the screen

Character position on the screen

Character color on the screen

ASCII code of the character

Made by: Waqar Siddhu

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

In Shift instructions the destination can be

Answer (Please select your correct option)

VuAnswers.com

- An 8-bit register only
- A register only
- A memory location only
- A register or memory location

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 10 is used for _____ services.

Answer (Please select your correct option)

VuAnswers.com

BIOS video

DOS video

RAM

Disk

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

VuAnswers.com

line

dot

circle

rectangle

Made by: Waqar Siddhu

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

A thread can not be created _____

Answer (Please select your correct option)

VuAnswers.com

- in static way
- in dynamic way
- in static and dynamic way at the same time
- manually

Made by: Waqar Siddhu

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

VuAnswers.com

control block

Process control block

stack

hard disk

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA (an organization) organizes 16 color bits for every pixel in _____ format.

Answer (Please select your correct option)

VuAnswers.com

5.5:5

5.6:5

5.5:6

5.6:6

Made by: Waqar Siddhu

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

VuAnswers.com

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

Made by: Waqar Siddhu

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

The Physical address of Interrupt Descriptor Table (IVT) is stored in _____

Answer (Please select your correct option)

VuAnswers.com

GDTR

LGTD

IDTR

IVT

Made by: Waqar Siddhu

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

IDTR is a _____ register.

Answer (Please select your correct option)

VuAnswers.com

16 bit

20 bit

32 bit

48 bit

Made by: Waqar Siddhu

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

In Motorola 68K processors, _____ registers can hold addresses in indirect memory accesses.

Answer (Please select your correct option)

VuAnswers.com

A0-A7

B0-B7

C0-C7

D0-D7

Made by: Waqar Siddhu

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

Motorola 68K processors have _____ 32 bit general purpose registers.

Answer (Please select your correct option)

VuAnswers.com

32

64

8

16

Made by: Waqar Siddhu

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, RISC stands for _____

Answer (Please select your correct option)

VuAnswers.com

- Reversed Instruction Set Computer
- Reversed Instruction System Computer
- Reduced Instruction Set Computer
- Reduced Instruction System Computer

Made by: Waqar Siddhu

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

VuAnswers.com

INT 10

INT 09

INT 08

INT 07

Made by: Waqar Siddhu

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

VuAnswers.com

global

local

legal

illegal

Made by: Waqar Siddhu

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking programming, all threads are

Answer (Please select your correct option)

VuAnswers.com

Independent to each other

Dependent on each other

Not executed

Not scheduled

Made by: Waqar Siddhu

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

“Far” jump makes intra _____ control possible

Answer (Please select your correct option)

VuAnswers.com

Register

Segment

Memory

Code

Made by: Waqar Siddhu

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

All possible types of Jumps in assembly language are:

Answer (Please select your correct option)

VuAnswers.com

short, far

short, near

short, near, far

short, near, far, long

Made by: Waqar Siddhu

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

BIOS sees the disks as

Answer (Please select your correct option)

VuAnswers.com

- logical storage device
- raw storage device
- in the form of sectors only
- in the form of tracks only

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

VuAnswers.com

DOS

RAM

ROM

BIOS

Made by: Waqar Siddhu

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer (Please select your correct option)

VuAnswers.com

1



2



3



4



Made by: Waqar Siddhu

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to DTR (Data Terminal Ready) ?

Answer (Please select your correct option)

VuAnswers.com

1

2

3

4

Made by: Waqar Siddhu

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____

Answer (Please select your correct option)

VuAnswers.com

7

8

9

6

Made by: Waqar Siddhu

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

VuAnswers.com

16 bit processor

32 bit processor

64 bit processor

128 bit processor

Made by: Waqar Siddhu

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

A 32 bit address register can access up to of memory, so memory access has increased a lot.

Answer (Please select your correct option)

VuAnswers.com

2GB

4GB

6GB

8GB

Made by: Waqar Siddhu

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

AX register in extended 32 bit register is renamed as _____

Answer (Please select your correct option)

VuAnswers.com

AXE

EAX

AAX

XAX

Made by: Waqar Siddhu

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

VuAnswers.com

1

0

-1

-2

Made by: Waqar Siddhu

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

The segment address of interrupt n will be at

Answer (Please select your correct option)

VuAnswers.com

nx4

nx4+1

nx4+2

+

nx4+3

Made by: Waqar Siddhu

Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

Programmable Interrupt Controller (PIC) has

Answer (Please select your correct option)

VuAnswers.com

- One input signals and eight output signals
- One input signal and one output signal
- Eight input signals and one output signals
- Eight input signals and eight output signals

Made by: Waqar Siddhu

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ number of pin(s) of a processor is/are used by the external hardware to generate an interrupt.

Answer (Please select your correct option)

VuAnswers.com

1

2

3

4

Made by: Waqar Siddhu

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, Port _____ is referred as a Control port.

Answer (Please select your correct option)

VuAnswers.com

19

20

21

22

Made by: Waqar Siddhu

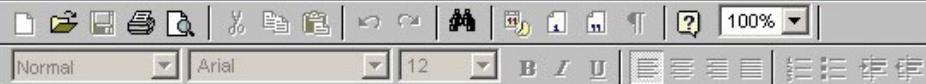
Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

How the data is transferred in serial communication?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

How to identify maximum possible speed of multitasking?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

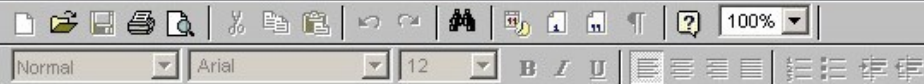
Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

In what order, the parameters are passed to routines in Pascal language?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

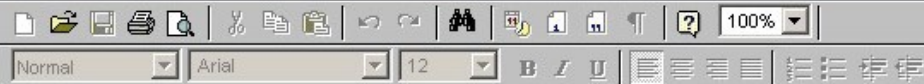
Question No : 44 of 52

Marks: 2 (Budgeted Time 4 Min)

“Interrupts are asynchronous” What does it mean?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



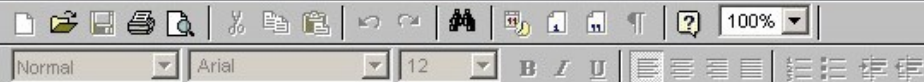
Made by: Waqar Siddhu

In context of Disk media, how many bytes are reserved for each of the following parts in “32 bytes directory format”?

- Extension
- Time
- Date

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 46 of 52

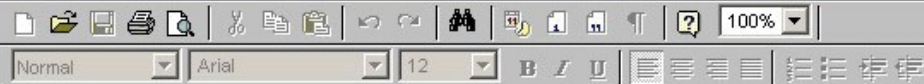
Marks: 3 (Budgeted Time 6 Min)

Write the functionality of following bits in IDTR

- P
- DPL
- S

Answer ([Please click here to Add Answer](#))

VuAnswers.com



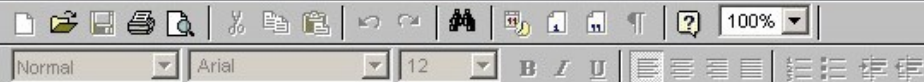
Made by: Waqar Siddhu

Give the value of the zero flag, the carry flag, and the sign flag after each of the following instructions if AX is initialized with 0x1254 and BX is initialized with 0x0FFF.

- add ax, 0xEDAB
- add ax, bx
- add bx, 0xF001

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

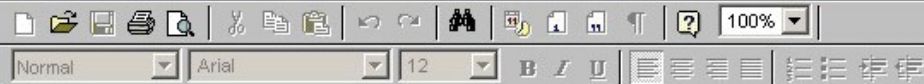
Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

How many flags that are not used in mathematical operations and also mention their names?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

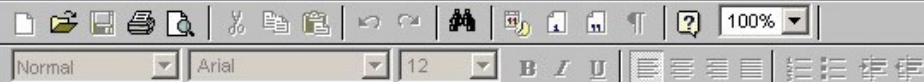
Question No : 49 of 52

Marks: 5 (Budgeted Time 10 Min)

How many types of interrupt are used for writing debugger and also elaborate functionality of these interrupts with proper explanation?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



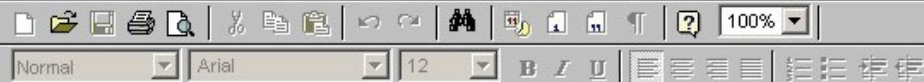
Made by: Waqar Siddhu

Calculate first and last physical address accessible by the following segment values:

- a. 0001
- b. E000

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

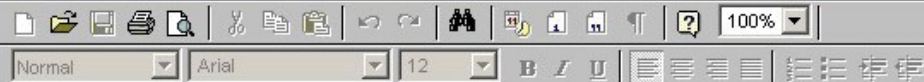
Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

Which interrupt is used to write Disk System and how can we write this system? Also mention (any four) attributes which are used in this service.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

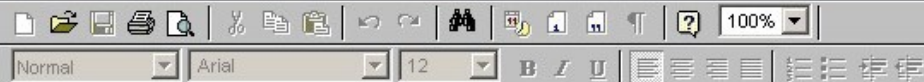
Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

Consider the function “**int multiply (int x, int y);**” declared in C, Write the code to call this function from Assembly language.

Answer (Please [click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSW instruction, when DF is clear, SI is

Answer (Please select your correct option)

VuAnswers.com

Incremented by 1

Incremented by 2

correct

Decrement by 1

Decrement by 2

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

correct

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is used by the parallel port?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

IRQ 5

IRQ 6

IRQ 7

correct

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PTI) has input frequency of

Answer (Please select your correct option)

VuAnswers.com

1.193MHZ

correct

2.193MHZ

3.193MHZ

4.193MHZ

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

correct

INT 2

INT 3

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer (Please select your correct option)

VuAnswers.com

Shl AX, 1

Shr AX, 1

correct

NOT AX

AND AX, 0xFFFF

Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

In the rotate right operation every bit moves one position to the right and the bit dropped from the right is inserted at the left and:

Answer (Please select your correct option)

VuAnswers.com

The dropped bit is also copied in CF

correct

The dropped bit is also copied in AF

The dropped bit is also copied in ZF

The dropped bit is not copied in any flag

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

In string instructions, CX is always

Answer (Please select your correct option)

VuAnswers.com

Decremented by 1

Decremented by 2

Incremented by 1

correct

Incremented by 2

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

The word size of the processor is defined by the size of _____

Answer (Please select your correct option)

VuAnswers.com

Its accumulator register

Address bus attached

~~X~~ correct

Memory attached

Control Bus

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ cannot predict which element the _____ needs, at a particular instant of time.

Answer (Please select your correct option)

VuAnswers.com

Control bus, Memory

Processor, Control bus
 Processor, Memory

Memory, Processor

correct

Control bus, Memory

Made by: Waqar Siddhu

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

The time interval between two timer ticks is _____

Answer (Please select your correct option)

VuAnswers.com

40ms

45ms

50ms

55ms

correct

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

Answer (Please select your correct option)

VuAnswers.com

third

fourth

first

second

correct

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

A thread can not be created _____

Answer (Please select your correct option)

VuAnswers.com

- in static way
- in dynamic way
- in static and dynamic way at the same time
- manually

correct

Made by: Waqar Siddhu

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

VuAnswers.com

control block

Process control block

correct

stack

hard disk

Made by: Waqar Siddhu

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

VuAnswers.com

Variable Electronics Standards Association

Video Electronics Standards Association

correct

Video Energy Standards Association

Vega Energy Standards Association

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

VuAnswers.com

Video BIOS Extensions

correct

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

Made by: Waqar Siddhu

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

VuAnswers.com

Programming mode

Real time mode

correct

Visualization mode

Memory mode

Made by: Waqar Siddhu

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

VuAnswers.com

1

2

correct

3

4

Made by: Waqar Siddhu

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

In Motorola 68K processors, _____ registers can hold addresses in indirect memory accesses.

Answer (Please select your correct option)

VuAnswers.com

A0-A7

correct

B0-B7

C0-C7

D0-D7

Made by: Waqar Siddhu

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

Motorola 68K processors have _____ 32 bit general purpose registers.

Answer (Please select your correct option)

VuAnswers.com

32

64

8

16

correct

Made by: Waqar Siddhu

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

VuAnswers.com

First Byte

Second Byte

correct

Third Byte

Last Byte

Made by: Waqar Siddhu

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

VuAnswers.com

global

local

correct

legal

illegal

Made by: Waqar Siddhu

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking programming, all threads are

Answer (Please select your correct option)

VuAnswers.com

Independent to each other

correct

Dependent on each other

Not executed

Not scheduled

Made by: Waqar Siddhu

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Video services are exported via

Answer (Please select your correct option)

VuAnswers.com

INT 7

INT 10

correct

INT 9

INT 8

Made by: Waqar Siddhu

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

The maximum memory, IAPX88 can access is _____.

Answer (Please select your correct option)

VuAnswers.com

1 KB

64 KB

1 MB

correct

64 MB

Made by: Waqar Siddhu

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In direct addressing the memory address is _____ and is given in the instruction.

Answer (Please select your correct option)

VuAnswers.com

Fixed

correct

Empty

A variable

A register

Made by: Waqar Siddhu

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Instruction "mov ax, bl" has a _____ problem.

Answer (Please select your correct option)

VuAnswers.com

size mismatch

correct

value mismatch

operand mismatch

memory mismatch

Made by: Waqar Siddhu

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

The jump "JC" is taken when

Answer (Please select your correct option)

VuAnswers.com

CF=1

correct

CF=0

ZF=0

ZF=1

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

DOS

RAM

ROM

BIOS

VuAnswers.com

Made by: Waqar Siddhu

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

VuAnswers.com

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

Made by: Waqar Siddhu

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer (Please select your correct option)

VuAnswers.com

5

6

7

correct

8

Made by: Waqar Siddhu

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

VuAnswers.com

6

7

8

correct

9

Made by: Waqar Siddhu

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____

Answer (Please select your correct option)

VuAnswers.com

6

7

8

9

correct

Made by: Waqar Siddhu

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

VuAnswers.com

- 16 bit processor
- 32 bit processor
- 64 bit processor
- 128 bit processor

Made by: Waqar Siddhu

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

AX register in extended 32 bit register is renamed as _____

Answer (Please select your correct option)

VuAnswers.com

AXE

EAX

AAX

XAX

Made by: Waqar Siddhu

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

VuAnswers.com

1

0

-1

-2

Made by: Waqar Siddhu

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

VuAnswers.com

8 bit

16 bit

32 bit

64 bit

Made by: Waqar Siddhu

Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

VuAnswers.com

2

4

8

16

Made by: Waqar Siddhu

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

VuAnswers.com

INT 0

INT 1

INT 2

INT 3

Made by: Waqar Siddhu

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

VuAnswers.com

19

20

21

22

Made by: Waqar Siddhu

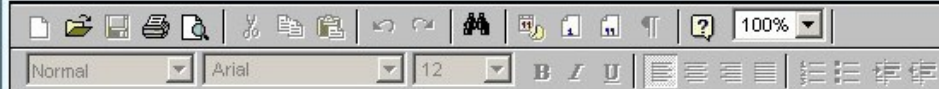
Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

Device drivers are operating system extensions which extend its services to new devices. Describe the structure of "Device Driver File" which is used for device management.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

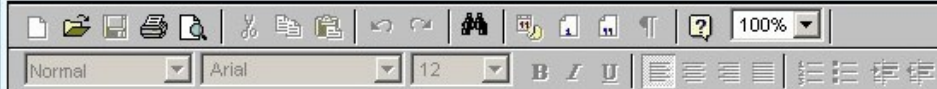
Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

With reference to branching technique used in assembly language programming, write any two control instructions.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

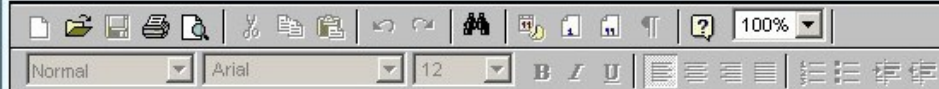
Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

How local thread variables are created and accessed ?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 44 of 52

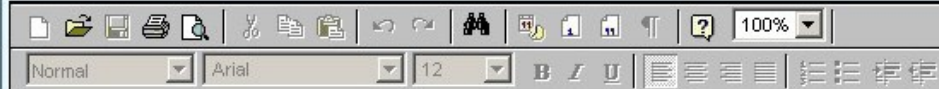
Marks: 2 (Budgeted Time 4 Min)

What are the following statements represent?

gdt: dd 0x00000000, 0x00000000

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

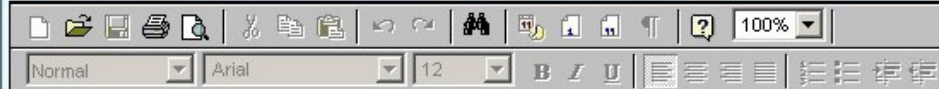
Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

What are the important "command codes" in request header of device driver?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

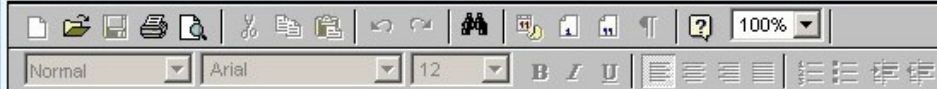
Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

In order to access Video memory in high resolution (more than a megabyte MB), what standard we have to use?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

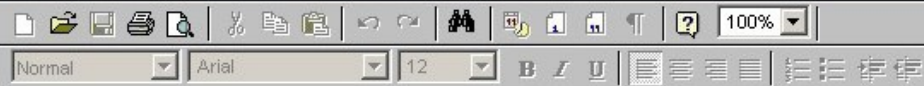
Question No : 47 of 52

Marks: 3 (Budgeted Time 6 Min)

Describe "Indexed Register Indirect + Offset" addressing mode with an example.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 48 of 52

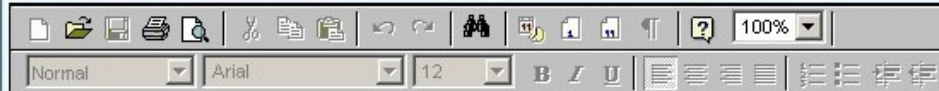
Marks: 3 (Budgeted Time 6 Min)

With reference to the multitasking program "TSR caller". Describe what task will be performed by each of these instructions?

- `mov al, [chars+bx]`
- `mov [es:40], al`
- `inc bx`

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

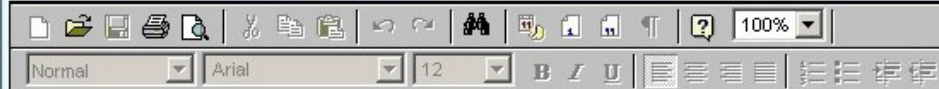
Question No : 49 of 52

Marks: 5 (Budgeted Time 10 Min)

How many types of interrupt are used for writing debugger and also elaborate functionality of these interrupts with proper explanation?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

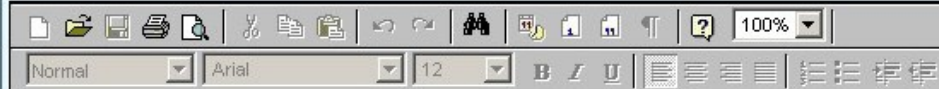
Question No : 50 of 52

Marks: 5 (Budgeted Time 10 Min)

What are the contents at the starting position of the file designated as device driver and mention important attributes of this driver file?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

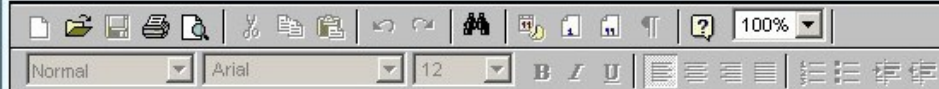
Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

At which location interrupt gate descriptor table is stored and describe the structure of this table with an example?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 52 of 52

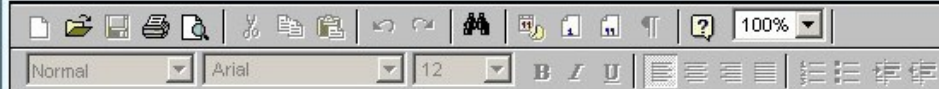
Marks: 5 (Budgeted Time 10 Min)

Give the new values of AX and DX for each of the following instructions, or write if the overflow occurs.

1. DIV BX, if DX contains 0000h, AX contains 0007h, and BX contains 0002h
- a. DIV BX, if DX contains 0000h, AX contains FFFFh, and BX contains 0010h

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of "PUSH AX" statement

Answer (Please select your correct option)

- AX register will reside on the stack
- A copy of AX will go on the stack
- The value of AX disappear after moving on stack
- Stack will send an acceptance message

Start Time: 7:01 PM
118:00
Time Left

1

MADE BY WAQAR SIDDHU

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSW instruction, when DF is clear, SI is

Answer (Please select your correct option)

Incremented by 1

Incremented by 2

correct

Decrement by 1

Decrement by 2

Start Time: 9:36 AM

120:00

Time Left



1



MADE BY WAQAR SIDDHU

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

- IRQ 4 **correct**
- IRQ 5
- IRQ 6
- IRQ 7

Start Time: 9:36 AM
119:00
Time Left

2



MADE BY WAQAR SIDDHU

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is used by the parallel port?

Answer (Please select your correct option)

IRQ 4

IRQ 5

IRQ 6

IRQ 7

correct

Start Time: 9:36 AM

119:00

Time Left



3



MADE BY WAQAR SIDDHU

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

- 1. 193MHZ **correct**
- 2. 193MHZ
- 3. 193MHZ
- 4. 193MHZ

Start Time: 9:36 AM
119:00
Time Left

4



MADE BY WAQAR SIDDHU

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

The actual physical address represented by the segment-offset pair "AB01:FFFF" will be:

Answer (Please select your correct option)

1AB00

001AB

BB00F

F00BB

Start Time: 7:01 PM

117:00

Time Left



6

MADE BY WAQAR SIDDHU

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

Key board interrupt

Timer interrupt

correct

INT 2

INT 3

Start Time: 9:36 AM

119:00

Time Left



5



MADE BY WAQAR SIDDHU

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer (Please select your correct option)

Shl AX , 1

Shr AX, 1

correct

NOT AX

AND AX, 0xFFFF

Start Time: 9:36 AM

119:00

Time Left



6



MADE BY WAQAR SIDDHU

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

In the rotate right operation every bit moves one position to the right and the bit dropped from the right is inserted at the left and:

Answer (Please select your correct option)

- The dropped bit is also copied in CF **correct**
- The dropped bit is also copied in AF
- The dropped bit is also copied in ZF
- The dropped bit is not copied in any flag

Start Time: 9:36 AM

119:00

Time Left



7



MADE BY WAQAR SIDDHU

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

In string instructions, CX is always

Answer (Please select your correct option)

- Decremented by 1
- Decremented by 2
- Incremented by 1 **correct**
- Incremented by 2

Start Time: 9:36 AM
119:00
Time Left

8



MADE BY WAQAR SIDDHU

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

The word size of the processor is defined by the size of _____

Answer (Please select your correct option)

Its accumulator register

Address bus attached

correct

Memory attached

Control Bus

Start Time: 9:36 AM

119:00

Time Left



9



MADE BY WAQAR SIDDHU

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ cannot predict which element the _____ needs, at a particular instant of time.

Answer (Please select your correct option)

- Control bus, Memory
- Processor, Control bus
- Processor, Memory
- Memory, Processor
- Control bus, Memory

correct

Start Time: 9:36 AM

119:00
Time Left

10



MADE BY WAQAR SIDDHU

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

The time interval between two timer ticks is ____

Answer (Please select your correct option)

40ms

45ms

50ms

55ms

correct

Start Time: 9:36 AM

119:00

Time Left



11



MADE BY WAQAR SIDDHU

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

Answer (Please select your correct option)

- third
- fourth
- first
- second **correct**

Start Time: 9:36 AM

119:00
Time Left

12



MADE BY WAQAR SIDDHU

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

A thread can not be created _____

Answer (Please select your correct option)

in static way

correct

in dynamic way

in static and dynamic way at the same time

manually

Start Time: 9:36 AM

119:00

Time Left



13



MADE BY WAQAR SIDDHU

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

control block

Process control block

correct

stack

hard disk

Start Time: 9:36 AM

119:00

Time Left



14



MADE BY WAQAR SIDDHU

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

Variable Electronics Standards Association

Video Electronics Standards Association

correct

Video Energy Standards Association

Vega Energy Standards Association

Start Time: 9:36 AM

119:00
Time Left

15



MADE BY WAQAR SIDDHU

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

Video BIOS Extensions

correct

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

Start Time: 9:36 AM

119:00

Time Left



16



MADE BY WAQAR SIDDHU

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

Programming mode

Real time mode

correct

Visualization mode

Memory mode

Start Time: 9:36 AM

119:00

Time Left



17



MADE BY WAQAR SIDDHU

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

- 1
- 2 **correct**
- 3
- 4

Start Time: 9:36 AM
119:00
Time Left

18



MADE BY WAQAR SIDDHU

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

In Motorola 68K processors, _____ registers can hold addresses in indirect memory accesses.

Answer (Please select your correct option)

A0-A7

correct

B0-B7

C0-C7

D0-D7

Start Time: 9:36 AM

118:00

Time Left



19



MADE BY WAQAR SIDDHU

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

Motorola 68K processors have _____ 32 bit general purpose registers.

Answer (Please select your correct option)

- 32
- 64
- 8
- 16 correct

Start Time: 9:36 AM
118:00
Time Left

20



MADE BY WAQAR SIDDHU

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

- First Byte
- Second Byte **correct**
- Third Byte
- Last Byte

Start Time: 9:36 AM
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Time Left

21



MADE BY WAQAR SIDDHU

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

- global
- local correct
- legal
- illegal

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Time Left

22



MADE BY WAQAR SIDDHU

Question No : 23 of 52 Marks: 1 (Budgeted Time 1 Min)

In multi-tasking programming, all threads are

Answer (Please select your correct option)

- Independent to each other **correct**
- Dependent on each other
- Not executed
- Not scheduled

Start Time: 9:36 AM
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Time Left

23



MADE BY WAQAR SIDDHU

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Video services are exported via

Answer (Please select your correct option)

- INT 7
- INT 10 **correct**
- INT 9
- INT 8

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24



MADE BY WAQAR SIDDHU

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

The maximum memory, LAPX88 can access is _____.

Answer (Please select your correct option)

- 1 KB
- 64 KB
- 1 MB **correct**
- 64 MB

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25



MADE BY WAQAR SIDDHU

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In direct addressing the memory address is _____ and is given in the instruction.

Answer (Please select your correct option)

- Fixed correct
- Empty
- A variable
- A register

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Time Left

26



MADE BY WAQAR SIDDHU

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Instruction "mov ax, bf" has a _____ problem.

Answer (Please select your correct option)

size mismatch

correct

value mismatch

operand mismatch

memory mismatch

Start Time: 9:36 AM

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Time Left



27



MADE BY WAQAR SIDDHU

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

The jump "JC" is taken when

Answer (Please select your correct option)

CF=1

correct

CF=0

ZF=0

ZF=1

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28



MADE BY WAQAR SIDDHU

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

- DOS
- RAM
- ROM
- BIOS

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29



MADE BY WAQAR SIDDHU

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

- 9 pin DB-9
- 8 pin DB-9
- 3 pin DB-9
- 9 pin DB-5

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30



MADE BY WAQAR SIDDHU

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer (Please select your correct option)

- 5
- 6
- 7 **correct**
- 8

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31



MADE BY WAQAR SIDDHU

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

- 6
- 7
- 8 **correct**
- 9

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32



MADE BY WAQAR SIDDHU

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____

Answer (Please select your correct option)

- 6
- 7
- 8
- 9 correct

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Time Left

33



MADE BY WAQAR SIDDHU

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

- 16 bit processor
- 32 bit processor
- 64 bit processor
- 128 bit processor

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34



MADE BY WAQAR SIDDHU

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

AX register in extended 32 bit register is renamed as _____

Answer (Please select your correct option)

AXE

EAX

AAX

XAX

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35



MADE BY WAQAR SIDDHU

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

- 1
- 0
- 1
- 2

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36



MADE BY WAQAR SIDDHU

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

- 8 bit
- 16 bit
- 32 bit
- 64 bit

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37



MADE BY WAQAR SIDDHU

Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

- 2
- 4
- 8
- 16

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MADE BY WAQAR SIDDHU

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

- INT 0
- INT 1
- INT 2
- INT 3

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MADE BY WAQAR SIDDHU

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

- 19
- 20
- 21
- 22

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40



MADE BY WAQAR SIDDHU

Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

Device drivers are operating system extensions which extend its services to new devices. Describe the structure of "Device Driver File" which is used for device management.

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for Bold, Italic, Underline, Bulleted List, Numbered List, Indent, Outdent, Undo, Redo, and a 100% zoom level. The text area below is empty.

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41

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

With reference to branching technique used in assembly language programming, write any two control instructions.

Answer (Please [click here](#) to Add Answer)

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42

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

How local thread variables are created and accessed ?

Answer ([Please click here to Add Answer](#))

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43



MADE BY WAQAR SIDDHU

Question No : 44 of 52

Marks: 2 (Budgeted Time 4 Min)

What are the following statements represent?

gdt: dd 0x00000000, 0x00000000

Answer (Please [click here](#) to Add Answer)

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44



MADE BY WAQAR SIDDHU

Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

What are the important "command codes" in request header of device driver?

Answer (Please [click here](#) to Add Answer)

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45



MADE BY WAQAR SIDDHU

Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

In order to access Video memory in high resolution (more than a megabyte MB), what standard we have to use?

Answer (Please [click here](#) to Add Answer)

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46

Navigation controls including an information icon, back, forward, and search buttons.

MADE BY WAQAR SIDDHU

Question No : 47 of 52

Marks: 3 (Budgeted Time 6 Min)

Describe "Indexed Register Indirect + Offset" addressing mode with an example.

Answer (Please [click here](#) to Add Answer)

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47

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

With reference to the multitasking program "TSR caller". Describe what task will be performed by each of these instructions?

- `mov al, [chars+bx]`
- `mov [es:40], al`
- `inc bx`

Answer ([Please click here to Add Answer](#))

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48

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 49 of 52 Marks: 5 (Budgeted Time 10 Min)

How many types of interrupt are used for writing debugger and also elaborate functionality of these interrupts with proper explanation?

Answer ([Please click here to Add Answer](#))

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49



MADE BY WAQAR SIDDHU

Question No : 50 of 52

Marks: 5 (Budgeted Time 10 Min)

What are the contents at the starting position of the file designated as device driver and mention important attributes of this driver file?

Answer ([Please click here to Add Answer](#))

Rich text editor toolbar with icons for undo, redo, bold, italic, underline, bulleted list, numbered list, link, unlink, insert image, insert table, and a 100% zoom level. The text area below is empty.

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50



MADE BY WAQAR SIDDHU

Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

At which location interrupt gate descriptor table is stored and describe the structure of this table with an example?

Answer (Please [click here](#) to Add Answer)

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51

Navigation icons: Information, Previous, Home, Next, Print.

MADE BY WAQAR SIDDHU

Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

Give the new values of AX and DX for each of the following instructions, or write if the overflow occurs.

- \. DIV BX, if DX contains 0000h, AX contains 0007h, and BX contains 0002h
- a. DIV BX, if DX contains 0000h, AX contains FFFFh, and BX contains 0010h

Answer ([Please click here to Add Answer](#))

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52

Navigation icons: Information, Previous, Home, Next, Print

MADE BY WAQAR SIDDHU

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

The operand of PUSH is called

Answer (Please select your correct option)

VuAnswers.com

Source operand

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Destination operand

Stack operand

Data operand

Made by: Waqar Siddhu

The operand of PUSH is called

Answer (Please select your correct option)

Source operand

Destination operand

Stack operand

Data operand

VuAnswers.com

Made by: Waqar Siddhu

The clear screen operation initializes whole block of video memory to

Answer (Please select your correct option)

0417

0741

0714

0174

VuAnswers.com

Correct answer solved by Hadi
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Cell : 03228043306

Made by: Waqar Siddhu

Which of the following IRQs is connected to serial port COM 2?

Answer (Please select your correct option)

IRQ 0



IRQ 1



IRQ 2



IRQ 3



VuAnswers.com

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Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

IRQ 4

IRQ 5

IRQ 6

IRQ 7

VuAnswers.com

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Cell : 03228043306

Made by: Waqar Siddhu

Which of the following IRQs is used for sound card or network card?

Answer (Please select your correct option)

IRQ 4



IRQ 5



IRQ 6



IRQ 7



VuAnswers.com

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The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

1. 193MHZ

Correct answer solved by Hadi

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2. 193MHZ

3. 193MHZ

4. 193MHZ

Made by: Waqar Siddhu

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer (Please select your correct option)

Shl AX , 1



Shr AX, 1



NOT AX



AND AX, 0xFFFF



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8088 processor divides interrupts into ____ classes.

Answer (Please select your correct option)

2

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3

4

5

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In A4FB:4872 Segment:offset pair the physical address is (both segment and offset are in hexadecimal):

Answer (Please select your correct option)

A4FB0



04872



A9822



A4872



VuAnswers.com

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When the instruction "CMP AX,BX" executes and after execution ZF is set, it indicates that

Answer (Please select your correct option)

- AX is greater
- BX is greater
- Both AX and BX are equal
- ZF has no relation with CMP

VuAnswers.com

Correct answer solved by Hadi
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Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

Code Segment register

Data Segment register

Accumulator register

Stack segment register

VuAnswers.com

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Stack is a data structure which behaves as

first in last out manner 59

Answer (Please select your correct option)

VuAnswers.com

First in first out manner

Last in first out manner

All in no out manner

Last in last out manner

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Segment wraparound occurs when during the effective address calculation _____ is generated.

Answer (Please select your correct option)

carry

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error

zero

parity

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The bits of _____ register work independently. If combined all these bits then they are meaningless.

Answer (Please select your correct option)

VuAnswers.com

Accumulator (AX)

Instruction Pointer (IP)

Flag

Base

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IBM PC has separate memory address space and peripheral address space. Memory address space is selected when _____ instruction is given to the processor.

Answer (Please select your correct option)

MOV

DEC

IN

ADD

VuAnswers.com

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INT 10 is used for _____ services.

Answer (Please select your correct option)

BIOS video

DOS video

RAM

Disk

VuAnswers.com

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_____ are not used in mathematical operations.

Answer (Please select your correct option)

Carry, Interrupt and Trap flag

Direction, Interrupt and Trap flag

Direction, Overflow and Trap flag

Direction, Interrupt and Sign flag

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VESA stands for _____

Answer (Please select your correct option)

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

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In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

Programming mode

Real time mode

not sure

Visualization mode

Memory mode

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In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

1

2

3

4

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Correct answer solved by Hadi

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Made by: Waqar Siddhu

In Computer Architecture, RISC stands for _____

Answer (Please select your correct option)

Reversed Instruction Set Computer

Reversed Instruction System Computer

Reduced Instruction Set Computer

Reduced Instruction System Computer

VuAnswers.com

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In Computer Architecture, CISC stands for _____

Answer (Please select your correct option)

Complex Instruction Set Computer

Correct answer selected by user

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Cell : 03228043306

Complex Instruction System Computer

Correct Instruction Set Computer

Correct Instruction System Computer

VuAnswers.com

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Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

First Byte



Second Byte



Third Byte



Last Byte



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In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

INT 10

INT 09

INT 08

INT 07

VuAnswers.com

Correct answer solved by Hadi
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Made by: Waqar Siddhu

The thread registration code initializes the PCB and adds it to the linked list. so that which will give it a turn?

Answer (Please select your correct option)

scheduler

linker

debugger

assembler

VuAnswers.com

Made by: Waqar Siddhu

In multi-tasking programming, all threads are

Answer (Please select your correct option)

Independent to each other

Dependent on each other

Not executed

Not scheduled

VuAnswers.com

Correct answer solved by Hadi

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"INT 10 Video - Scroll Up Window" is a type of video services used in which mode?

Answer (Please select your correct option)

Graphics

Text

Audio

Numeric

VuAnswers.com

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BIOS sees the disks as

Answer (Please select your correct option)

logical storage device

raw storage device

in the form of sectors only

in the form of tracks only

VuAnswers.com

Correct answer solved by Hadi

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Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

DOS

RAM

ROM

BIOS

VuAnswers.com

Made by: Waqar Siddhu

In device attribute word, which of the following bit decides whether it is a character device or a block device ?

Answer (Please select your correct option)

Bit 12



Bit 13



Bit 14



Bit 15



Correct answer solved by Hadi
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Cell : 03228043306

VuAnswers.com

Made by: Waqar Siddhu

The serial port connection is a ----- connector

Answer (Please select your correct option)

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

VuAnswers.com

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer (Please select your correct option)

1

2

3

4

VuAnswers.com

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____.

Answer (Please select your correct option)

7

8

9

6

VuAnswers.com

Made by: Waqar Siddhu

8088 is a

Answer (Please select your correct option)

16 bit processor

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

VuAnswers.com

32 bit processor

64 bit processor

128 bit processor

Made by: Waqar Siddhu

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

1

0

-1

-2

VuAnswers.com

Made by: Waqar Siddhu

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

8 bit

16 bit

32 bit

64 bit

VuAnswers.com

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

In 68K processors there is a 32 bit that holds the address of currently executing instruction.

Answer (Please select your correct option)

Program counter

Stack pointer

Register

Stack

VuAnswers.com

Made by: Waqar Siddhu

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

2



4



8



16



VuAnswers.com

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Which of the following interrupts is Non Maskable Interrupt (NMI).

Answer (Please select your correct option)

INT 2

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

VuAnswers.com

INT 3

INT 0

INT 1

Made by: Waqar Siddhu

_____ number of pin(s) of a processor is/are used by the external hardware to generate an interrupt.

Answer (Please select your correct option)

1

VuAnswers.com

2

3

4

Made by: Waqar Siddhu

If num1 is 0005 and the address of num1 is 0117 then after execution of instruction "mov ax, [num1]", what will be contents of AX register ?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Following are three common video services for INT 10 used in text mode. Identify the use of each video service which is listed below.

0. AH =00h
1. AH =01h
2. Ah =02h

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

What is the format of input buffer of DOS?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

In assembly language programming, which information is represented by descriptor's first 16-bits?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Which two instructions are used to check whether particular bits are set or not? How these two instructions differ from each other?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

In case of branching techniques used in Intel's architecture, which instruction is used to provide temporary diversion and how?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

CLR (A3)
EXG D0, D2
MOVE.B (A1), (A2)
MOVEA (2222).L, A4

Answer ([Please click here to Add Answer](#))

VuAnswers.com

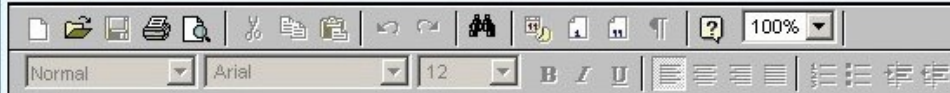


Made by: Waqar Siddhu

What are attributes of charged video service for getting information about font of the text?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

In BIOS video services, which specific services in text mode are invoked by the following values of AH register?

AH = 01

AH = 02

AH = 06

AH = 07

AH = 09

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of "PUSH AX" statement

Answer (Please select your correct option)

VuAnswers.com

AX register will reside on the stack

A copy of AX will go on the stack

The value of AX disappear after moving on stack

Stack will send an acceptance message

Correct answer solved by Hadi

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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

An End of Interrupt (EOI) signal is sent by

Answer (Please select your correct option)

VuAnswers.com

Handler

Correct answer solved by Hadi

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Cell : 03228043306

Processor

IRQ

PIC

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

VuAnswers.com

1.193MHZ

2.193MHZ

3.193MHZ

4.193MHZ

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

Correct answer solved by Hadi

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Cell : 03228043306

INT 2

INT 3

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

The actual physical address represented by the segment-offset pair "AB01:FFFF" will be:

Answer (Please select your correct option)

VuAnswers.com

1AB00



001AB



BB00F



F00BB



Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:
SHL DH, CL.

Answer (Please select your correct option)

VuAnswers.com

01010000

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

10001010

00010100

01010111

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

When two devices in the system want to use the same IRQ line then what will happen?

Answer (Please select your correct option)

VuAnswers.com

An IRQ Collision

An IRQ Conflict

An IRQ Crash

An IRQ Blockage

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of STOSW, the CX will be

Answer (Please select your correct option)

VuAnswers.com

Decrement by 1

Correct answer solved by Hadi

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Cell : 03228043306

Decrement by 2

Incremented by 1

Incremented by 2

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following is true ?

Answer (Please select your correct option)

VuAnswers.com

In general the memory cell cannot be wider than data bus

Control bus is unidirectional

Memory can use the address bus to refer to certain register in processor

There is no relation between memory and data bus

Made by: Waqar Siddhu

Write a subroutine to calculate the string length.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Explain the purpose of Assembly language instructions from 1 to 4 lines as given below.

1. `mov si, 12`
2. `mov al, [cs:si]`
3. `mov [opcode], al`
4. `mov byte [cs:si], 0xCC`

Answer ([Please click here to Add Answer](#))

VuAnswers.com

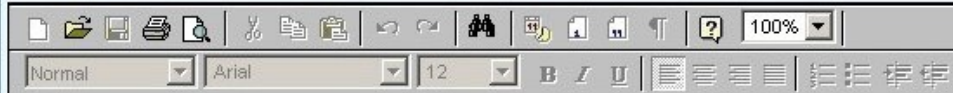


Made by: Waqar Siddhu

```
5. 12: inc ax
6. add bx, 2
7. dec cx
8. sub dx, 2
9. jmp 12
```

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

The instruction mov ax, 0

Answer (Please select your correct option)

VuAnswers.com

will set the ZF

will not set the ZF

is a mathematical instruction

is a logical instruction

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSB instruction, when DF is set, SI is

Answer (Please select the correct option)

Incremented by 1

Incremented by 2

Decrement by 1

Decrement by 2

VuAnswers.com

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

The data on which we want to perform some operations is called

Answer (Please select your correct option)

VuAnswers.com

Opcode

Operand

Operator

Operation

Correct answer solved by Hadi

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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of instruction "RET 2"

Answer (Please select your correct option)

VuAnswers.com

SP is incremented by 2

SP is decremented by 2

not sure

SP is incremented by 4

SP is decremented by 4

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:
SHL DH, CL

Answer (Please select your correct option)

VuAnswers.com

01010000

10001010

00010100

01010111

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

Computer Architecture and Assembly Language Programming is a course that is designed to study _____

Answer (Please select your correct option)

VuAnswers.com

Hardware only

Software only

Computer system more closely

Computer Graphics

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

IBM PC has separate memory address space and peripheral address space. Peripheral address space is selected when _____ instructions is given to the processor.

Answer (Please select your correct option)

VuAnswers.com

- IN
- ADD
- MOV
- DEC

Made by: Waqar Siddhu

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

Video services are exported via

Answer (Please select your correct option)

VuAnswers.com

INT 7

INT 10

INT 9

INT 8

Correct answer solved by Hadi

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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

BIOS sees the disks as

Answer (Please select your correct option)

VuAnswers.com

logical storage device

raw storage device

in the form of sectors only

in the form of tracks only

Made by: Waqar Siddhu

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer (Please select your correct option)

VuAnswers.com

5

6

7

8

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

VuAnswers.com

6

7

8

9

Made by: Waqar Siddhu

Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to TD (Transmitted Data) ?

Answer (Please select your correct option)

VuAnswers.com

1

2

3

4

Made by: Waqar Siddhu

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____.

Answer (Please select your correct option)

VuAnswers.com

6

7

8

9

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

VuAnswers.com

1

0

-1

-2

Made by: Waqar Siddhu

Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

VuAnswers.com

INT 0

INT 1

INT 2

INT 3

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

VuAnswers.com

19

20

21

22

Made by: Waqar Siddhu

Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

Which one of the following interrupts is used for arithmetic overflow?

Answer (Please select your correct option)

INT 4

INT 1

INT 2

INT 3

VuAnswers.com

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

Define following abbreviation:

- Dw

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

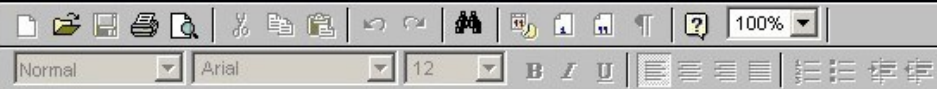
Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

What is the purpose of Triple fault ?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

Write an assembly instruction that will allocate space for 32 PCBs?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

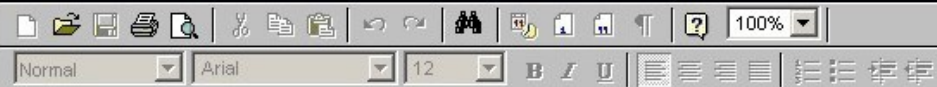
Question No : 44 of 52

Marks: 2 (Budgeted Time 4 Min)

In order to switch "Protected Mode", which bit of CR0 (Control Register 0) is needed to be turn on?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

Identify the technique in which user feels that programs are running at the same time and elaborate that how this technique works ?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

How arithmetic operations instructions are written by Sun SPARC and by Motorola 68K Processor? State your answer with at least two examples from each processor.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 47 of 52

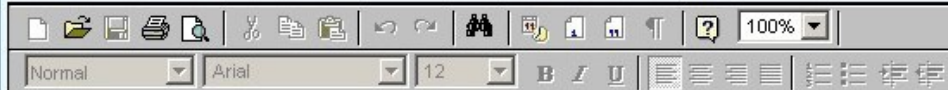
Marks: 3 (Budgeted Time 6 Min)

What are the results after performing the following instructions, where $ax = 00110011$ and $bx = 00010001$

1. and ax,bx
2. or ax,bx
3. xor ax,bx

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

What are the values required for following registers to write a character on serial port using INT 21?

AH =

AL =

DX =

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 49 of 52

Marks: 5 (Budgeted Time 10 Min)

Which instruction makes trap flag zero? If there is no such instruction then how this task is performed?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 50 of 52

Marks: 5 (Budgeted Time 10 Min)

Which processor's instruction set allows the following instructions and describe what task will be performed by each of these instructions?

CLR (A3)
EXG D0, D2
MOVE.B (A1), (A2)
MOVEA (2222).L, A4

Answer ([Please click here to Add Answer](#))

VuAnswers.com



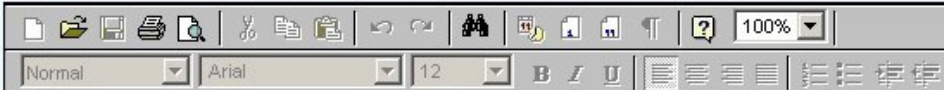
Made by: Waqar Siddhu

How these instructions are legal or illegal. If illegal tell the reason and how can we make this legal, if legal then what these instructions do.

- . mov ax, bx
- a. mov 10,ax
- b. mov bx, [num2]
- c. mov [num1],[num2]

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

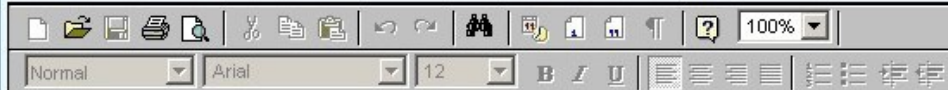
Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

- a. mov ax, bx
- b. mov 10,ax
- c. mov bx, [num2]
- d. mov [num1],[num2]
- e. add [num2],[num1]

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 52 of 52

Marks: 5 (Budgeted Time 10 Min)

Write an assembly program attributes for loading program using INT 21 Service.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of "PUSH AX" statement

Answer (Please select your correct option)

VuAnswers.com

- AX register will reside on the stack
- A copy of AX will go on the stack
- The value of AX disappear after moving on stack
- Stack will send an acceptance message

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

pg 114

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

An End of Interrupt (EOI) signal is sent by

Answer (Please select your correct option)

VuAnswers.com

Handler

pg 114

Correct answer solved by Hadi

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Cell : 03228043306

Processor

IRQ

PIC

Made by: Waqar Siddhu

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

VuAnswers.com

1.193MHZ

pg 122

2.193MHZ

3.193MHZ

4.193MHZ

Made by: Waqar Siddhu

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

pg 122

Correct answer solved by Hadi

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Cell : 03228043306

INT 2

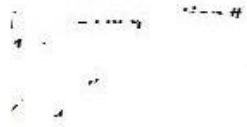
INT 3

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

The actual physical address represented by the segment-offset pair "AB01:FFFF" will be:



VuAnswers.com

Answer (Please select your correct option)

1AB00

001AB

BB00F

F00BB

Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:
SHL DH, CL.

Answer (Please select your correct option)

VuAnswers.com

01010000

Correct answer solved by Hadi

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Cell : 03228043306

10001010

00010100

01010111

Made by: Waqar Siddhu

When two devices in the system want to use the same IRQ line then what will happen?

Answer (Please select your correct option)

VuAnswers.com

An IRQ Collision

An IRQ Conflict

pg 114

An IRQ Crash

An IRQ Blockage

Made by: Waqar Siddhu

After the execution of STOSW, the CX will be

Answer (Please select your correct option)

VuAnswers.com

Decrement by 1

pg 92

Correct answer solved by Hadi

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Cell : 03228043306

Decrement by 2

Incremented by 1

Incremented by 2

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following is true ?

Answer (Please select your correct option)

VuAnswers.com

In general the memory cell cannot be wider than data bus

Control bus is unidirectional

Memory can use the address bus to refer to certain register in processor

~~not confirm~~

There is no relation between memory and data bus

Made by: Waqar Siddhu

Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

VuAnswers.com

Code Segment register

Data Segment register

Accumulator register

pg 12

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Stack segment register

Made by: Waqar Siddhu

Relating to the stack in 8088 architecture which of the following statement is true ?

Answer (Please select your correct option)

VuAnswers.com

- Single word can not be pushed on the stack
- Single word can not be popped from the stack
- Single byte can not be popped from the stack
- Single quad can be popped from the stack

pg 68

Made by: Waqar Siddhu

The second byte of the word designated for one screen location holds

VuAnswers.com

Answer (Please select your correct option)

The dimensions of the screen

Character position on the screen

Character color on the screen

ASCII code of the character

~~pg 81~~

Made by: Waqar Siddhu

INT 10 is used for _____ services.

Answer (Please select your correct option)

VuAnswers.com

BIOS video

pg 149

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

DOS video

RAM

Disk

Made by: Waqar Siddhu

In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

VuAnswers.com

line

dot *pg 149*

circle

rectangle

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

The value of AH register in the write Graphics pixel service is _____

Answer (Please select your correct option)

VuAnswers.com

0Ch

pg 152

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

0Bh

1Ch

2Ch

Made by: Waqar Siddhu

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

VuAnswers.com

control block



pg 140

Process control block

stack

hard disk

Made by: Waqar Siddhu

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

Answer (Please select your correct option)

VuAnswers.com

a)

pg 152

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

b)

c)

d)

Made by: Waqar Siddhu

VESA VBE 2.0 is a standard for _____

Answer (Please select your correct option)

VuAnswers.com

Ultra resolution mode

High resolution mode

pg 180

Medium resolution mode

Low resolution mode

Made by: Waqar Siddhu

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

VuAnswers.com

Variable Electronics Standards Association

Video Electronics Standards Association

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Video Energy Standards Association

Vega Energy Standards Association

Made by: Waqar Siddhu

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

VuAnswers.com

Video BIOS Extensions

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

Made by: Waqar Siddhu

IDTR is a _____ register.

VuAnswers.com

Answer (Please select your correct option)

16 bit

20 bit

32 bit

48 bit

pg 182

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

VuAnswers.com

Programming mode

Real time mode

not confirm

Visualization mode

Memory mode

Made by: Waqar Siddhu

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

VuAnswers.com

First Byte

Second Byte

pg 152

Third Byte

Last Byte

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

What is the size of a Sector of a standard 1.44MB Floppy disk having 80 tracks and 18 Sectors/track?

Answer (Please select your correct option)

VuAnswers.com

128 Bytes

256 Bytes

512 Bytes

pg 155

1024 Bytes

Made by: Waqar Siddhu

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

VuAnswers.com

INT 10

INT 09

INT 08

pg 141

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

INT 07

Made by: Waqar Siddhu

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

VuAnswers.com

global

local

pg 141

legal

illegal

Made by: Waqar Siddhu

Technically IAPX88 stands for “_____Advanced Processor Extensions 88.”

Answer (Please select your correct option)

VuAnswers.com

Intel

pg 14

Integrated

Intra

Internal

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

In Left-Shift-Operation, the left most bit _____

Answer (Please select your correct option)

VuAnswers.com

- will be dropped
- will go into CF
- will be moved to the right-most position
- will always be 1

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

The serial port connection is a ----- connector

Answer (Please select your correct option)

VuAnswers.com

9 pin DB-9

pg 171

8 pin DB-9

3 pin DB-9

9 pin DB-5

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

VuAnswers.com

Answer (Please select your correct option)

6

7

8

pg 171

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

9

Made by: Waqar Siddhu

In 9 pin DB-9, which pin number is assigned to CD (Carrier Detect) ?

Answer (Please select your correct option)

VuAnswers.com

 1

pg 171

 2 3 4

Made by: Waqar Siddhu

Which of the following BIOS INT provides serial port services

Answer (Please select your correct option)

VuAnswers.com

INT14

pg 171

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

INT11

INT12

INT13

Made by: Waqar Siddhu

8088 is a

Answer (Please select your correct option)

VuAnswers.com

16 bit processor

pg 14

32 bit processor

64 bit processor

128 bit processor

Made by: Waqar Siddhu

A 32 bit address register can access up to of memory, so memory access has increased a lot.

VuAnswers.com

Answer (Please select your correct option)

2GB



4GB



6GB



8GB



Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

VuAnswers.com

1

0

pg 175

-1

-2

Made by: Waqar Siddhu

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

VuAnswers.com

8 bit

16 bit

32 bit

pg 191

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

64 bit

Made by: Waqar Siddhu

In 68K processors there is a 32 bit that holds the address of currently executing instruction.

Answer (Please select your correct option)

VuAnswers.com

Program counter

pg 191

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Stack pointer

Register

Stack

Made by: Waqar Siddhu

Programmable Interrupt Controller (PIC) has

Answer (Please select your correct option)

VuAnswers.com

One input signals and eight output signals

One input signal and one output signal

Eight input signals and one output signals

pg 113

Eight input signals and eight output signals

Made by: Waqar Siddhu

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

VuAnswers.com

INT 0

INT 1

pg 105

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

INT 2

INT 3

Made by: Waqar Siddhu

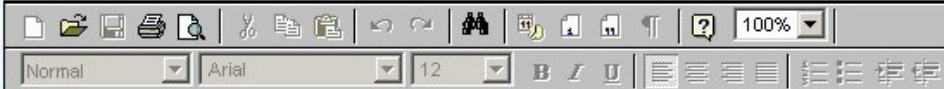
Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

For which purpose INT 1 is used and describe the functionality of this interrupt?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of "PUSH AX" statement

Answer (Please select your correct option)

VuAnswers.com

- AX register will reside on the stack
- A copy of AX will go on the stack
- The value of AX disappear after moving on stack
- Stack will send an acceptance message

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

An End of Interrupt (EOI) signal is sent by

Answer (Please select your correct option)

VuAnswers.com

Handler

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Processor

IRQ

PIC

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PIT) has input frequency of

Answer (Please select your correct option)

VuAnswers.com

1.193MHZ

2.193MHZ

3.193MHZ

4.193MHZ

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

INT 2

INT 3

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

The actual physical address represented by the segment-offset pair "AB01:FFFF" will be:

Answer (Please select your correct option)

VuAnswers.com

1AB00



001AB



BB00F



F00BB



Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

Suppose DH contains 10001010 and CL contains 3, what is the value of DH after the following instruction:
SHL DH, CL.

Answer (Please select your correct option)

VuAnswers.com

01010000

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

10001010

00010100

01010111

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

When two devices in the system want to use the same IRQ line then what will happen?

Answer (Please select your correct option)

VuAnswers.com

An IRQ Collision

An IRQ Conflict

An IRQ Crash

An IRQ Blockage

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

After the execution of STOSW, the CX will be

Answer (Please select your correct option)

VuAnswers.com

Decrement by 1

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Decrement by 2

Incremented by 1

Incremented by 2

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following is true ?

Answer (Please select your correct option)

VuAnswers.com

In general the memory cell cannot be wider than data bus

Control bus is unidirectional

Memory can use the address bus to refer to certain register in processor

There is no relation between memory and data bus

Made by: Waqar Siddhu

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

Traditionally all the mathematical and logical operations are performed in _____

Answer (Please select your correct option)

VuAnswers.com

Code Segment register

Data Segment register

Accumulator register

Stack segment register

Correct answer solved by Hadi

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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

Relating to the stack in 8088 architecture which of the following statement is true ?

Answer (Please select your correct option)

VuAnswers.com

- Single word can not be pushed on the stack
- Single word can not be popped from the stack
- Single byte can not be popped from the stack
- Single quad can be popped from the stack

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

The second byte of the word designated for one screen location holds

Answer (Please select your correct option)

VuAnswers.com

- The dimensions of the screen
- Character position on the screen
- Character color on the screen
- ASCII code of the character

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 10 is used for _____ services.

Answer (Please select your correct option)

VuAnswers.com

BIOS video

DOS video

RAM

Disk

Made by: Waqar Siddhu

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

In graphics mode a location in video memory corresponds to a _____ on the screen.

Answer (Please select your correct option)

VuAnswers.com

- line
- dot
- circle
- rectangle

Correct answer solved by Hadi
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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

The value of AH register in the write Graphics pixel service is _____

Answer (Please select your correct option)

VuAnswers.com

0Ch

0Bh

1Ch

2Ch

Made by: Waqar Siddhu

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

VuAnswers.com

control block

Process control block

stack

hard disk

Correct answer solved by Hadi

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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

INT 21 service 01H is used to read character from standard input with echo. It returns the result in _____ register.

Answer (Please select your correct option)

VuAnswers.com

a)

b)

c)

d)

Made by: Waqar Siddhu

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA VBE 2.0 is a standard for _____

Answer (Please select your correct option)

VuAnswers.com

Ultra resolution mode



High resolution mode



Medium resolution mode



Low resolution mode



Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

VuAnswers.com

Variable Electronics Standards Association



Video Electronics Standards Association



Video Energy Standards Association



Vega Energy Standards Association



Made by: Waqar Siddhu

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

VuAnswers.com

Video BIOS Extensions

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

Made by: Waqar Siddhu

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

IDTR is a _____ register.

Answer (Please select your correct option)

VuAnswers.com

16 bit

20 bit

32 bit

48 bit

Made by: Waqar Siddhu

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

VuAnswers.com

Programming mode

Real time mode



Visualization mode



Memory mode



Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

VuAnswers.com

First Byte



Second Byte



Third Byte



Last Byte



Made by: Waqar Siddhu

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

What is the size of a Sector of a standard 1.44MB Floppy disk having 80 tracks and 18 Sectors/track?

Answer (Please select your correct option)

VuAnswers.com

128 Bytes

256 Bytes

512 Bytes

1024 Bytes

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking environment, which is called scheduler for saving and restoring the registers?

Answer (Please select your correct option)

VuAnswers.com

INT 10

INT 09

INT 08

INT 07

Made by: Waqar Siddhu

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

VuAnswers.com

global



local



legal



illegal



Correct answer solved by Hadi

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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

Technically IAPX88 stands for “_____ Advanced Processor Extensions 88.”

Answer (Please select your correct option)

VuAnswers.com

Intel

Integrated

Intra

Internal

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

In Left-Shift-Operation, the left most bit _____

Answer (Please select your correct option)

VuAnswers.com

- will be dropped
- will go into CF
- will be moved to the right-most position
- will always be 1

Correct answer solved by Hadi
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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

VuAnswers.com

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

Made by: Waqar Siddhu

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

VuAnswers.com

6

7

8

9

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

INT 2

INT 3

Correct answer solved by Hadi
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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

In STOSW instruction, when DF is clear, SI is

Answer (Please select your correct option)

VuAnswers.com

Incremented by 1

Incremented by 2

Decrement by 1

Decrement by 2

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is connected to serial port COM 1?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

correct

IRQ 5

IRQ 6

IRQ 7

Made by: Waqar Siddhu

Question No : 3 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following IRQs is used by the parallel port?

Answer (Please select your correct option)

VuAnswers.com

IRQ 4

IRQ 5

IRQ 6

IRQ 7

correct

Correct answer solved by Hadi

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Cell : 03228043306

Made by: Waqar Siddhu

Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The programmable interval timer (PTI) has input frequency of

Answer (Please select your correct option)

VuAnswers.com

1.193MHZ

correct

2.193MHZ

3.193MHZ

4.193MHZ

Made by: Waqar Siddhu

Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is of highest priority interrupt?

Answer (Please select your correct option)

VuAnswers.com

Key board interrupt

Timer interrupt

correct

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

INT 2

INT 3

Made by: Waqar Siddhu

Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

By executing which of the following instruction, the value in AX register becomes the half of previously stored value ?

Answer (Please select your correct option)

VuAnswers.com

Shl AX, 1

Shr AX, 1

correct

NOT AX

AND AX, 0xFFFF

Made by: Waqar Siddhu

Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

In the rotate right operation every bit moves one position to the right and the bit dropped from the right is inserted at the left and:

Answer (Please select your correct option)

VuAnswers.com

The dropped bit is also copied in CF

correct

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

The dropped bit is also copied in AF

The dropped bit is also copied in ZF

The dropped bit is not copied in any flag

Made by: Waqar Siddhu

Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

In string instructions, CX is always

Answer (Please select your correct option)

VuAnswers.com

Decremented by 1

Decremented by 2

Incremented by 1

correct

Incremented by 2

Made by: Waqar Siddhu

Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

The word size of the processor is defined by the size of _____

Answer (Please select your correct option)

VuAnswers.com

Its accumulator register

Address bus attached

~~X~~ correct

Memory attached

Control Bus

Made by: Waqar Siddhu

Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ cannot predict which element the _____ needs, at a particular instant of time.

Answer (Please select your correct option)

VuAnswers.com

Control bus, Memory

Processor, Control bus
 Processor, Memory

Memory, Processor

correct

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Control bus, Memory

Made by: Waqar Siddhu

Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

The time interval between two timer ticks is _____

Answer (Please select your correct option)

VuAnswers.com

40ms

45ms

50ms

55ms

correct

Made by: Waqar Siddhu

Question No : 12 of 52

Marks: 1 (Budgeted Time 1 Min)

In DOS input buffer, the number of characters actually read on return is stored in _____ byte.

Answer (Please select your correct option)

VuAnswers.com

third

fourth

first

second

correct

Made by: Waqar Siddhu

Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

A thread can not be created _____

Answer (Please select your correct option)

VuAnswers.com

in static way

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

correct

in dynamic way

in static and dynamic way at the same time

manually

Made by: Waqar Siddhu

Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

The space where the current state of a task is stored is called _____

Answer (Please select your correct option)

VuAnswers.com

control block

Process control block

correct

stack

hard disk

Made by: Waqar Siddhu

Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

VESA stands for _____

Answer (Please select your correct option)

VuAnswers.com

Variable Electronics Standards Association

Video Electronics Standards Association

Video Energy Standards Association

Vega Energy Standards Association

correct

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

VBE stands for _____, as proposed by VESA (an organization).

Answer (Please select your correct option)

VuAnswers.com

Video BIOS Extensions

correct

Video BIOS Emergence

Video Binary Extensions

Video Blocked Extensions

Made by: Waqar Siddhu

Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

In Computer Architecture, the Protected mode is different from _____

Answer (Please select your correct option)

VuAnswers.com

Programming mode

Real time mode

correct

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Visualization mode

Memory mode

Made by: Waqar Siddhu

Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

In Assembly language programming, how many prevalent Calling Conventions do exist ?

Answer (Please select your correct option)

VuAnswers.com

1

2

correct

3

4

Made by: Waqar Siddhu

Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

In Motorola 68K processors, _____ registers can hold addresses in indirect memory accesses.

Answer (Please select your correct option)

VuAnswers.com

A0-A7

correct

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

B0-B7

C0-C7

D0-D7

Made by: Waqar Siddhu

Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

Motorola 68K processors have _____ 32 bit general purpose registers.

Answer (Please select your correct option)

VuAnswers.com

32

64

8

16

correct

Made by: Waqar Siddhu

Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

Which byte of the DOS input buffer holds the number of characters actually read on return?

Answer (Please select your correct option)

VuAnswers.com

First Byte

Second Byte

correct

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Third Byte

Last Byte

Made by: Waqar Siddhu

Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

Threads can have function calls, parameters and which type of variables?

Answer (Please select your correct option)

VuAnswers.com

global

local

correct

legal

illegal

Made by: Waqar Siddhu

Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

In multi-tasking programming, all threads are

Answer (Please select your correct option)

VuAnswers.com

Independent to each other

correct

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Dependent on each other

Not executed

Not scheduled

Made by: Waqar Siddhu

Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

Video services are exported via

Answer (Please select your correct option)

VuAnswers.com

INT 7

INT 10

correct

INT 9

INT 8

Made by: Waqar Siddhu

Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

The maximum memory, IAPX88 can access is _____.

Answer (Please select your correct option)

VuAnswers.com

1 KB

64 KB

1 MB

correct

64 MB

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

Made by: Waqar Siddhu

Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In direct addressing the memory address is _____ and is given in the instruction.

Answer (Please select your correct option)

VuAnswers.com

Fixed

correct

Empty

A variable

A register

Made by: Waqar Siddhu

Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

Instruction "mov ax, bl" has a _____ problem.

Answer (Please select your correct option)

VuAnswers.com

size mismatch

correct

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

value mismatch

operand mismatch

memory mismatch

Made by: Waqar Siddhu

Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

The jump "JC" is taken when

Answer (Please select your correct option)

VuAnswers.com

CF=1

correct

CF=0

ZF=0

ZF=1

Made by: Waqar Siddhu

Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following gives more logical view of the storage medium ?

Answer (Please select your correct option)

VuAnswers.com

DOS

Correct answer solved by Hadi

Email : usmanraj20@gmail.com

Cell : 03228043306

RAM

ROM

BIOS

Made by: Waqar Siddhu

Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

The serial port connection is a ----- connector

Answer (Please select your correct option)

VuAnswers.com

9 pin DB-9

8 pin DB-9

3 pin DB-9

9 pin DB-5

Made by: Waqar Siddhu

Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to RTS (Request To Send) ?

Answer (Please select your correct option)

VuAnswers.com

5

6

7

correct

8

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, which pin number is assigned to CTS (Clear To Send) ?

Answer (Please select your correct option)

VuAnswers.com

6

7

8

correct

9

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Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

In 9 pin DB-9, RI (Ring Indicator) is assigned on pin number _____

Answer (Please select your correct option)

VuAnswers.com

6

7

8

9

correct

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

Question No : 34 of 52

Marks: 1 (Budgeted Time 1 Min)

8088 is a

Answer (Please select your correct option)

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- 16 bit processor
- 32 bit processor
- 64 bit processor
- 128 bit processor

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Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

AX register in extended 32 bit register is renamed as _____

Answer (Please select your correct option)

VuAnswers.com

AXE

EAX

AAX

XAX

Made by: Waqar Siddhu

Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

The table index (TI) is set to _____ to access the GDT (Global Descriptor Table).

Answer (Please select your correct option)

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1

0

-1

-2

Correct answer solved by Hadi
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Cell : 03228043306

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Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

In 68K processors there is a program counter (PC) that holds the address of currently executing instruction.

Answer (Please select your correct option)

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8 bit

16 bit

32 bit

64 bit

Correct answer solved by Hadi
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Cell : 03228043306

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Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

An 8 x 16 font is stored in _____ bytes.

Answer (Please select your correct option)

VuAnswers.com

2

4

8

16

Correct answer solved by Hadi
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Cell : 03228043306

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Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

Which of the following interrupt is used in debugging with the help of trap flag?

Answer (Please select your correct option)

VuAnswers.com

INT 0

INT 1

INT 2

INT 3

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

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Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

In Programmable Interrupt Controller, which one of the following ports is referred as interrupt mask register?

Answer (Please select your correct option)

VuAnswers.com

19

20

21

22

Correct answer solved by Hadi
Email : usmanraj20@gmail.com
Cell : 03228043306

Made by: Waqar Siddhu

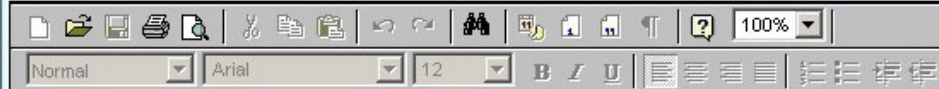
Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

Device drivers are operating system extensions which extend its services to new devices. Describe the structure of "Device Driver File" which is used for device management.

Answer ([Please click here to Add Answer](#))

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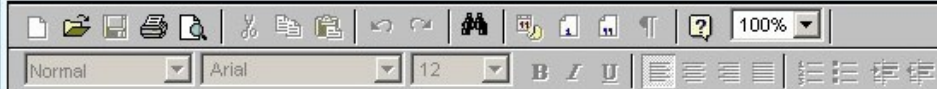
Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

With reference to branching technique used in assembly language programming, write any two control instructions.

Answer ([Please click here to Add Answer](#))

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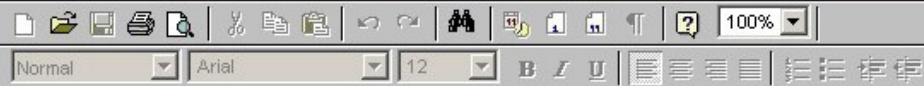
Question No : 43 of 52

Marks: 2 (Budgeted Time 4 Min)

How local thread variables are created and accessed ?

Answer ([Please click here to Add Answer](#))

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Question No : 44 of 52

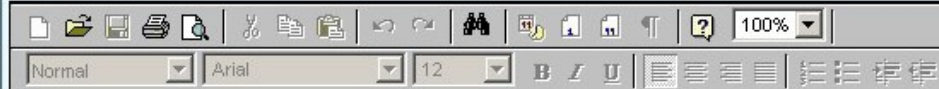
Marks: 2 (Budgeted Time 4 Min)

What are the following statements represent?

gdt: dd 0x00000000, 0x00000000

Answer ([Please click here to Add Answer](#))

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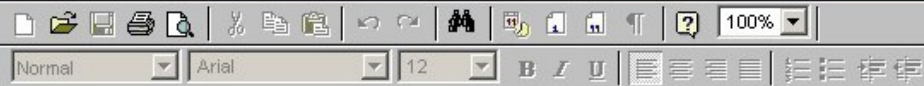
Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

What are the important "command codes" in request header of device driver?

Answer ([Please click here to Add Answer](#))

VuAnswers.com



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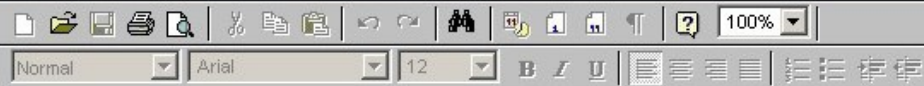
Question No : 46 of 52

Marks: 3 (Budgeted Time 6 Min)

In order to access Video memory in high resolution (more than a megabyte MB), what standard we have to use?

Answer ([Please click here to Add Answer](#))

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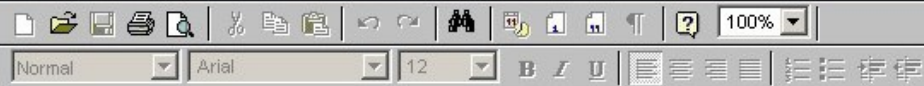
Question No : 47 of 52

Marks: 3 (Budgeted Time 6 Min)

Describe "Indexed Register Indirect + Offset" addressing mode with an example.

Answer ([Please click here to Add Answer](#))

VuAnswers.com



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Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

With reference to the multitasking program "TSR caller". Describe what task will be performed by each of these instructions?

- `mov al, [chars+bx]`
- `mov [es:40], al`
- `inc bx`

Answer ([Please click here to Add Answer](#))

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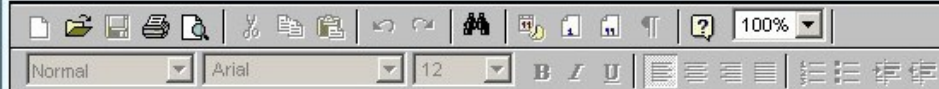
Question No : 49 of 52

Marks: 5 (Budgeted Time 10 Min)

How many types of interrupt are used for writing debugger and also elaborate functionality of these interrupts with proper explanation?

Answer ([Please click here to Add Answer](#))

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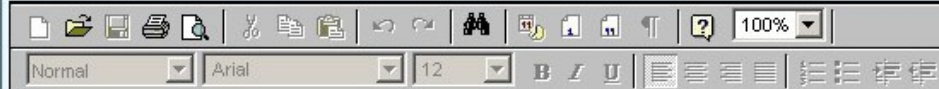
Question No : 50 of 52

Marks: 5 (Budgeted Time 10 Min)

What are the contents at the starting position of the file designated as device driver and mention important attributes of this driver file?

Answer ([Please click here to Add Answer](#))

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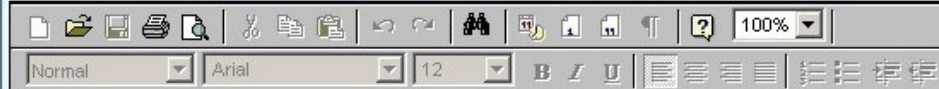
Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

At which location interrupt gate descriptor table is stored and describe the structure of this table with an example?

Answer ([Please click here to Add Answer](#))

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Made by: Waqar Siddhu

Question No : 52 of 52

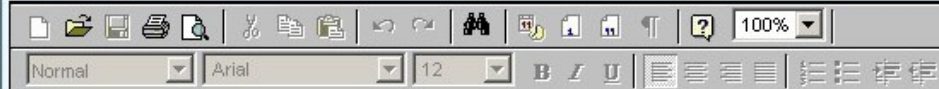
Marks: 5 (Budgeted Time 10 Min)

Give the new values of AX and DX for each of the following instructions, or write if the overflow occurs.

1. DIV BX, if DX contains 0000h, AX contains 0007h, and BX contains 0002h
- a. DIV BX, if DX contains 0000h, AX contains FFFFh, and BX contains 0010h

Answer ([Please click here to Add Answer](#))

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