

CS402

Quiz-1

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MCSQ

AND Many MoRe

In GTG, there can be more than one.

Start states

There can be more than ____ FA for a certain language but for ____ FA there is only one language associated with it.

one, one

The complement of a regular language is also ____.

regular

Which of the following represent the absence and presence of current in sequential circuit respectively?

0, 1

The strings or words which do not belong to a language are lalled ____ of that language.

Complement

If the intersection of two regular language is regular then the complement of the intersection of these two languages is ____.

regular

In pref (Q in R), Q is ___ to/then R.

Not equal

Which of the following state is introduced while developing NFA for the closure of FA?

An initial state which should be final as well

The reverse of the string sbsbb over {sb, f, b }

bsbfsb

A problem that his decision procedure is called ___ problem .

decidable

A language ending with 'b' partition Σ^* into ___ distinct classes.

two

___ State is not important in Moore Machine?

Initial

A non regular language an be repewsentated by

None of the given options

TG
FA
RE

Let L be the language of all strings, defined over $\Sigma = \{0,1\}$, ending in 111. Which of the following strings are indistinguishable with respect to L with Z being 11?

111, 101

Let L be the language of all strings, defined over $\Sigma = \{0,1\}$, ending in 10. Which of the following strings are indistinguishable with respect to L with Z being 11?

111.101

The CFG $S \rightarrow aSb \mid ab \mid \Lambda$ is used to express the language ____.

Equal

If there is no final state of two FAs then their ____ also have no ____ state.

union, final

Formal is also known as ____.

Syntactic language

Which of the following refers to the set of strings of letters that when concatenated to the front of some word in Q produces some word in R?

Pref (Q in R)

13. The grammatical rules which involve the meaning of words are called ____.

semantics

Which of the following is a non-regular language?

Prime

A problem is said to be ____ if there exists an algorithm that provides the solution in ____ number of steps.

effectively solvable, finite

In programmable interpreter controller which of the following ports is referred as interrupt mask register?

21

Which of the following string belongs to the language of the regular expression (aa^*b) ?

aabaab

____ is the highest priority interrupt in interrupt controller

IRQ 0

If r_1 and r_2 are regular expressions then (r_1*r_2) is_____

RE (Regular expression)

IRET returns on the basis of and

CS, IP

If r1 is regular expression the (r1)* is _____.

Also regular expression

If is set, the after every instruction a type 1 interrupt will be automatically generated.

Trap flag

Melay machine to increase the output string in magnitude by 1 is called _____.

Incrementing machine

The interrupt handler uses instruction to return back to the caller.

IRET

Which of the following interrupt is used for Arithmetic overflow?

INT 4

During the execution of INT instruction, some contents are pushed on to the stack, the order of pushing then is

FLAGS register, CS and then IP

Each of the bits at Port _____ corresponding to one of the IRQ lines.

21

Which of the following pins of the parallel port connector are grounded? a.

18-25

Which of the following interrupts plays the most significant part during step debugging of a program?

INT 1

Which of the following IRQs is connected to serial port COM 2?

IRQ 3

Which of the following IRQs is connected to serial port COM 1?

IRQ 4

Which of the following IRQs is used by the parallel port?

IRQ 7

In programmable interpreter controller which of the following ports is referred as interrupt mask register?

21

In programmable interpreter controller which of the following ports is used to selectively enabling or disabling interrupts?

21

The space where all the registers of task are stored is called

Process control box

Which of the following is the destination register in IN instruction?

AL or AX

In multitasking environment, which of the following structure is used to maintain the ordering of active PCBs?

Linked List

In multitasking which of the following interrupts is used as scheduler during context switching?

INT 8

Which of the following are required for thread entry?

CS and IP

The time interval between two timer tick is?

55 MS

Which of the following flag can be used for mathematical operations ?

Carry Flag

In multitasking, the process of saving and restoring to values of registers from a process control block (PCB) is called **context switching**

When two devices in a system want to use the same IRQ line, is referred as:

IRQ Conflit

AN End of Interrupt (EOI) signal is sent by **Hardler**

Threads can have function calls, parameters and _ variables.
Local

Which of the following arranges jobs in a sequence in order to be executed?
Process control box

Which of the following flags cannot be cleared using an assembly instruction?
Carry flag

Which of the following is the highest priority interrupt?
INT0

At the end of servicing an interrupt, which of the following is used to inform the PIC that it is completed?
EOI

Which of the following interrupts is used for maintaining the system time?
INT 8

Which of the following is used for exporting parallel port services?
INT 17

$[(a+b)(a+)]$, given RE cannot generate the string _____.
bbbbbb

Which of the following is free of non-determinism?
FA

In programmable interpret control which of the following ports is referred as a control port?

20

_____ is/are the port number(s) for parallel port.

378

The number of pins in a parallel port connector are?

25

In proving Keene theorem II , if three statuses are connected then middle state is removed by connecting first and third and writing corresponding RE in:

Concentration

The offset address of an interrupt n will be at

nx4

Non-Deterministic is present in :

Both TG and GTG

programmable interpret control has two ports (20 and 21). Port 20 is the control port while port21 is .

The interrupt mask register

Which of the following instruction selects memory address space?

MOV

The thread registration code initializes the PCB and adds it to the linked list so that the will give it a turn.

Scheduler

Which of the following instruction is used for disabling all the interrupts during a program execution?

Cli

programmable interrupt control (PIC) has

Eight input signals and one output signals

In 8088 processor, there can be total possible entries in an interrupt vector table.

256

each thread can have their own

Execution area

The parallel port controller is called?

DB-25

In 8088, the physically memory address for Interrupt Vector Table is fixed and the table occupies exactly of memory.

1 KB

Which of the following interrupts is non maskable interrupt?

INT 2

Using OUT instruction on parallel data port results into a signal of for every 1 bit.

5 V

PCB stands for?

Process control block

Which of the following port number is used to send an end of interrupt (EOI) signal to the PIC after an interrupt is ended?

0x20

Which of the following instruction is used for reading a char from keyboard?

in al , 0x60

In PIC, which of the following port is used for selectively enabling or disabling interrupts?

21

DOS has single entry points to access all its services through interrupt

a) INT 21

Which of the following pins of parallel port are used to carry the data from processor to the device connected through parallel port?

Pin 2 to Pin 9

56. The input frequency of the prigramable interval timer (PIT) is

Fixed

All registers of task are stored in space called

Process control block

Which of the following IRQ's is derived by a key board

IRQ 1

changing and restoring the state of control process unit (CPU) is referred as

Multitasking

For the external hardware to generate an interrupt there are how many pins outside the processor?

1

Which of the following instruction selects peripheral address space?

IN

Which of the following is the most commonly used port with printer?

Parallel Port

DVI Port

INT 1

Which of the following is used for exporting keyboard services?

INT 16

TSR stands for

Terminate and Stay resident

Which of the following is the ACK pin in DB-25 connector

a) 10

Which of the following IRQs is a cascading interrupt?

IRQ 2

In interrupt masking, we use to get control from the program without letting the program know about it.

IRQ 0

The output of programmable timer interval is connected to which line of the PIC?

IRQ 0

Which of the following IRQs is used for Floppy disk drive?

IRQ 6

In multitasking, each PCB requires bytes to store the values of registers.

28

The CFG is said to be ambiguous if there exist at least one word of its language that can be generated by the _____ production trees.

More than one / Different

The language of the all string beginning with 'b; partitions Σ^* into _____ distinct classes.

Two

Every _____ is a _____ as well, but the converse may not be true.

FA, TG

Which of the following should not be NULL in the context of pumping Lemma?

N

Regular languages are closed under the following operations.

Union, Concatenation and closure

Reverse of string "YxwzYz" defined over $\Sigma = \{w, x, Y, xz\}$ is _____

zYzwxY

If two Res generated same language then these Res are called:

Equivalent RE

In FA initial state can be represented by:

Drawing ' + ' sign in that state

Which of the following is used to delay the transmission of signal along the wire by one step (clock pulse)?

Delay box

What is false about the PALINDROME LANGUAGE?

None of the given option

FA can be build for it
Every word is reverse of itself
It is an infinite language

Which of the following string is a part of equal language defined over $\Sigma=\{a,b\}$?

ababab

The strings of FA2 are accepted first before the strings of FA1

Palindromes

The reverse of the string sbfsbb over $\{s, f, b\}$

bbsfbs

Which of the following IRQs is used for sound card or network card?

IRQ 5

An FA is a collection of:

Finite states , finite transition and finite input letters

One language can have ____ CGFs.

More than one

Programmable interrupt controller has ports.

2

Which of the following bits of parallel control port enables the IRQ 7 triggering by the ACK pin?

Bit 4

Which one of the following machine is represented as a pictorial representation with states and directed edges labeled by an input letter along with an output character?

Mealy machine

Which of the following IRQs is derived by a timer device?

IRQ 0

Suppose we have FA3 (which is equal to FA1+FA2), then the final state of FA3 will be declared final if:

It corresponds to any of the final states of FA1 or FA2

If “r1” and “r2” are regular languages, then which of the following is not a regular expression.

r1 – r2

_____ is a special type of interrupt that returns to the same instruction instead of the next instruction

Divide overflow

If L_1' and L_2' are regular languages, then L_1, L_2 will be

Regular

All the registers & stack are saved in

Multitasking

Every NFA can be considered to be a _____ as well, but the converse may not be true.

TG

Which of the following is the source register in OUT instruction?

AL or AX

Melay machine can have _____ final state

Zero

Which of the following is the destination register in IN instruction?

AL or AX

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