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A Good Education is a Foundation For a Better Future

CORRECT ANSWER SOLVED BY HADI
EMAIL: hadirajputofficial@gmail.com
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(Final Term Past Paper)

MADE AND SOLVED BY TEAM HADI

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



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Best of luck!



Question No : 1 of 52

Marks: 1 (Budgeted Time 1 Min)

Which one of the following is the memory organization of **EAGLE processor**?

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Answer (Please select your correct option)

$2^8 * 8$ bits

$2^{16} * 8$ bits

$2^{32} * 8$ bits

$2^{64} * 8$ bits



correct answer
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Question No : 2 of 52

Marks: 1 (Budgeted Time 1 Min)

-----is a collection of binary digits or bits that the computer reads and interprets.

Answer (Please select your correct option)

assembly language

higher level language

English language

machine language



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Question No : 3 of 52


Marks: 1 (Budgeted Time 1 Min)

In which one of the following addressing modes, the value to be stored in memory is obtained by directly retrieving it from another memory location?

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Answer (Please select your correct option)

- Direct Addressing Mode  correct answer
solved by hadi
- Immediate addressing mode
- Indirect Addressing Mode
- Register (Direct) Addressing Mode

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Question No : 4 of 52

Marks: 1 (Budgeted Time 1 Min)

The external interface of FALCON-A consists of a _____ address bus and a _____ data bus.

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Answer (Please select your correct option)

8-bit , 8-bit

16-bit , 16-bit

16-bit , 24-bit

16-bit , 32-bit



correct answer
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Question No : 5 of 52

Marks: 1 (Budgeted Time 1 Min)

Execution time of a program with respect to the processor is calculated as:

Answer (Please select your correct option)

Execution Time = IC x CPI x MIPS

Execution Time = IC x CPI x T

Execution Time = CPI x T x MFLOPS

Execution Time = IC x T

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Question No : 6 of 52

Marks: 1 (Budgeted Time 1 Min)

In which of the following instructions the data move between a register in the processor and a memory location (or another register) and are also called data movement?

Answer (Please select your correct option)

Arithmetic

Load/store

Control

Logic



correct answer
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Question No : 7 of 52

Marks: 1 (Budgeted Time 1 Min)

The _____ instruction is completed once memory access has been made and the memory location has been written to.

Answer (Please select your correct option)

Store



Branch

Load

Control

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Question No : 8 of 52

Marks: 1 (Budgeted Time 1 Min)

Which type of instructions enables mathematical computations?

Answer (Please select your correct option)

Arithmetic



correct answer
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Control

Data transfer

Miscellaneous

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Question No : 9 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ control signal allows the bus to read from the selected register.

Answer (Please select your correct option)

RBE

RCE

LCON

R2BUS



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Question No : 10 of 52

Marks: 1 (Budgeted Time 1 Min)

For any of the instructions that are a part of the instruction set of the SRC, there are certain _____ required; which may be used to select the appropriate function for the ALU to be performed, to select the appropriate registers, or the appropriate memory location.

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Answer (Please select your correct option)

Registers

Control signals

Memory

DMA controllers



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Question No : 11 of 52

Marks: 1 (Budgeted Time 1 Min)

Every I/O port has a unique identifier associated with it, which is called its -----

Answer (Please select your correct option)

Address



correct answer
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Access point

Interval Identifier

Device driver

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_____ is a technique in which some of the CPU's address lines forming an input to the address decoder are ignored.

Answer (Please select your correct option)

Partial decoding

correct answer
solved by hadi



Full encoding

Partial multiplexing

Half encoding

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Question No : 13 of 52

Marks: 1 (Budgeted Time 1 Min)

Partial decoding is an attractive choice in _____.

Answer (Please select your correct option)

Small systems



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Large systems

Medium systems

All of the above

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Question No : 14 of 52

Marks: 1 (Budgeted Time 1 Min)

Consider Falcon A, with 16 address lines, the total address space is _____ Kbytes.

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Answer (Please select your correct option)

2^{16}



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2^{10}

2^6

2^8

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Question No : 15 of 52

Marks: 1 (Budgeted Time 1 Min)

In which one of the following methods, does the CPU poll to identify the interrupting module and branch to an interrupt service routine on detecting an interrupt?

Answer (Please select your correct option)

Multiple interrupt lines

Software Poll

Daisy Chain

Parallel Priority



correct answer
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Question No : 16 of 52

Marks: 1 (Budgeted Time 1 Min)

Maskable Interrupts are applied to the _____ pin of the processor.

Answer (Please select your correct option)

INTR



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NMI

IRET

INT

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Question No : 17 of 52

Marks: 1 (Budgeted Time 1 Min)

Non-maskable Interrupts are detected using the _____ pin of the processor.

Answer (Please select your correct option)

INTR

NMI

IRET

INT



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Question No : 18 of 52

Marks: 1 (Budgeted Time 1 Min)

In Multiple Interrupt Lines approach, a number of interrupt lines are provided between the _____ modules.

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Answer (Please select your correct option)

External and Internal

CPU and I/O

CPU and Memory

Memory and I/O



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Question No : 19 of 52

Marks: 1 (Budgeted Time 1 Min)

In FALCON-A assembler and simulator (FALSIM), variables are defined by using the _____ directive.

Answer (Please select your correct option)

.bin

.equ

.iret

.end



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Question No : 20 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ allows a peripheral device to read from and/or write to memory without intervention by the CPU.

Answer (Please select your correct option)

Programmed I/O

Interrupt driven I/O

Direct memory access

Polling



correct answer
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Question No : 21 of 52

Marks: 1 (Budgeted Time 1 Min)

Taking control of the system bus for a few bus cycles is known as _____.

Answer (Please select your correct option)

Bus Scheduling

Cycle Stealing

Cycle Transferring

CPU Scheduling



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Question No : 22 of 52

Marks: 1 (Budgeted Time 1 Min)

A component connected to the system bus and having control of it during a particular bus cycle is called _____.

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Answer (Please select your correct option)

Address decoder

BIOS

Master component

Slave component



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Question No : 23 of 52

Marks: 1 (Budgeted Time 1 Min)

When it is required to read data from a particular location of the disk, the head moves towards the selected track and this process is called _____.

Answer (Please select your correct option)

Fragmentation

Defragmentation

Seek

Encoding

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Question No : 24 of 52

Marks: 1 (Budgeted Time 1 Min)

CRC has _____ overhead as compared to Hamming code.

Answer (Please select your correct option)

Equal

Greater

Lesser

Absolutely no



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Question No : 25 of 52

Marks: 1 (Budgeted Time 1 Min)

The conversion of numbers from a representation in one base to another is known as _____

Answer (Please select your correct option)

Radix Conversion



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Number Representation

Decimal Representation

Hexadecimal Representation

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Question No : 26 of 52

Marks: 1 (Budgeted Time 1 Min)

In _____, bits are encoded in pairs so there are only ' $n/2$ ' additions instead of ' n '.

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Answer (Please select your correct option)

Booth Recoding

Bit-Pair Recoding

Fraction Division

Integer Division



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Question No : 27 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ are computed by the ALU and stored in processor status register.

Answer (Please select your correct option)

Condition Codes



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Control Signals

Flip Flops

Multiplexers

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Question No : 28 of 52

Marks: 1 (Budgeted Time 1 Min)

In computers, floating-point representation uses _____ to encode significand, exponent and their sign in a single word.

Answer (Please select your correct option)

Decimal Numbers

Binary Numbers

Octal Numbers

Hexadecimal Numbers



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Question No : 29 of 52

Marks: 1 (Budgeted Time 1 Min)

In floating point representations _____ is also called mantissa.

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Answer (Please select your correct option)

Sign

Base

Significand

Exponent



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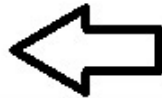
Question No : 30 of 52

Marks: 1 (Budgeted Time 1 Min)

In Single-Precision Binary Floating Point Representation the size of exponent is _____.

Answer (Please select your correct option)

8-bits



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11-bits

1-bits

23-bits

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Question No : 31 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ is nonvolatile i.e. it retains the information in it when power is removed from it.

Answer (Please select your correct option)

RAM

DRAM

ROM

SRAM



correct answer
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Question No : 32 of 52

Marks: 1 (Budgeted Time 1 Min)

Based on the statistical results, the cache block which has been least used in the recent past, is replaced with a new block. This technique is called _____.

Answer (Please select your correct option)

Always Replacement

Random Replacement

LFU (Least Frequently Used)

Write Allocate



correct answer
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Question No : 33 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ acts as a cache between main memory and secondary memory.

Answer (Please select your correct option)

Read Only Memory

Flash Memory

Virtual Memory

Magnetic Tape



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Marks: 1 (Budgeted Time 1 Min)

Randomly replacing any older page to bring in the desired page is known as _____.

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Answer (Please select your correct option)

Always Replacement

LFU (Least Frequently Used)

Random Replacement

Fragmentation



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Question No : 35 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ are formed by concatenating the page number with the word number.

Answer (Please select your correct option)

Memory chips

Protocols

Hazards

Virtual addresses



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Question No : 36 of 52

Marks: 1 (Budgeted Time 1 Min)

In _____ technique memory is divided into segments of variable sizes depending upon the requirements.

Answer (Please select your correct option)

Multiplexing

Segmentation

Hamming code

Partial decoding



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Question No : 37 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ depends upon the average number of calls and the service time taken by a particular server.

Answer (Please select your correct option)

Throughput



right

Latency

Poisson Distribution

Response Time

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Question No : 38 of 52

Marks: 1 (Budgeted Time 1 Min)

_____ is also called traffic intensity and its value must be between 0 and 1.

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Answer (Please select your correct option)

Little's Law

Poisson Distribution

Server Utilization

SPEC

correct answer
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Question No : 39 of 52

Marks: 1 (Budgeted Time 1 Min)

The time for the message to pass through the network, except the time of flight is called _____.

Answer (Please select your correct option)

Transmission Time



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Latency

Transport Latency

Bandwidth

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Question No : 40 of 52

Marks: 1 (Budgeted Time 1 Min)

A set of rules followed by different components in a network is called _____.

Answer (Please select your correct option)

Host

Connectivity

Resource Sharing

Protocol



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Question No : 41 of 52

Marks: 2 (Budgeted Time 4 Min)

What is the size of address bus in external CPU interface of FALCON-A?

Answer (Please [click here](#) to Add Answer)



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Question No : 42 of 52

Marks: 2 (Budgeted Time 4 Min)

What happens when one drive fails in RAID 0?

Answer (Please [click here](#) to Add Answer)



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Differentiate between Spatial Locality and Temporal Correlation.

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Answer (Please [click here](#) to Add Answer)



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Question No : 44 of 52

Marks: 2 (Budgeted Time 4 Min)

What is the purpose of virtual memory?

Answer (Please [click here](#) to Add Answer)



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Question No : 45 of 52

Marks: 3 (Budgeted Time 6 Min)

Differentiate between sender overhead and receiver overhead related to performance issues of networks.

Answer (Please [click here](#) to Add Answer)

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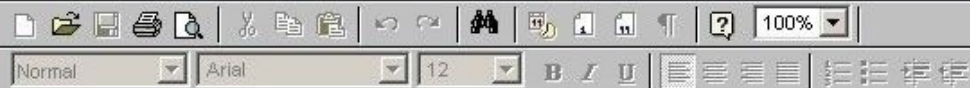
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Identify the following structural RTL of an instruction and **rewrite** it in SRC assembly language.

Step	RTL
T0-T2	Instruction fetch
---	---

Answer (Please [click here](#) to Add Answer)



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When do we use the following file extensions in FALSIM?

- .asmfa
- .lstfa
- .binfa

Answer (Please [click here](#) to Add Answer)

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Question No : 48 of 52

Marks: 3 (Budgeted Time 6 Min)

How many inputs and outputs are available in one level decoder? Also describe the usage of matrix decoders.

Answer (Please [click here](#) to Add Answer)

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Normal Arial 12 B I U

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Write SRC assembly language program to evaluate the following expression

$$Z=4(a+b)-8(c-27)$$

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Answer (Please [click here](#) to Add Answer)



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Question No : 50 of 52

Marks: 5 (Budgeted Time 10 Min)

Assume that a floppy drive having a transfer rate of 25KB per second is attached to a 32 bit, 10MIPS CPU using an interrupt driven interface. The drive has a 16-bit data bus. Assume that the interrupt overhead is 10 instructions. Calculate the fraction of CPU time required to service this drive when it is active.

Answer (Please [click here](#) to Add Answer)



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Question No : 51 of 52

Marks: 5 (Budgeted Time 10 Min)

Show all the steps involved in integer division algorithm to divide 45_{10} by 5_{10} .

Answer (Please [click here](#) to Add Answer)

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Find the average access time of a level of memory hierarchy if the hit rate is 80%. The memory access takes 10ns on a hit and 100ns on a miss.

A Good Education is a Foundation For a Better Future

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Answer (Please [click here](#) to Add Answer)



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