

VIRTUAL UNIVERSITY OF PAKISTAN

A Good Education is a Foundation For a Better Future

CORRECT ANSWER SOLVED BY HADI
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(Final Term Past Paper)

(Made by Waqar & Solved By Hadi)

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



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Best of luck!



Question # 1 of 10 (Start time: 03:42:15 PM, 29 January 2021)

Total Marks: 1

The _____ can be determined from the number of platters and the number of tracks.

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Select the correct option

<input type="radio"/>	Speed of processing	//
<input type="radio"/>	execution time	//
<input checked="" type="radio"/>	storage capacity	
<input type="radio"/>	Latency	

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Question # 3 of 10 (Start time: 03:43:55 PM, 29 January 2021)

Total Marks: 1

_____ is an electrical pathway through which the processor communicates with the internal and external devices attached to the computer.

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Select the correct option

<input checked="" type="radio"/>	Computer Bus	//
<input type="radio"/>	Hazard	//
<input type="radio"/>	Memory	//
<input type="radio"/>	Disk	//

Question # 6 of 10 (Start time: 03:46:14 PM, 29 January 2021)

Total Marks: 1

Identify the type of serial communication error condition in which "0" is received instead of a stop bit (which is always a "1")?

Select the correct option

<input type="radio"/>	Framing error	
<input type="radio"/>	Parity error	
<input checked="" type="radio"/>	Overrun error	
<input type="radio"/>	Under-run error	

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Question # 10 of 10 (Start time: 03:47:58 PM, 29 January 2021)

Total Marks: 1

_____ means that the CPU should input data from an input device only when the device is ready to provide data and send data to an output device only when it is ready to receive data.

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Select the correct option

<input type="radio"/>	Data location	//
<input checked="" type="radio"/>	Data synchronization	//
<input type="radio"/>	Data transfer	//
<input type="radio"/>	Asynchronous transmission	//

Question # 5 of 10 (Start time: 03:45:14 PM, 29 January 2021)

Total Marks: 1

Which one of the following is NOT a technique used when the CPU wants to exchange data with a peripheral device?

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Select the correct option

<input type="radio"/>	Direct Memory Access (DMA)	//
<input type="radio"/>	Interrupt driven I/O	//
<input type="radio"/>	Programmed I/O	
<input checked="" type="radio"/>	Virtual Memory	

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Question # 6 of 10 (Start time: 08:56:42 PM, 06 August 2021)

Total Marks: 1

_____ depends upon the present position of the head and the position of the required sector.

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Select the correct option

<input type="radio"/>	Execution time
<input type="radio"/>	Direct memory Access
<input checked="" type="radio"/>	Seek time
<input type="radio"/>	Throughput

Question # 7 of 10 (Start time: 03:46:42 PM, 29 January 2021)

Total Marks: 1

In _____, a separate address space of the CPU is reserved for I/O operations.

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Select the correct option

<input checked="" type="radio"/>	Isolated I/O	//
<input type="radio"/>	Memory Mapped I/O	//
<input type="radio"/>	All of above	
<input type="radio"/>	None of above	

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Question # 9 of 10 (Start time: 03:47:28 PM, 29 January 2021)

Total Marks: 1

In which one of the following methods, does the CPU poll to identify the interrupting module and branches to an interrupt service routine on detecting an interrupt?

Select the correct option

<input type="radio"/>	Daisy Chain	
<input checked="" type="radio"/>	Software Poll	
<input type="radio"/>	Multiple interrupt lines	
<input type="radio"/>	All of the given options	

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Question # 1 of 10 (Start time: 08:11:40 PM, 24 August 2021)

Total Marks: 1

The register file is a collection of _____ bit wide registers used for data transfer between memory and the CPU .

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Select the correct option

<input type="radio"/>	8
<input type="radio"/>	16
<input type="radio"/>	64
<input checked="" type="radio"/>	32

Question # 2 of 10 (Start time: 08:52:03 PM, 06 August 2021)

Total Marks: 1

The Pentium does allow the use of some part of its ____ accumulator register EAX

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Select the correct option

<input type="radio"/>	64 bits
<input checked="" type="radio"/>	32 bits
<input type="radio"/>	8 bits
<input type="radio"/>	16 bits

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Question # 4 of 10 (Start time: 08:54:26 PM, 06 August 2021)

Total Marks: 1

In 8086/8088 processor, interrupt vector table is located at the memory location _____.

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Select the correct option

- | | |
|----------------------------------|------|
| <input checked="" type="radio"/> | 0 |
| <input type="radio"/> | 4 |
| <input type="radio"/> | 1024 |
| <input type="radio"/> | 256 |

Question # 7 of 10 (Start time: 08:57:25 PM, 06 August 2021)

Total Marks: 1

ET = _____.

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Select the correct option

<input type="radio"/>	CPI x IC / T
<input type="radio"/>	CPI / IC x T
<input checked="" type="radio"/>	CPI x IC x T
<input type="radio"/>	CP x IC x T

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Question # 2 of 10 (Start time: 08:12:22 PM, 24 August 2021)

Total Marks: 1

The _____ signal coming from the CPU tells the memory that some interaction is required between the CPU and memory.

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Select the correct option

<input type="radio"/>	R/W
<input type="radio"/>	None of the given
<input checked="" type="radio"/>	REQUEST
<input type="radio"/>	COMPLETE

Question # 5 of 10 (Start time: 08:15:16 PM, 24 August 2021)

Total Marks: 1

A combination of parallel and sequential hardware used to build a multiplier is known as _____

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Select the correct option

<input checked="" type="radio"/>	Series Parallel Multiplier
<input type="radio"/>	None of the given
<input type="radio"/>	Parallel Array Multiplier
<input type="radio"/>	Booth Recording

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Question # 5 of 10 (Start time: 08:55:38 PM, 06 August 2021)

Total Marks: 1

In which one of the following methods, does the CPU poll to identify the interrupting module and branches to an interrupt service routine on detecting an interrupt?

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Select the correct option

- | | |
|----------------------------------|--------------------------|
| <input type="radio"/> | All of the given options |
| <input type="radio"/> | Multiple interrupt lines |
| <input type="radio"/> | Daisy Chain |
| <input checked="" type="radio"/> | Software Poll |

Question # 9 of 10 (Start time: 08:59:31 PM, 06 August 2021)

Total Marks: 1

Taking control of the system bus for a few bus cycles is known as _____.

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Select the correct option

<input type="radio"/>	Bus Stealing
<input type="radio"/>	Cycle Transferring
<input type="radio"/>	None of given
<input checked="" type="radio"/>	Cycle Stealing

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Question # 10 of 10 (Start time: 09:00:24 PM, 06 August 2021)

Total Marks: 1

Raid level _____ distributes the parity strips across all disks.

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Select the correct option

<input checked="" type="radio"/>	5
<input type="radio"/>	4
<input type="radio"/>	2
<input type="radio"/>	3

Question # 4 of 10 (Start time: 08:14:14 PM, 24 August 2021)

Total Marks: 1

_____ refers to the fact when a given address has been referenced, the next address is highly probable to be accessed within a short period of time

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Select the correct option

<input type="radio"/>	Half Locality
<input type="radio"/>	Temporal Locality
<input checked="" type="radio"/>	Spatial Locality
<input type="radio"/>	Full Locality

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Question # 3 of 10 (Start time: 08:53:34 PM, 06 August 2021)

Total Marks: 1

A component connected to the system bus and having control of it during a particular bus cycle is called _____.

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Select the correct option

<input type="radio"/>	System bus
<input type="radio"/>	Buffer component
<input checked="" type="radio"/>	Master component
<input type="radio"/>	Slave component

Question # 3 of 10 (Start time: 08:13:39 PM, 24 August 2021)

Total Marks: 1

The cache contains a copy of portions of the _____.

Select the correct option

<input type="radio"/>	Flash Memory
<input type="radio"/>	Rom
<input type="radio"/>	EPROM
<input checked="" type="radio"/>	Main memory

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Question # 8 of 10 (Start time: 08:58:47 PM, 06 August 2021)

Total Marks: 1

-----the device usually means reading its status register every so often until the device's status changes to indicate that it has completed the request.

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Select the correct option

<input type="radio"/>	Polling
<input checked="" type="radio"/>	Executing
<input type="radio"/>	Interrupting
<input type="radio"/>	Masking

Question # 6 of 10 (Start time: 08:16:08 PM, 24 August 2021)

Total Marks: 1

Adding a data pin to a chip with 2^m words of s bits increases the number of bits it can store by only a factor of _____

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Select the correct option

<input type="radio"/>	$(s+2)/s$
<input checked="" type="radio"/>	$(s+1)/s$
<input type="radio"/>	s^2/s
<input type="radio"/>	$s/(s+1)$

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Question # 10 of 10 (Start time: 08:18:03 PM, 24 August 2021)

Total Marks: 1

The _____ of an m digit number x is $\{x^c\} = (b^m - 1 - x)$

Select the correct option

- | | |
|----------------------------------|-----------------------------|
| <input type="radio"/> | Biased Representation |
| <input checked="" type="radio"/> | Diminished Radix Compliment |
| <input type="radio"/> | Signed Magnitude Fom |
| <input type="radio"/> | Radix Compliment |

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Question # 8 of 10 (Start time: 08:17:09 PM, 24 August 2021)

Total Marks: 1

_____ is the simplest form for representing a signed number

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Select the correct option

<input type="radio"/>	Biased Representation
<input checked="" type="radio"/>	Sign Magnitude Form
<input type="radio"/>	None of the given
<input type="radio"/>	Diminished Radix Compliment Form

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- 3) GDB
- 4) GRAND QUIZZES

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