

CS501 - Advance Computer Architecture

Grand Quiz 2020 (Covid-19)

Solved By VU-Tech-Soft



Question # 1 of 30 (Start time: 10:17:15 PM, 04 July 2020)

The multiplexer _____ is used to decide which value is transferred to be written back to the register file.

Select the correct option

- | | |
|----------------------------------|-----|
| <input type="radio"/> | MP2 |
| <input type="radio"/> | MP3 |
| <input type="radio"/> | MP4 |
| <input checked="" type="radio"/> | MP5 |

Question # 2 of 30 (Start time: 10:17:50 PM, 04 July 2020)

Which of the following conditions is evaluated when executing the branch instruction "brzr R2, R1"?

Select the correct option

- | | |
|----------------------------------|--------------|
| <input type="radio"/> | if (R2 == 0) |
| <input type="radio"/> | if (R1 > 0) |
| <input checked="" type="radio"/> | if (R1 == 0) |
| <input type="radio"/> | if (R1 < 0) |

Question # 3 of 30 (Start time: 10:18:35 PM, 04 July 2020)

In case of SRC processor, bits _____ of IR (instruction register) are reserved for the op-code.

Select the correct option

- | | |
|----------------------------------|----------|
| <input type="radio"/> | 0 to 4 |
| <input type="radio"/> | 11 to 15 |
| <input checked="" type="radio"/> | 27 to 31 |
| <input type="radio"/> | 59 to 63 |

Question # 4 of 30 (Start time: 10:19:58 PM, 04 July 2020)

Which of the given RTL descriptions is used to represent "load register relative (ldr)" instruction?

Select the correct option

- | | |
|----------------------------------|--|
| <input type="radio"/> | $(op<4..0>= 6) : R [ra] \leftarrow rel$ |
| <input checked="" type="radio"/> | $(op<4..0>= 2) : R [ra] \leftarrow M [rel]$ |
| <input type="radio"/> | $(op<4..0>= 3) : M [disp] \leftarrow R [ra]$ |
| <input type="radio"/> | $(op<4..0>= 4) : M[rel] \leftarrow R[ra]$ |

Question # 5 of 50 (Start time: 10:20:00 AM, 07 July 2020)

_____ instruction is used to divide a register value by the immediate value in FALCON-E processor.

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | div |
| <input type="radio"/> | idiv |
| <input checked="" type="radio"/> | divi |
| <input type="radio"/> | divim |

Question # 6 of 30 (Start time: 10:21:31 PM, 04 July 2020)

Which field of the machine language instruction is the "type of operation" that is to be performed?

Select the correct option

<input checked="" type="radio"/>	Op-code (or the operation code)
<input type="radio"/>	CPU registers
<input type="radio"/>	Memory cells
<input type="radio"/>	I/O locations

Question # 7 of 30 (Start time: 10:22:20 PM, 04 July 2020)

Which of the following control signals is NOT activated during instruction fetch operation?

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | PCout |
| <input type="radio"/> | LC |
| <input checked="" type="radio"/> | LMAR |
| <input type="radio"/> | Cout |

Question # 8 of 30 (Start time: 10:23:19 PM, 04 July 2020)

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In case of FALCON-A, _____ instructions are present which are not present in the SRC processor.

Select the correct option

- | | |
|----------------------------------|--------------------|
| <input type="radio"/> | create and destroy |
| <input checked="" type="radio"/> | in and out |
| <input type="radio"/> | open and close |
| <input type="radio"/> | read and write |

Question # 10 of 30 (Start time: 10:25:31 PM, 04 July 2020)

_____ provides a temporary storage for the address of memory location to be accessed.

Select the correct option

- | | |
|----------------------------------|-----|
| <input checked="" type="radio"/> | MAR |
| <input type="radio"/> | MBR |
| <input type="radio"/> | PC |
| <input type="radio"/> | LPC |

Question # 11 of 30 (Start time: 10:26:01 PM, 04 July 2020)

Which of the following register is used to enable the tri-state buffers with the MBR?

Select the correct option

<input checked="" type="radio"/>	MBRout
<input type="radio"/>	MARout
<input type="radio"/>	LMBR
<input type="radio"/>	INC4

Question # 12 of 30 (Start time: 10:26:50 PM, 04 July 2020)

What functionality is performed by the instruction "str R8, 34" of SRC?

Select the correct option

- | | |
|----------------------------------|---|
| <input type="radio"/> | It will load the register R8 with the contents of the memory location M [PC+34] |
| <input type="radio"/> | It will load the register R8 with the contents of the memory location M [34] |
| <input checked="" type="radio"/> | It will store the register R8 contents to the memory location M [PC+34] |
| <input type="radio"/> | It will store the register R8 contents to the memory location M [34] |

Question # 13 of 30 (Start time: 10:27:51 PM, 04 July 2020)

Program counter (PC) holds the memory address of _____ ?

Select the correct option

- | | |
|----------------------------------|----------------------------------|
| <input type="radio"/> | Previous instruction |
| <input type="radio"/> | Current instruction |
| <input checked="" type="radio"/> | Next instruction |
| <input type="radio"/> | Previous and current instruction |

Question # 14 of 30 (Start time: 10:28:13 PM, 04 July 2020)

What is the working of Processor Status Word (PSW)?

Select the correct option

- | | |
|----------------------------------|--|
| <input checked="" type="radio"/> | To hold the current status of the processor |
| <input type="radio"/> | To hold the address of the current process |
| <input type="radio"/> | To hold the instruction that the computer is currently processing |
| <input type="radio"/> | To hold the address of the next instruction in memory that is to be executed |

Question # 15 of 30 (Start time: 10:29:03 PM, 04 July 2020)

mul is the example of a(n) _____ operation.

Select the correct option

- | | |
|----------------------------------|---------------|
| <input type="radio"/> | Logic |
| <input type="radio"/> | Shift |
| <input checked="" type="radio"/> | Arithmetic |
| <input type="radio"/> | Data transfer |
- Your internet connection is unstable

Question # 16 of 30 (Start time: 10:29:26 PM, 04 July 2020)

Control signal for RTL "IR \leftarrow MBR" will be _____

Select the correct option

- | | |
|----------------------------------|---------------------|
| <input checked="" type="radio"/> | MBRout, LIR |
| <input type="radio"/> | PC \leftarrow C |
| <input type="radio"/> | PC \leftarrow MBR |
| <input type="radio"/> | PC \leftarrow IR |

Question # 17 of 30 (Start time: 10:30:54 PM, 04 July 2020)

What does the instruction "ldr R3, 58" of SRC do?

Select the correct option

- | | |
|----------------------------------|---|
| <input type="radio"/> | It will load register R3 with the contents of the memory location M[PC+58] |
| <input checked="" type="radio"/> | It will load register R3 with the relative address itself (PC+58) |
| <input type="radio"/> | It will store register R3 contents to the memory location M[PC+58] |
| <input type="radio"/> | It will store the value of register R3 at the relative address itself (PC+58) |

Question # 18 of 30 (Start time: 10:32:18 PM, 04 July 2020)

The status register of the 68000 has ____ condition codes.

Select the correct option

- | | |
|----------------------------------|---|
| <input type="radio"/> | 2 |
| <input type="radio"/> | 3 |
| <input checked="" type="radio"/> | 5 |
| <input type="radio"/> | 8 |

Question # 19 of 30 (Start time: 10:33:27 PM, 04 July 2020)

Which of the instruction is used to load register from memory using a relative address?

Select the correct option

<input type="radio"/>	ld instruction
<input checked="" type="radio"/>	ldr instruction
<input type="radio"/>	lar instruction
<input type="radio"/>	str instruction

For the _____ type instructions, we require a register to hold the data that is to be loaded from the memory, or stored back to the memory

Select the correct option

- | | |
|----------------------------------|------------|
| <input type="radio"/> | Jump |
| <input type="radio"/> | Control |
| <input checked="" type="radio"/> | load/store |
| <input type="radio"/> | Branch |

Question # 21 of 30 (Start time: 10:34:24 PM, 04 July 2020)

In a processor, _____ is responsible for the synchronization of internal as well as external events.

Select the correct option

- | | |
|----------------------------------|---------------------------|
| <input type="radio"/> | Memory Unit |
| <input type="radio"/> | Data Unit |
| <input type="radio"/> | Arithmetic & Logical Unit |
| <input checked="" type="radio"/> | Control Unit |

Question # 22 of 30 (Start time: 10:35:00 PM, 04 July 2020)

In CPU design, _____ creates or forms the interface between the data path and the control unit.

Select the correct option

- | | |
|----------------------------------|----------------|
| <input type="radio"/> | Busses |
| <input type="radio"/> | ALU |
| <input checked="" type="radio"/> | Control Signal |
| <input type="radio"/> | Cache |

Question # 23 of 30 (Start time: 10:35:44 PM, 04 July 2020)

_____ control signal enables the input to the PC for receiving a value that is currently on the internal processor bus.

Select the correct option

<input checked="" type="radio"/>	LPC
<input type="radio"/>	INC4
<input type="radio"/>	LC
<input type="radio"/>	Cout

Question # 24 of 30 (Start time: 10:36:39 PM, 04 July 2020)

In SRC, the effective address is computed at run-time by adding a constant to value of _____ register .

Select the correct option

<input type="radio"/>	FLAGS
<input type="radio"/>	IR
<input checked="" type="radio"/>	PC
<input type="radio"/>	Ra

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Question # 25 of 30 (Start time: 10:37:51 PM, 04 July 2020)

_____ control signal allows the contents of the Program Counter register to be written onto the internal processor bus.

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | INC4 |
| <input type="radio"/> | LPC |
| <input checked="" type="radio"/> | PCout |
| <input type="radio"/> | LC |

Question # 26 of 30 (Start time: 10:38:30 PM, 04 July 2020)

In _____ instruction format of EAGLE processor, there is no field reserved for operands.

Select the correct option

- | | |
|----------------------------------|--------|
| <input type="radio"/> | Type V |
| <input type="radio"/> | Type X |
| <input type="radio"/> | Type Y |
| <input checked="" type="radio"/> | Type Z |

Question # 27 of 30 (Start time: 10:39:10 PM, 04 July 2020)

In Type C instruction of SRC, _____ bits are allocated for constant value.

Select the correct option

- | | |
|----------------------------------|----|
| <input type="radio"/> | 16 |
| <input checked="" type="radio"/> | 17 |
| <input type="radio"/> | 21 |
| <input type="radio"/> | 22 |

Question # 28 of 30 (Start time: 10:40:06 PM, 04 July 2020)

A general purpose digital computer has _____ main components.

Select the correct option

- | | |
|----------------------------------|---|
| <input type="radio"/> | 2 |
| <input checked="" type="radio"/> | 3 |
| <input type="radio"/> | 4 |
| <input type="radio"/> | 5 |

Question # 29 of 30 (Start time: 10:41:55 PM, 04 July 2020)

The instruction _____ will load the register R3 with the contents of the memory location M [PC+56]

Select the correct option

- | | |
|----------------------------------|---------------|
| <input type="radio"/> | lwr R3, M[56] |
| <input type="radio"/> | ldr R3, M[56] |
| <input checked="" type="radio"/> | ldr R3, 56 |
| <input type="radio"/> | lwr R3, [56] |

Question # 30 of 30 (Start time: 10:41:58 PM, 04 July 2020)

In FALCON-A instruction format of TYPE-2 constants and variable should be in the range of

Select the correct option

- | | |
|----------------------------------|--------------|
| <input type="radio"/> | -132 to +131 |
| <input type="radio"/> | -164 to +163 |
| <input type="radio"/> | -32 to +31 |
| <input checked="" type="radio"/> | -128 to +127 |

Question # 1 of 30 (Start time: 10:50:19 PM, 04 July 2020)

In a Falcon-A assembly program, labels are used to implement _____ jump.

Select the correct option

<input type="radio"/>	Direct
<input type="radio"/>	Indirect
<input checked="" type="radio"/>	Relative
<input type="radio"/>	Displacement

Question # 2 of 30 (Start time: 10:51:32 PM, 04 July 2020)

In "Jump [8]" instruction, the size of the constant field is _____ bits.

Select the correct option

- | | |
|----------------------------------|----|
| <input type="radio"/> | 4 |
| <input type="radio"/> | 5 |
| <input checked="" type="radio"/> | 8 |
| <input type="radio"/> | 16 |

The multiplexer _____ is used to decide which value is transferred to be written back to the register file.

Select the correct option

- | | |
|----------------------------------|-----|
| <input type="radio"/> | MP2 |
| <input type="radio"/> | MP3 |
| <input type="radio"/> | MP4 |
| <input checked="" type="radio"/> | MP5 |

_____ is a register which takes input from the ALSU as a memory address to be accessed and transfers the memory contents on that location onto the memory sub-system.

Select the correct option

- | | | |
|----------------------------------|-----|----|
| <input type="radio"/> | PC | // |
| <input type="radio"/> | MBR | // |
| <input checked="" type="radio"/> | MAR | // |
| <input type="radio"/> | IR | // |

Question # 5 of 30 (Start time: 10:53:24 PM, 04 July 2020)

Total M

In pipe-lined processors, there should be a _____ port register file so that if the register write and register read stages overlap, they can be performed in parallel.

Select the correct option

<input type="radio"/>	Four
<input checked="" type="radio"/>	Three
<input type="radio"/>	Two
<input type="radio"/>	One

▶ Sa

Question # 7 of 30 (Start time: 10:54:29 PM, 04 July 2020)

Which of the following registers is used as an implicit operand in MUL/DIV instruction of Falcon-A?

Select the correct option

- | | |
|----------------------------------|----|
| <input checked="" type="radio"/> | R0 |
| <input type="radio"/> | PC |
| <input type="radio"/> | IR |
| <input type="radio"/> | SP |

Question # 8 of 30 (Start time: 10:55:38 PM, 04 July 2020)

_____ instruction is used to divide a register value by the immediate value in FALCON-E processor.

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | div |
| <input type="radio"/> | idiv |
| <input checked="" type="radio"/> | divi |
| <input type="radio"/> | divim |

Question # 9 of 30 (Start time: 10:55:57 PM, 04 July 2020)

Which one of the following control signals causes the data from the bus to be read into the register MAR.

Select the correct option

- | | |
|----------------------------------|-------------------|
| <input type="radio"/> | MARout |
| <input type="radio"/> | MARin |
| <input checked="" type="radio"/> | LMAR |
| <input type="radio"/> | None of the given |

Question # 10 of 30 (Start time: 10:56:43 PM, 04 July 2020)

Which one of the following operations is NOT performed by using miscellaneous instructions?

Select the correct option

- | | |
|----------------------------------|----------------------------|
| <input type="radio"/> | Clearing all registers |
| <input type="radio"/> | Stopping the processor |
| <input type="radio"/> | NOP |
| <input checked="" type="radio"/> | Returning from a procedure |

Question # 11 of 30 (Start time: 10:57:30 PM, 04 July 2020)

To set the value of micro-PC from branch address, the value of 4 to 1 multiplexer is _____.

Select the correct option

- | | |
|----------------------------------|----|
| <input type="radio"/> | 00 |
| <input type="radio"/> | 01 |
| <input type="radio"/> | 10 |
| <input checked="" type="radio"/> | 11 |

Question # 12 of 30 (Start time: 10:58:33 PM, 04 July 2020)

The instruction "PUSH A" is an example of _____.

Select the correct option

- | | |
|----------------------------------|-----------------------|
| <input checked="" type="radio"/> | 0-address instruction |
| <input type="radio"/> | 1-address instruction |
| <input type="radio"/> | 2-address instruction |
| <input type="radio"/> | 3-address instruction |

Question # 13 of 30 (Start time: 10:58:58 PM, 04 July 2020)

Which of the following branch instructions has a condition which is always executed?

Select the correct option

- | | |
|----------------------------------|------|
| <input type="radio"/> | JZ |
| <input checked="" type="radio"/> | JUMP |
| <input type="radio"/> | JPL |
| <input type="radio"/> | JMI |

Question # 14 of 30 (Start time: 10:59:49 PM, 04 July 2020)

_____ hazard occurs when attempting to access the same resource in different ways at the same time

Select the correct option

- | | |
|----------------------------------|-------------|
| <input type="radio"/> | Branch |
| <input type="radio"/> | Data |
| <input checked="" type="radio"/> | Structural |
| <input type="radio"/> | Instruction |

Question # 15 of 30 (Start time: 11:00:38 PM, 04 July 2020)

_____ is an example of Miscellaneous instruction.

Select the correct option

- | | |
|----------------------------------|--------|
| <input type="radio"/> | Shiftl |
| <input type="radio"/> | Store |
| <input checked="" type="radio"/> | Halt |
| <input type="radio"/> | Call |

Question # 16 of 30 (Start time: 11:01:05 PM, 04 July 2020)

Which type of instructions enables mathematical computations?

Select the correct option

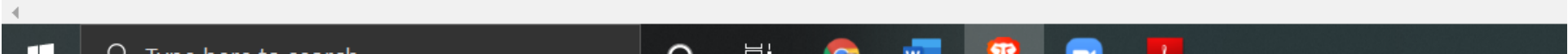
<input checked="" type="radio"/>	Arithmetic
<input type="radio"/>	Control
<input type="radio"/>	Data transfer
<input type="radio"/>	Numeric

Question # 17 of 30 (Start time: 11:01:15 PM, 04 July 2020)

VLIW stands for _____.

Select the correct option

- Variable Length Instruction Word
- Very Long Instruction Word
- Very Long Instruction Width
- Variable Length Instruction Width



Question # 18 of 30 (Start time: 11:01:40 PM, 04 July 2020)

Which of the following is NOT related to the architecture of a computer?

Select the correct option

- | | |
|----------------------------------|-------------------------|
| <input type="radio"/> | Instruction set |
| <input checked="" type="radio"/> | Control signals |
| <input type="radio"/> | I/O mechanisms |
| <input type="radio"/> | Memory addressing modes |

Question # 19 of 30 (Start time: 11:02:42 PM, 04 July 2020)

In SRC, the general-purpose register file includes _____ registers, each 32 bit wide.

Select the correct option

- | | |
|----------------------------------|------------------------|
| <input type="radio"/> | 16 registers R0 to R15 |
| <input type="radio"/> | 24 registers R0 to R23 |
| <input checked="" type="radio"/> | 32 registers R0 to R31 |
| <input type="radio"/> | 64 registers R0 to R63 |

Question # 20 of 30 (Start time: 11:03:34 PM, 04 July 2020)

In SRC, which of the following is a notation which is used to represent 32-bit memory word stored at address starting from 56?

Select the correct option

- | | |
|----------------------------------|---|
| <input checked="" type="radio"/> | $M[56]<31\dots 0> := M[56] M[57] M[58] M[59]$ |
| <input type="radio"/> | $M[56]<0\dots 31> := M[56] M[57] M[58] M[59]$ |
| <input type="radio"/> | $M[56]<0\dots 31> := M[59] M[58] M[57] M[56]$ |
| <input type="radio"/> | $M[56]<31\dots 0> := M[59] M[58] M[57] M[56]$ |

Question # 21 of 30 (Start time: 11:05:01 PM, 04 July 2020)

Which of the following RTL descriptions is used for specifying the operation of an SRC instruction?

Select the correct option

- | | |
|----------------------------------|------------|
| <input type="radio"/> | IR<31..27> |
| <input checked="" type="radio"/> | IR<22..26> |
| <input type="radio"/> | IR<21..17> |
| <input type="radio"/> | IR<21..0> |

Question # 22 of 30 (Start time: 11:06:06 PM, 04 July 2020)

Among the two approaches available to design a control unit, hardwired approach is relatively _____ as compared to micro-programming.

Select the correct option

<input type="radio"/>	Slow
<input type="radio"/>	Average
<input checked="" type="radio"/>	Faster
<input type="radio"/>	Better

Question # 23 of 30 (Start time: 11:07:00 PM, 04 July 2020)

Which of the given RTL descriptions is used to represent "load register relative (ldr)" instruction?

Select the correct option



(op<4..0>= 6) : R [ra] ← rel



(op<4..0>= 2) : R [ra] ← M [rel]



(op<4..0>= 3) : M [disp] ← R [ra]



(op<4..0>= 4) : M[rel] ← R[ra]

Question # 24 of 30 (Start time: 11:08:09 PM, 04 July 2020)

The MAR is connected directly to the _____.

Select the correct option

- | | |
|----------------------------------|------------------|
| <input type="radio"/> | MBR |
| <input checked="" type="radio"/> | CPU internal bus |
| <input type="radio"/> | CPU external bus |
| <input type="radio"/> | LIC |

Question # 25 of 30 (Start time: 11:08:29 PM, 04 July 2020)

In a processor, _____ is responsible for the synchronization of internal as well as external events.

Select the correct option

- | | |
|----------------------------------|---------------------------|
| <input type="radio"/> | Memory Unit |
| <input type="radio"/> | Data Unit |
| <input type="radio"/> | Arithmetic & Logical Unit |
| <input checked="" type="radio"/> | Control Unit |

Question # 26 of 30 (Start time: 11:08:50 PM, 04 July 2020)

Which notation do we use to name different fields of a register in RTL?

Select the correct option

- | | |
|----------------------------------|----|
| <input type="radio"/> | () |
| <input type="radio"/> | <- |
| <input type="radio"/> | + |
| <input checked="" type="radio"/> | := |

Question # 27 of 30 (Start time: 11:09:27 PM, 04 July 2020)

In Type C instruction of SRC, the maximum size of constant field is _____ bits.

Select the correct option

- | | |
|----------------------------------|----|
| <input type="radio"/> | 8 |
| <input type="radio"/> | 16 |
| <input checked="" type="radio"/> | 17 |
| <input type="radio"/> | 27 |

Question # 28 of 30 (Start time: 11:09:58 PM, 04 July 2020)

Which of the following is not a part of processor state?

Select the correct option

- | | |
|----------------------------------|-----------|
| <input type="radio"/> | IR |
| <input type="radio"/> | PC |
| <input type="radio"/> | Stack |
| <input checked="" type="radio"/> | Registers |

Question # 29 of 30 (Start time: 11:10:53 PM, 04 July 2020)

Total

Which type of Instruction is stored in instruction register (IR)?

Select the correct option

- | | |
|----------------------------------|---|
| <input checked="" type="radio"/> | Current instruction being executed |
| <input type="radio"/> | Next instruction to be executed |
| <input type="radio"/> | Previously executed instruction |
| <input type="radio"/> | Next branch instruction in the instruction sequence |



Question # 30 of 30 (Start time: 11:11:59 PM, 04 July 2020)

_____ form the branch control field in the micro instruction.

Select the correct option

- | | |
|----------------------------------|--------|
| <input type="radio"/> | C Bits |
| <input type="radio"/> | M Bits |
| <input checked="" type="radio"/> | B Bits |
| <input type="radio"/> | A Bits |

Question # 16 of 30 (Start time: 11:29:17 PM, 04 July 2020)

During the RESET operation of processor, control step counter is set to _____.

Select the correct option

- | | |
|----------------------------------|----|
| <input checked="" type="radio"/> | 0 |
| <input type="radio"/> | 1 |
| <input type="radio"/> | 2 |
| <input type="radio"/> | -1 |

Question # 18 of 30 (Start time: 11:31:19 PM, 04 July 2020)

Total

An "assembler" that runs on one processor and translates an assembly language program written for another processor into the machine language of the other processor is called a -----



Select the correct option

<input type="radio"/>	compiler
<input checked="" type="radio"/>	cross assembler
<input type="radio"/>	debugger
<input type="radio"/>	linker

Question # 19 of 30 (Start time: 11:31:59 PM, 04 July 2020)

Which of the following conditions is evaluated when executing the branch instruction "brzr R2, R1"?

Select the correct option

- | | |
|----------------------------------|--------------|
| <input type="radio"/> | if (R2 == 0) |
| <input type="radio"/> | if (R1 > 0) |
| <input checked="" type="radio"/> | if (R1 == 0) |
| <input type="radio"/> | if (R1 < 0) |

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Question # 20 of 30 (Start time: 11:32:37 PM, 04 July 2020)

What is the instruction length of the FALCON-E processor?

Select the correct option

- | | |
|----------------------------------|---------|
| <input type="radio"/> | 8 bits |
| <input type="radio"/> | 16 bits |
| <input checked="" type="radio"/> | 32 bits |
| <input type="radio"/> | 64 bits |

Question # 21 of 30 (Start time: 11:34:00 PM, 04 July 2020)

In _____, only one field is required which specifies the type of operation.

Select the correct option

- | | |
|----------------------------------|-----------------------|
| <input type="radio"/> | 3-Address Instruction |
| <input type="radio"/> | 2-Address Instruction |
| <input type="radio"/> | 1-Address Instruction |
| <input checked="" type="radio"/> | 0-Address Instruction |

Question # 22 of 30 (Start time: 11:34:12 PM, 04 July 2020)

Which of the following control signals is NOT activated during instruction fetch operation?

Select the correct option

- | | |
|----------------------------------|-------|
| <input type="radio"/> | PCout |
| <input type="radio"/> | LC |
| <input type="radio"/> | LMAR |
| <input checked="" type="radio"/> | Cout |

Question # 23 of 30 (Start time: 11:35:45 PM, 04 July 2020)

Which of the following is not a part of processor state?

Select the correct option

- | | |
|----------------------------------|-----------|
| <input type="radio"/> | IR |
| <input type="radio"/> | PC |
| <input type="radio"/> | Stack |
| <input checked="" type="radio"/> | Registers |

Question # 24 of 30 (Start time: 11:35:53 PM, 04 July 2020)

In Falcon-A processor, the size of each I/O port is _____.

Select the correct option

- | | |
|----------------------------------|-----------|
| <input type="radio"/> | 16 bits |
| <input checked="" type="radio"/> | 8 bits |
| <input type="radio"/> | 8 bytes |
| <input type="radio"/> | 256 bytes |

Question # 24 of 30 (Start time: 11:35:53 PM, 04 July 2020)

In Falcon-A processor, the size of each I/O port is _____.

Select the correct option

- | | |
|----------------------------------|-----------|
| <input checked="" type="radio"/> | 16 bits |
| <input type="radio"/> | 8 bits |
| <input type="radio"/> | 8 bytes |
| <input type="radio"/> | 256 bytes |

Question # 25 of 30 (Start time: 11:37:27 PM, 04 July 2020)

In "DIV R3" instruction of EAGLE, the register _____ is used as both source operand and destination operand.

Select the correct option

- | | |
|----------------------------------|----|
| <input checked="" type="radio"/> | R0 |
| <input type="radio"/> | R1 |
| <input type="radio"/> | R2 |
| <input type="radio"/> | R3 |

Question # 26 of 30 (Start time: 11:38:57 PM, 04 July 2020)

In which of the following addressing modes, the address of the memory from where the value is to be accessed is given in the instruction?

Select the correct option

- | | |
|-------------------------------------|----------------------------|
| <input checked="" type="checkbox"/> | Direct Addressing |
| <input type="checkbox"/> | Immediate Addressing |
| <input type="checkbox"/> | Indirect Addressing |
| <input type="checkbox"/> | Register Direct Addressing |

Question # 26 of 30 (Start time: 11:38:57 PM, 04 July 2020)

In which of the following addressing modes, the address of the memory from where the value is to be accessed is given in the instruction?

Select the correct option

- | | |
|----------------------------------|----------------------------|
| <input type="radio"/> | Direct Addressing |
| <input type="radio"/> | Immediate Addressing |
| <input checked="" type="radio"/> | Indirect Addressing |
| <input type="radio"/> | Register Direct Addressing |

Question # 27 of 30 (Start time: 11:40:15 PM, 04 July 2020)

Which one of the following control signals causes the data from the bus to be read into the register MAR.

Select the correct option

- | | |
|----------------------------------|-------------------|
| <input type="radio"/> | MARout |
| <input type="radio"/> | MARin |
| <input checked="" type="radio"/> | LMAR |
| <input type="radio"/> | None of the given |

Question # 28 of 30 (Start time: 11:41:22 PM, 04 July 2020)

_____ is defined as the number of instructions processed per second.

Select the correct option

- | | |
|----------------------------------|----------------|
| <input type="radio"/> | Memory Access |
| <input checked="" type="radio"/> | Throughput |
| <input type="radio"/> | ALU Operations |
| <input type="radio"/> | Latency |

Question # 29 of 30 (Start time: 11:42:19 PM, 04 July 2020)

In Falcon-A ISA, which of the following opcodes is used to perform "No Operation"?

Select the correct option

- | | |
|----------------------------------|----|
| <input type="radio"/> | 20 |
| <input checked="" type="radio"/> | 21 |
| <input type="radio"/> | 22 |
| <input type="radio"/> | 23 |

CS501:Midterm Grand Quiz

Question # 30 of 30 (Start time: 11:43:02 PM, 04 July 2020)

Which one of the following is the memory organization of EAGLE processor?

Select the correct option

- | | |
|----------------------------------|-------------------|
| <input type="radio"/> | $2^8 * 8$ bits |
| <input checked="" type="radio"/> | $2^{16} * 8$ bits |
| <input type="radio"/> | $2^{32} * 8$ bits |
| <input type="radio"/> | $2^{64} * 8$ bits |

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