

VIRTUAL UNIVERSITY OF PAKISTAN

A Good Education is a Foundation For a Better Future
CORRECT ANSWER SOLVED BY HADI
EMAIL: hadirajputofficial@gmail.com
WHATSAPP & CALL: 03087122922

(Final Term Past Paper)

(Made by Waqar & Solved By Hadi)

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



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Best of luck!



Question # 4 of 10 (Start time: 03:46:06 PM, 14 July 2021)

Total Marks: 1

The integer value of _____ semaphores can range over an unrestricted integer domain.

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Select the correct option

| | |
|----------------------------------|----------------|
| <input type="radio"/> | Mutex |
| <input type="radio"/> | Binary |
| <input type="radio"/> | Bounded buffer |
| <input checked="" type="radio"/> | Counting |

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Question # 8 of 10 (Start time: 03:50:36 PM, 14 July 2021)

Total Marks: 1

Following is not the classical problem of synchronization.

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Select the correct option

| | |
|----------------------------------|-----------------------------|
| <input type="radio"/> | Reader writer problem |
| <input checked="" type="radio"/> | Counting Semaphore problem |
| <input type="radio"/> | Bounded buffer problem |
| <input type="radio"/> | Dining philosophers problem |

Question # 2 of 10 (Start time: 05:17:28 PM, 09 August 2021)

Total Marks: 1

Segmentation is a memory management scheme that supports _____ ?

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Select the correct option

| | |
|----------------------------------|-----------------------------|
| <input type="radio"/> | Hardware's view of memory |
| <input type="radio"/> | None of the given |
| <input type="radio"/> | System's view of memory |
| <input checked="" type="radio"/> | Programmer's view of memory |

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Question # 5 of 10 (Start time: 03:47:31 PM, 14 July 2021)

Total Marks: 1

_____ is an integer variable accessible through wait and signal which are atomic operations.

Select the correct option

- | | |
|----------------------------------|--------------|
| <input type="radio"/> | Signal |
| <input type="radio"/> | Busy waiting |
| <input type="radio"/> | Mutex |
| <input checked="" type="radio"/> | Semaphore |

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Question # 9 of 10 (Start time: 05:26:02 PM, 09 August 2021)

Total Marks: 1

Intel is basically designed for following Operating Systems except _____.

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Select the correct option

| | |
|----------------------------------|---------|
| <input checked="" type="radio"/> | MULTICS |
| <input type="radio"/> | Linux |
| <input type="radio"/> | Windows |
| <input type="radio"/> | OS/2 |

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Question # 6 of 10 (Start time: 03:48:46 PM, 14 July 2021)

Total Marks: 1

The _____ requires that once a writer is ready, that writer performs its write as soon as possible. In other words, if a writer is waiting to access the object, no new readers may start reading.

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Select the correct option

- | | |
|----------------------------------|--------------------------------|
| <input checked="" type="radio"/> | second readers-writers problem |
| <input type="radio"/> | fourth readers-writers problem |
| <input type="radio"/> | first readers-writers problem |
| <input type="radio"/> | third readers-writers problem |

Question # 2 of 10 (Start time: 03:43:25 PM, 14 July 2021)

Total Marks: 1

Deadlock can be deal with _____ ways.

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Select the correct option

- | | |
|----------------------------------|---|
| <input type="radio"/> | 2 |
| <input type="radio"/> | 4 |
| <input checked="" type="radio"/> | 3 |
| <input type="radio"/> | 6 |

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Question # 4 of 10 (Start time: 05:19:42 PM, 09 August 2021)

Total Marks: 1

The logical address of Intel 80386 is _____.

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Select the correct option

- | | |
|----------------------------------|----------|
| <input type="radio"/> | 36 bits |
| <input checked="" type="radio"/> | 48 bits |
| <input type="radio"/> | 128 bits |
| <input type="radio"/> | 64 bits |

Question # 3 of 10 (Start time: 05:18:55 PM, 09 August 2021)

Total Marks: 1

The _____ protection scheme guards against a process trying to access a page that does not belong to its address space.

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Select the correct option

| | |
|----------------------------------|-----------|
| <input type="radio"/> | Hybrid |
| <input type="radio"/> | Secondary |
| <input type="radio"/> | Tertiary |
| <input checked="" type="radio"/> | Primary |

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Question # 6 of 10 (Start time: 05:21:57 PM, 09 August 2021)

Total Marks: 1

Memory protection in paging is achieved by associating _____ with each page. This bit indicates whether the page is in the process address space or not.

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Select the correct option

| | |
|----------------------------------|-----------------|
| <input type="radio"/> | segment table |
| <input type="radio"/> | paging table |
| <input type="radio"/> | counting bits |
| <input checked="" type="radio"/> | protection bits |

Question # 1 of 10 (Start time: 03:42:18 PM, 14 July 2021)

Total Marks: 1

_____ algorithm is used in Deadlock avoidance.

Select the correct option

- Banker's
- Bakery
- Mutual exclusion
- Safe Sequence

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Question # 7 of 10 (Start time: 05:23:31 PM, 09 August 2021)

Total Marks: 1

Intel 80386 used paged segmentation with _____ level paging.

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Select the correct option

| | |
|----------------------------------|-------|
| <input checked="" type="radio"/> | Two |
| <input type="radio"/> | Three |
| <input type="radio"/> | Four |
| <input type="radio"/> | One |

Question # 5 of 10 (Start time: 05:20:44 PM, 09 August 2021)

Total Marks: 1

.....points to the page table

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Select the correct option

| | |
|----------------------------------|-----------------------------------|
| <input type="radio"/> | Page offset |
| <input type="radio"/> | Translation look-aside buffers |
| <input checked="" type="radio"/> | Page-table base register (PTBR) |
| <input type="radio"/> | Page-table length register (PRLR) |

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- 3) GDB
- 4) GRAND QUIZZES

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