

VIRTUAL UNIVERSITY OF PAKISTAN

A Good Education is a Foundation For a Better Future

CORRECT ANSWER SOLVED BY HADI
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(Final Term Past Paper)

MADE AND SOLVED BY TEAM HADI

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



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Best of luck!



Question # 10 of 10 (Start time: 02:19:51 PM, 13 February 2018)

Total Marks: 1

Cluster number can also be referred as block number.

Select the correct option

<input type="radio"/>	True
<input checked="" type="radio"/>	False

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Click to Save Answer & Move to Next Question

Question # 3 of 10 (Start time: 02:11:58 PM, 13 February 2018)

Total Marks: 1

To store a cluster in FAT 32 _____ is/are needed.

Select the correct option

<input type="radio"/>	Nibble	//
<input type="radio"/>	Byte	//
<input type="radio"/>	2 Bytes	//
<input checked="" type="radio"/>	4 Bytes	//

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Question # 5 of 10 (Start time: 02:50:10 PM, 13 February 2018)

Total Marks: 1

If we want to produce the shrill voice from the speaker phone then we have to load the ___ divisor value at Port ___.

Select the correct option

<input type="radio"/>	high, 0x42	/
<input type="radio"/>	low, 0x42	/
<input checked="" type="radio"/>	high, 0x22	/
<input type="radio"/>	low, 0x22	/

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Question # 6 of 10 (Start time: 02:15:32 PM, 13 February 2018)

Total Marks: 1

The _____ function uses the COM port number to receive a byte from the COM port using BIOS services.

Select the correct option

- | | | |
|----------------------------------|---------------|----|
| <input type="radio"/> | initialize() | // |
| <input type="radio"/> | recievebyte() | // |
| <input type="radio"/> | recieve() | // |
| <input checked="" type="radio"/> | recievechar() | // |
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Question # 7 of 10 (Start time: 02:16:52 PM, 13 February 2018)

Total Marks: 1

In FAT32, lower _____ bits are used.

Select the correct option

<input type="radio"/>	26	CORRECT ANSWER SOLVED BY HADI CELL NO: 03228043306 EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM
<input checked="" type="radio"/>	28	
<input type="radio"/>	30	
<input type="radio"/>	32	

Question # 8 of 10 (Start time: 02:17:57 PM, 13 February 2018)

Total Marks: 1

Int86() has _____ parameters.

Select the correct option

<input type="radio"/>	0	
<input type="radio"/>	1	
<input type="radio"/>	2	
<input checked="" type="radio"/>	3	

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Question # 8 of 10 (Start time: 02:52:22 PM, 13 February 2018)

Total Marks: 1

Int 14H _____ can be used to set the line parameter of the UART or COM port.

Select the correct option

<input checked="" type="radio"/>	Service # 0	CORRECT ANSWER SOLVED BY HADI CELL NO: 03228043306 EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM
<input type="radio"/>	Service # 1	
<input type="radio"/>	Service # 2	
<input type="radio"/>	None of the given options	

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Question # 7 of 10 (Start time: 02:51:57 PM, 13 February 2018)

Total Marks: 1

In counter register bit no. 3 changes its value between 0 and 1 with in ____clock cycles

Select the correct option

<input type="radio"/>	1	
<input type="radio"/>	2	
<input type="radio"/>	4	
<input checked="" type="radio"/>	16	

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Question # 9 of 10 (Start time: 02:53:16 PM, 13 February 2018)

Total Marks: 1

Only _____ ports are important from programming point of view.

Select the correct option

<input checked="" type="radio"/>	70 and 71H	CORRECT ANSWER SOLVED BY HADI CELL NO: 03228043306 EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM	/
<input type="radio"/>	71 and 72H		/
<input type="radio"/>	70 and 72H		/
<input type="radio"/>	72 and 73H		/

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Question # 5 of 10 (Start time: 02:14:10 PM, 13 February 2018)

Total Marks: 1

if the following function,
keep(1, 1000);
is called in the TSR program, it means that _____ No. of bytes are reserved in the memory.

CHECK

Select the correct option

<input checked="" type="radio"/>	8000	CORRECT ANSWER SOLVED BY HADI CELL NO: 03228043306 EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM	//
<input type="radio"/>	16000		//
<input type="radio"/>	32000		//
<input type="radio"/>	64000		//

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Total size of partition table is _____ bytes.

Select the correct option

<input type="radio"/>	64	
<input type="radio"/>	128	
<input type="radio"/>	256	
<input checked="" type="radio"/>	512	

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Question # 5 of 10 (Start time: 02:50:10 PM, 13 February 2018)

Total Marks: 1

If we want to produce the shrill voice from the speaker phone then we have to load the ___ divisor value at Port ___.

Select the correct option

<input type="radio"/>	high, 0x42	/
<input type="radio"/>	low, 0x42	/
<input checked="" type="radio"/>	high, 0x22	/
<input type="radio"/>	low, 0x22	/

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Question # 3 of 10 (Start time: 06:00:24 PM, 20 July 2020)

Total Marks:

The value _____ of partition table signature indicate that the code part contains valid executable code.

Select the correct option

- | | |
|----------------------------------|------|
| <input type="radio"/> | AA22 |
| <input type="radio"/> | AA33 |
| <input type="radio"/> | AA44 |
| <input checked="" type="radio"/> | AA55 |

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Question # 5 of 10 (Start time: 06:03:50 PM, 20 July 2020)

Total Marks: 1

Highest physical capacity of the disk according to the IDE interface is approximately _____.

Select the correct option

- 504MB
- 80GB
- 127GB
- 256GB

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Question # 10 of 10 (Start time: 06:11:05 PM, 20 July 2020)

Total Marks: 1

If 16 bits values are to be transmitted then _____ DMA controllers are required.

Select the correct option

<input checked="" type="radio"/>	Two	//
<input type="radio"/>	Three	//
<input type="radio"/>	Four	//
<input type="radio"/>	Eight	//

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Question # 6 of 10 (Start time: 06:04:48 PM, 20 July 2020)

Total Marks: 1

_____ will specify if the DMA transfer will happen as a single transfer, block transfer or demand transfer.

Select the correct option

<input type="radio"/>	DMA Request register
<input type="radio"/>	DMA Mask register
<input type="radio"/>	DMA Mode register
<input checked="" type="radio"/>	DMA Command register

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Question # 7 of 10 (Start time: 06:06:17 PM, 20 July 2020)

Total Marks: 1

Total size of partition table is _____ bytes.

Select the correct option

<input type="radio"/>	64
<input type="radio"/>	128
<input type="radio"/>	256
<input checked="" type="radio"/>	512

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Question # 8 of 10 (Start time: 06:07:29 PM, 20 July 2020)

Total Marks: 1

A single DMA can transfer _____ bit operand to and from memory in one bus cycle.

Select the correct option

- 4
- 8
- 12
- 16

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