

CS302 QUIZ 2 BY MATH ZONE FOR VU

CS302

Digital Logic Design

Quiz no 2

Solved by : Math zone for Vu

Last date : 27 Nov 2024

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1. Karnaugh Map Array is considered to be wrapped around where all sides are _____ to each other. **Adjacent**
2. Adjacent 1s detector circuit will have active high output for the input **0001**
3. Rules of Boolean algebra can be proved by replacing the literals with Boolean value(s)_____.
0 and 1
4. A SOP expression can be implemented by an _____ combination of gates. AND-OR
5. A _____ is converted into a standard POS by using the rule $AA' = 0$. Select the correct option
Simplified SOP
Simplified POS
Non-standard POS
Non-standard SOP
6. Canonical form is a unique way of representing _____.
7. A 3-variable karnaugh map has
eight cell
8. Which circuit is used in 7-segments display?
BCD code
9. The Quine-McCluskey method has _____ number of steps.
2
10. To determine the seven expressions for each of the seven outputs in 7-segment display, seven ___-variable Karnaugh maps are used.
4
11. The boolean expression $A + BC$ equals
12. The simplified expression is presented in a _____ format.
Function Table
13. How many maximum group can be generated in 4 variable K map?
16
14. K-Map is difficult to manage for _____ or more variables
5
15. Implementing the Adjacent 1s detector circuit directly from the function table based on the SOP form requires _____ gates for the 8 product terms (minterms) with an 8-input OR gate.
8 AND gate
16. Rules of Boolean algebra can be proved by replacing the literals with Boolean value(s)_____.
0 and 1
17. Rules of Boolean algebra can be proved by replacing the literals with Boolean value(s)_____.
0 and 1
18. The Boolean expression $(A + B' + C)$ is

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SUM TERMS

19. Any of the _____ forms of the Karnaugh Map can be used to simplify Boolean expressions.

2

20. The simplified expressions using either of the two K-maps are

_____. IDENTICAL