

VU WITH ABRAR CH
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CS401 Quiz 2 Searching
file 2022

1. There are _____ forms of MOVS instruction.

Two.....confirm

2. The program segment prefix for com files is of _____ size.

256 bytes

3. Which of the following instructions, selects the memory address space?

Movconfirm

4. Which of the following instructions, selects the peripheral address space?

IN.....confirm

5. Which of the following instructions to read and write from the peripheral address space?

IN and OUT.....confirm

6. INT instruction takes a _____ byte(s) argument varying from 0-255.

Single(1).....confirm

7. Which of the following interrupt requests (IRQ) is derived by the keyboard when generates an interrupts when a key is pressed or released.

IRQ 1.....confirm

8. Which of the following interrupt requests (IRQ) is the cascading interrupt connected to the output of the second 8451 in the machine?

IRQ 2....confirm

9. Which of the following interrupt requests (IRQ) is connected to serial port COM 2?

IRQ 3.....confirm

10. Which of the following interrupt requests (IRQ) is connected to serial port COM 1?

IRQ 4....confirm

11. Which of the following interrupt requests (IRQ) is used by the sound card or network card.

IRQ 5.....confirm

12. Which of the following interrupt requests (IRQ) is used by the floppy disk drive ?

IRQ 6....confirm

13. Which of the following interrupt requests (IRQ) is used by the parallel port?

IRQ 7confirm

14. _____register is used for storing the base and limit of GDT.

GDTR.....confirm

15. The first entry of the GDT must always be zero. It is called the_____.

Null descriptor....confirm

16. In intel 8088 there are a total of_____possible interrupt vectors in an interrupt Vector Table.

256....confirm

17. Which register is generally used to specify the service number of an interrupt?

Ax.....confirm from net

18. In the context of video services, if we want to write string then which of the value will be placed in AH?

13h....confirm

19. In 9pin DB 9 connector, which pin is assigned to Signal Ground?

5....confirm

20. In 9pin DB 9 connector, which pin is assigned to Data terminal?

4....confirm

21. In 9pin DB 9 connector, which pin is assigned to transmitted data?

3.....confirm

22. In 9pin DB 9 connector, which pin is assigned to Received data?

2.....confirm

23. In 9pin DB 9 connector, which pin is assigned to Carrier Detect?

1.....confirm

24. In 9pin DB 9 connector, which pin is assigned to Data Set?

6.....confirm

25. In 9pin DB 9 connector, which pin is assigned to request to send (RTS)?

7.....confirm

26. In 9pin DB 9 connector, which pin is assigned to Clear to send (CTS)?

8.....confirm

27. In 9pin DB 9 connector, which pin is assigned to Ring indicator(RI)?

9.....confirm

28. The first parallel port LPT1 has ports designated from _____ to _____.

378 to 37 A.....confirm

29. A parallel port has _____ views

2.....confirm

30. COM2 is connected with

IRQ3....confirm

31. Which of the following service of INT 21 is used to write a string on standard output?

09h.....confirm

32. Which of the following register hold the page number for using the write string service of INT 10?

BH....confirm

33. New devices are allowed to work with the existing operating system with the help of _____.

Device drivers...confirm

34. Which of the following register hold the cursor start and options for using the write string service of INT 10?

CH.....confirm

35. Which of the following register hold the number of lines by which to scroll up for using the write string service of INT 10?

AL....confirm

36. Which of the following register hold the character to display for using the write string service of INT 10?

AL.....confirm

37. Which of the following register hold write mode for using the write string service of INT 10?

AL...confirm

38. The _____ and DPL have the same meaning as in data and code descriptors.

P.....confirm

39. The routine which executes as a result of INT instruction is called _____.

Interrupt service routine.....confirm

40. To declare a character in assembly language, we store its ASCII code in a _____.

Byte....confirm

41. When two devices in a system want to use the same interrupt request (IRQ) line, is referred as IRQ _____.

Conflict....confirm

42. During multitasking _____ is used to get control from the program without letting the program know about it.

IRQ 0...confirm

43. Which of the following is BIOS interrupt providing keyboard services?

INT 0x16.....confirm

44. REPNZ repeats the following instruction while the _____.

ZF is not set....confirm

45. Which of the following is the source register in OUT instruction?

AL or AX....confirm

46. At the end of servicing an interrupt_____signal is used to inform the Programmable interrupt Controller (PIC) about it.

EOI...confirm

47. INT 0x13 service 0x03 is used to_____.

Write disk sector...confirm

48. INT 0x13 service 0x00 is used to_____.

RESET disk system....confirm

49. INT 0x13 service 0x02 is used to_____.

READ sector into memory

50. INT 0x13 service 0x08 is used to_____.

GET drive parameters....confirm

51. INT 0x21 service 0x01 is used to_____.

READ character from standard input, with echo.....confirm

52. INT 0x21 service 0x09 is used to_____.

Write string to standard output...confirm

53. INT 0x21 service 0x0A is used to_____.

Buffered input....confirm

54. In programmable interrupt Controller (PIC) which of the following ports is used to selectively enable or disable interrupts?

21....confirm

55. In multitasking, which of the following interrupts is used scheduler?

INT 08....confirm

56. Which of the following interrupts is used for saving and restoring the registers?

INT 08....confirm

57. Scrolling is the process of moving one or more lines towards the top or bottom of the screen and this is_____.

Cleared.....confirm

58. Software interrupts are called by extended_____call mechanism

Far

59. The root directory of floppy contains_____fixed entries

512....confirm

60. Programmable interrupt controller has_____ports.

Two(20, 21)...confirm

61. The space where all registers of a task are stored is called_____.

Process control block(PCB).....confirm

62. In the context of video services, if we want to scroll the window up which of the following will be the value of AH?

AH=06h....confirm

63. In the context of video services, if we want to scroll the window down which of the following will be the value of AH?

AH=07h....confirm

64. In the context of video services, if we want to set text-mode cursor shape which of the following will be the value of AH?

AH=01h....confirm

65. In the context of video services, if we want to set cursor position which of the following will be the value of AH?

AH=02h....confirm

66. In the context of video services, if we want to write character and attribute at cursor position which of the following will be the value of AH?

AH=09h....confirm

67. In the context of video services, if we want to write character only at cursor position which of the following will be the value of AH?

AH=0Ah....confirm

68. In the context of video services, if we want to write string which of the following will be the value of AH?

AH=13h....confirm

69. INT instruction takes a _____byte(s) argument varying from 0-255.

1....confirm

70. Which of the following is the number of pin(s) outside the processor for generating interrupts by the external hardware?

1.....confirm

71. Each entry of the interrupt vector table is of _____ byte(s).

4....confirm

72. Which of the following is the priority of interrupt request (IRQ) 0?

Highest...confirm

73. The interrupt call loads new values in CS, IP and _____.

FLAgS....confirm

74. REP allows the instruction to be repeated _____ times.

CX ...confirm

75. Which of the following interrupt Requests (IRQ) is derived by a timer device?

IRQ 0confirm

76. An END of interrupt (EOI) signal is sent by the _____

Programmable interrupt controller.....confirm

77. Which of the following INT21 service is used to read character from standard input?

AH= 01h.....confirm

78. Which of the following INT21 service is used to write string to standard output?

AH=09h.....confirm

79. Which of the following INT21 service is used to buffered Input?

AH=0Ah....confirm

80. Which of the following instructions, select the peripheral address space?

IN.....confirm

81. Software interrupts are called by extended_____call mechanism

Far....confirm

82. Which of the following is the interrupt number for debug interrupt?

Int 3.....confirm

83. In intel 8088, the interrupt vector table occupies_____of memory

4 bytes....confirm

84. The register size of 386 architecture is _____.

32 bit.....confirm

85. Which of the following instructions is used to disable all interrupts during the execution of a program?

cli.....confirm

86. In case of COM file, first command line parameter is stored at_____offset of program segment prefix?

0x80....confirm

87. _____is a special type of interrupt that returns to the same instruction instead of the next instruction.

Divide overflow interrupt.....confirm

88. Which of the following is the order of pushing the contents on stack during the execution of INT instruction?

CS, IP and flags.....confirm

89. The output of Programmable interval Timer (PIT) is connected to the _____ line of programmable interrupt controller (PIC).

IRQ 0....confirm

90. Which of the following formulae is used for calculating the segment address of an interrupt service routine "n"?

$N * 4 + 2$confirm

91. The thread registration code initializes the Process Control Block (PCB) and adds it to the linked list. The _____, then gives it a return.

Scheduler.....confirm

92. Interrupts are _____ and _____

Asynchronous , unpredictable.....confirm

93. The execution of the instruction `mov word [ES:160] 0x1230` will print a character on screen at the _____ column of the _____ row.

First, second....confirm

94. Which of the following formulae calculates the desired location on the screen?

$\text{Location} = (\text{rowno} * 80 + \text{column}) * 2$

95. The clear screen operation initializes the whole block of video memory to _____.

0x0720.....confirm

96. In programmable interrupt controller, which of the following ports is the control port?

Port 20....confirm

97. The convention to return a value from a subroutine is to use the_____register.

AX.....confirm

98. The parallel port connector is called _____

DB-25.....confirm

99. Which of the following is the number of pin(s) outside the processor for generating interrupts by the external hardware?

1.....confirm

100. Which of the following is the time interval between two timer ticks?

55ms.....confirm

101. Which of the following IRQs is used for sound card or network card ?

IRQ 5.....confirm

102. _____is used for exporting keyboard services.

INT 11....confirm

103. If the_____flag is set, then after every instruction, a type 1 interrupt will be generated automatically.

Trap....confirm

104. On executing INT 0x21 service 0x3D, if file is successfully opened then

ZF will contain 0....confirm from net

105. On executing INT 0x21 service 0x3D, if file can't be opened then

ZF will contain 1....confirm from net

106. BPB stands for_____.

Bios parameter block.....confirm

107. Which of the following is the destination register in IN instruction?

AL or AX....confirm

108. Interrupts are_____events

Synchronous

109. The correlation process from the interrupt number to the interrupt handler uses a table called_____.

Interrupt vector table....confirm

110. Which of the following services of INT 10 is used to get the video font information?

1130h....confirm

111. In case of COM file, maximum length of parameters passes through command line can be_.

255 bytes

112. Programmable interrupt Controller (PIC) has_____input signal(s) and _____output signal(s).

Eight, one....confirm

113. Which of the following is used to clear the direction flag?

Cld.....confirm

114. Which of the following is used to set direction flag?

Std.....confirm

115. Which of the following is the ACK pin in DB-25 connector?

10.....confirm

116. BIOS video services are broadly classified into _____ categories.

2.....confirm

117. In multitasking environment, which of the following structure is used to maintain the order of active process control blocks (PCB)?

Register.....confirm

118. _____ is a special instruction that loads a general purpose register, and a segment register from two consecutive memory locations.

LES.....confirm

119. Programmable interrupt Controller (PIC) has _____ input signal(s) and _____ output signal(s).

Eight, one....confirm

120. Which of the following interrupts plays the most significant part during single step debugging of a program?

INT 1.....confirm

121. Iret returns on the basis of _____ and _____.

Cs,IP.....confrim

122. Total number of reserved interrupts in Intel 8088 are

255.....confirm

123. The 8088 processor divides interrupts into _____ classes.

Two...confirm

124. As compared to _____, _____ provides more cooked services.

BIOS, DOS.....confirm

125. The IDTR is a _____ bit register similar in structure to the GDTR

48.....confirm

126. INT13-BIOS disk services generally uses which register to return the error flag?

CF.....confirm

127. DOS has a single entry point to access all of its services through _____.

INT 21.....confirm

128. Which of the following flags can be used in mathematical operations?

Carry flag....confirm

129. What is the purpose of the following two instructions? MOV AX, 0XB800 MOV EX,AX

POINTS ES to video base.....confirm

130. Which of the following instruction is used to return from an interrupt service routine?

iret.....confirm

131. Which of the following pins of DB-25 connector are ground?

18-25.....confirm

132. INT13-BIOS disk services generally uses which register to return the error code?

AH.....confirm

133. IBM AT has _____ Programmable interrupt Controllers (PIC)

2....confirm

134. WHICH OF the following interrupts is used to maintain the system time?

INT 8.....confirm

135. Which of the following flags is cleared with the help of stack?

Direction flag.....confirm

136. The register size of 386 architecture is _____.

32 bit.....confirm

137. After the execution of STOSB and STOSW the CX register will be _____ by _____.

Decrement by 1.....confirm

138. Which of the following is the highest priority interrupt?

INT 08.....confirm

139. There is an auto-increment mode when the _____ is _____.

DF, set.....confirm

140. Which of the following services of INT 10 is used for writing the string?

0x13.....confrim

141. Changing and restoring the state of Central Processing Unit (CPU) is called.

Multitasking

142. Which of the following stands for TSR?

Terminate and stay resident....confirm

143. After the execution of STOSB and STOSW, the CX register will be _____ by _____.

Decrementd by 1....confirm

144. While writing graphics pixel on the screen, which registers hold the value for setting cursor position?

CX, DX...confirm from net

145. Which of the following BIOS INT provides serial port services.

INT 14.....confirm

146. Which of the following stands for TSR?

Terminate and stay resident.....confirm

147. PCB stands for _____.

Process control block....confirm

148. The bits are sent one by one in a specially formatted fashion on the_____.

Serial port...confirm

149. In assembly language_____is used to load a character in AL.

Lodsb.....confirm

150. Which of the following services of INT 10 is used to write graphic pixel on the screen?

0x0C.....confirm

151. COM1 is accessible via ports_____while COM2 is accessible via_____.

358-3FF, 258-2FF.....confirm

152. Port_____is used to send an END of interrupt (EOI) signal to the programmable interrupt Controller (PIC) after an interrupt has ended.

0x20.....confirm

153. _____is a special prefix that is used to repeat the block instructions.

REP....confirm

154. COM1 is connected with

IRQ4....confirm

155. BIOS video services are exported via interrupt.

INT 10.....confirm

156. In the context of video services, if we want to set the cursor at top left corner, which of the values will be stored in DH and DL respectively?

00.00.....confirm

157. REPE repeats a string instruction while the_____ flag is set.

Zero...confirm

158. Device drivers can be divided into _____ major categories.

2.....confirm

159. A 32-bit address register can access up to ____ of memory

4GB....confirm

160. _____ is used in debugging along with the trap flag.

INT 1.....confirm

161. Push and pop operations always operate on_____.

Words.....confirm

162. Which of the following instructions is used to read a character from the keyboard port?

In al, 0x60.....confirm

163. REP with MOVS utilizes the _____ power of a processor to do scrolling in minimum time.

Full...confirm

164. The programmable interval timer (PIT) has input frequency of_____.

1.19318 MHZ.....confirm

Question # 1

Which of the following IRQs is connected to serial port COM 1?

IQR 4 Correct

Question # 2

In interrupt masking, We use _____ to get control form the program without letting the program know about it.

IQR 0 Correct

Question # 3

In assembly language, ISR stands for.

Interrupt service routine Correct

Question # 4

In multitasking, which of the following interrupts in used as scheduler during context switching ?

INT 8 Not Sure

Question # 5

Which of the following interrupts is used for maintaining the system

INT 8 Correct

Question # 6

Each entry of the interrupt vector table is of _____ bytes.

4 Correct

Question # 7

Each entry of the interrupt vector table is of _____ bytes.

4 Correct

Question # 8

Which of the following is the pair of registers used to access memory in string instructions?

DS and SI Correct

Question9

The maximum length of DOS command line parameters is

512 bits Correct

Question # 10

TSR stands for:

Transmits and Save Resources Correct